

## Advanced Test Equipment Rentals www.atecorp.com 800-404-ATEC (2832)

#### NOTE

This manual documents the 5450A Resistance Calibrator and its assemblies at the revision levels shown in Section 8. If your instrument contains assemblies with different revision letters, it will be necessary for you to either update or backdate this manual. Refer to the supplemental change/errata sheet for newer assemblies, or to the backdating sheet in Section 8 for older assemblies.

# **5450A**Resistance Calibrator

Instruction Manual



#### WARRANTY

Notwithstanding any provision of any agreement the following warranty is exclusive:

The JOHN FLUKE MFG. CO., INC., warrants each instrument it manufactures to be free from defects in material and workmanship under normal use and service for the period of 1-year from date of purchase. This warranty extends only to the original purchaser. This warranty shall not apply to fuses, disposable batteries (rechargeable type batteries are warranted for 90-days), or any product or parts which have been subject to misuse, neglect, accident, or abnormal conditions of operations.

In the event of failure of a product covered by this warranty, John Fluke Mfg. Co., Inc., will repair and calibrate an instrument returned to an authorized Service Facility within 1 year of the original purchase; provided the warrantor's examination discloses to its satisfaction that the product was defective. The warrantor may, at its option, replace the product in lieu of repair. With regard to any instrument returned within 1 year of the original purchase, said repairs or replacement will be made without charge. If the failure has been caused by misuse, neglect, accident, or abnormal conditions of operations, repairs will be billed at a nominal cost. In such case, an estimate will be submitted before work is started, if requested.

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- 1. Notify the JOHN FLUKE MFG. CO., INC., or nearest Service facility, giving full details of the difficulty, and include the model number, type number, and serial number. On receipt of this information, service data, or shipping instructions will be forwarded to you.
- 2. On receipt of the shipping instructions, forward the instrument, transportation prepaid. Repairs will be made at the Service Facility and the instrument returned, transportation prepaid.

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#### CLAIM FOR DAMAGE IN SHIPMENT TO ORIGINAL PURCHASER

The instrument should be thoroughly inspected immediately upon original delivery to purchaser. All material in the container should be checked against the enclosed packing list. The manufacturer will not be responsible for shortages against the packing sheet unless notified immediately. If the instrument is damaged in any way, a claim should be filed with the carrier immediately. (To obtain a quotation to repair shipment damage, contact the nearest Fluke Technical Center.) Final claim and negotiations with the carrier must be completed by the customer.

The JOHN FLUKE MFG. CO., INC, will be happy to answer all applications or use questions, which will enhance your use of this instrument. Please address your requests or correspondence to: JOHN FLUKE MFG. CO., INC., P.O. BOX C9090, EVERETT, WASHINGTON 98206, ATTN: Sales Dept. For European Customers: Fluke (Holland) B.V., P.O. Box 5053, 5004 EB, Tilburg, The Netherlands.

\*For European customers, Air Freight prepaid.

John Fluke Mfg. Co., Inc., P.O. Box C9090, Everett, Washington 98206

### **Table of Contents**

SECTION		TITLE	PAGE
1	INTRO	DDUCTION AND SPECIFICATIONS	1-1
	1-1.	INTRODUCTION	1-1
	1-2.	SPECIFICATIONS	1-1
2	OPER	ATING INSTRUCTIONS	2-1
	2-1.	INTRODUCTION	2-1
	2-2.	SHIPPING INFORMATION	2-1
	2-3.	INPUT LINE POWER	2-1
	2-4.	INSTALLATION	2-1
	2-5.	IEEE-488 CONNECTION	
	2-6.	CONTROLS AND INDICATORS	2-2
	2-7.	OPERATING NOTES	
	2-8.	Rear Panel Settings	
	2-11.	Connecting to a Unit Under Test	
	2-18.	Powering Up the 5450A	
	2-19.	Selecting an Output	
	2-20.	Computing Error	
	2-21.	2-Wire Enhancement	
	2-22.	REMOTE OPERATION	
	2-23.	Introduction	
	2-24.	Capabilities	
	2-25.	Device Clear	
	2-26.	Local/Remote Operation	
	2-27.	Command Separators and Terminators	
	2-28.	Remote Error Handling	
	2-29.	Command Set	
	2-30.	Programming Examples	. 2-14
3	THEO	RY OF OPERATION	
	3-1.	INTRODUCTION	. 3-1
	3-2.	FUNCTIONAL DESCRIPTION	
	3-3.	CIRCUIT DESCRIPTION	
	3-4.	Power Supply Circuit	
	3-8.	Display Circuit	
	3-11.	Digital Circuit	
	3-21.	Analog Circuit	. 3-5

#### **TABLE OF CONTENTS,** continued

SECTION	TITLE	PAGE
4	MAINTENANCE	4-1
	4-1. INTRODUCTION	4-1
	4-2. SERVICE INFORMATION	4-1
	4-3. GENERAL MAINTENANCE	4-1
	4-4. Setting Line Voltage	4-1
	4-5. AC Line Power Fuse Replacement	
	4-6. Cleaning	4-4
	4-7. Handling Precautions	
	4-10. Replacement of Selected Components	4-5
	4-11. Disassembly and Reassembly	
	4-22. PERFORMANCE TEST/CALIBRATION PROCEDURE	4-7
	4-23. Introduction	
	4-24. Calibration Philosophy	
	4-28. Calibration Procedure	
	4-37. Alternatre Calibration Procedures	
	4-38. TROUBLESHOOTING	
	4-39. Introduction	
	4-40. Power Supply Isolation	
	4-41. Analog Assembly Isolation	
	4-42. Display Assembly Isolation	
	4-43. Digital Assembly Isolation	4-23
5	LIST OF REPLACEABLE PARTS	5-1
	TABLE OF CONTENTS	5-1
	5-1. INTRODUCTION	5-2
	5-2. HOW TO OBTAIN PARTS	
6	OPTIONS AND ACCESSORIES	6-1
7	REFERENCE INFORMATION	7-1
8	MANUAL CHANGE INFORMATION	8-1
9	SCHEMATIC DIAGRAMS	9-1
	TABLE OF CONTENTS	9-1

## **List of Tables**

TABLE	TITLE				
1-1.	Specifications	1-2			
2-1.	Rack Mounting Accessories	2-1			
2-2.	IEEE-488 Cable Accessories	2-2			
2-3.	Front Panel Controls, Indicators, and Connectors	2-2			
2-4	Rear Panel Controls, Indicators, and Connectors	2-5			
2-5.	IEEE-488 Addresses	2-7			
2-6.	Fundamental IEEE-488 Commands	2-11			
2-7.	Extended IEEE-488 Commands	2-13			
3-1.	Relay Truth Table	3-7			
4-1.	Required Test Equipment	4-2			
4-2.	Required Cables	4-3			
4-3.	Calibration Worksheet	4-10			
4-4.	Calibration Data for Transfer of Standards	4-18			
4-5.	Nulling Procedure	4-18			
4-6.	Ratio Calibration Data	4-19			
8-1.	Manual Status and Backdating Information	8-1			

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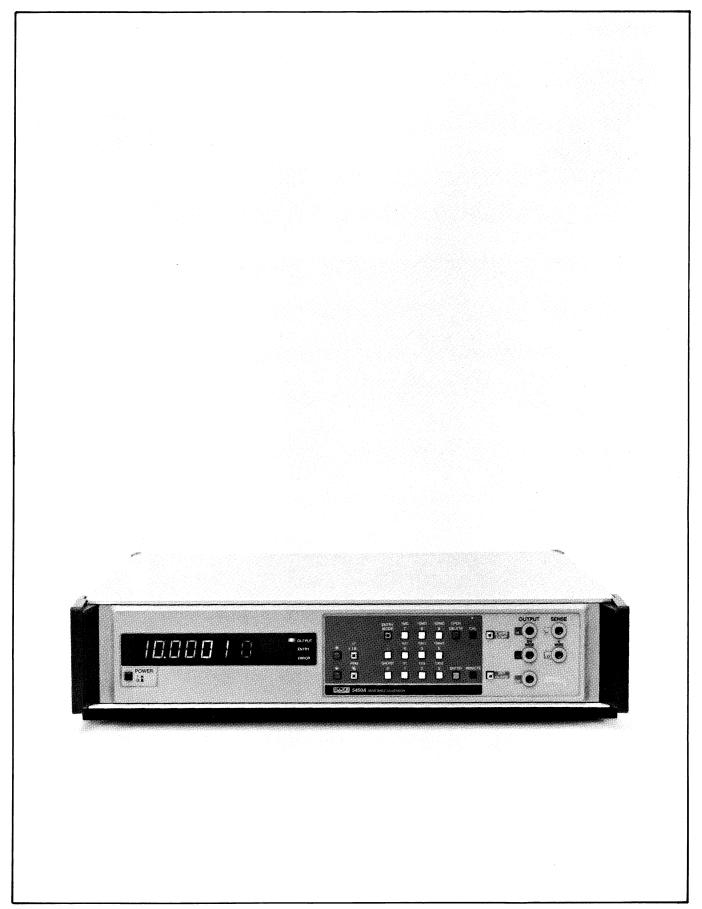
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## **List of Illustrations**

FIGURE	TITLE	PAGE
Frontispiece	5450A Resistance Calibrator	vi
1-1.	Outline Drawing	1-7
2-1.	Front Panel Controls, Indicators, and Connectors	2-2
2-2.	Rear Panel Controls, Indicators, and Connectors	2-5
3-1.	Functional Block Diagram	3-2
4-1.	Line Voltage Selection Switches	
4-2.	Transfer of Standard Resistance	4-2
4-3.	Internal Ratio Divider	4-3
4-4.	Ratio Calibration	4-4
4-5.	Connections for Transfer of Standard Resistance	4-12
4-6.	Connections for Ratio Calibration	4-14
4-7.	Connections for Calibrating the Short	4-16
6-1.	Rack Adapter Installation	
6.2	Pack Slide Installation	6.2



5450A Resistance Calibrator

# Section 1 Introduction & Specifications

#### 1-1. INTRODUCTION

The Fluke 5450A Resistance Calibrator is designed to facilitate manual or automated calibration of the resistance measuring circuitry of high-quality digital multimeters (DMMs). The 5450A provides cardinal point resistances in the range of  $1\Omega$  to 100 M $\Omega$ . Resistances may be selected either at decade points (e.g.,  $1\Omega$ ,  $10\Omega$ ,  $100\Omega$ , etc.) or at x1.9 points (e.g.,  $1.9\Omega$ ,  $19\Omega$ ,  $190\Omega$ , etc.).

The 5450A display indicates the value of the selected resistance and can also indicate the error of the instrument being calibrated (the unit under test, or UUT), expressed in percent or parts-per-million (ppm).

The 5450A may be controlled from the front panel or by remote control via the IEEE-488 interface bus. All functions available on the front panel are also available over the IEEE-488 bus.

The UUT may be connected directly to the 5450A front panel terminals using banana plugs,

or to five-way low-thermal binding posts on the rear panel.

The 5450A is calibrated with its covers installed and may be calibrated remotely via the IEEE-488 bus. Calibration constants are stored in electrically alterable read-only memory (EAROM).

Accessories available for the 5450A include a rack mounting kit, rack mounting ears, and IEEE-488 interface bus cables.

#### 1-2. SPECIFICATIONS

Specifications for the model 5450A are given in Table 1-1. An outline drawing is shown in Figure 1-1.

The accuracy specifications assume that the ambient temperature is held within the limits specified and that the relative humidity is held at or below 80% unless indicated. A minimum 2-hour warm-up time is also assumed.

#### Table 1-1. Specifications

#### **FOUR-WIRE OPERATION**

The four-wire specifications apply to the four-wire measurement configuration in which the OUTPUT terminals are used for test current and the SENSE terminals are used for sensing voltage.

	FOUR-WIRE ACCURACY SPECIFICATIONS <sup>1</sup>							
NOMINAL	NOMINAL <sup>2</sup>	1	NORMAL I RANGE⁴ IL < I < I∪		ABSOLUTE UNCERTAINTY <sup>2</sup> (±ppm unless indicated)			
VALUE	TOLERANCE (±ppm)	Lower Limit	Upper Limit	23°C ±1°	C, 70% RH	23°C ±5°C	, 70% RH	
	(± <b>pp</b> iii)	IL	lu	24 Hours	90 Days	90 Days	1 Year	
SHORT		10 mA	500 mA	.1 mΩ	.1 mΩ	.1 mΩ	.1 mΩ	
1Ω	1000	10 mA	100 mA	50	62	75	110	
1.9Ω	1000	10 mA	75 mA	4C	52	65	100	
10Ω	500	10 mA	25 mA	18	20.5	25	33	
19Ω	500	10 mA	25 mA	16	18.5	23	31	
100Ω	175	10 mA	15 mA	7.5	9	11	16	
190Ω	150	10 mA	15 mA	7	8.5	10.5	15.5	
1 kΩ	100	700 μA	2.5 mA	5.5	7	8.5	13.5	
1.9 kΩ	100	500 μA	2.5 mA	5	6.5	8	13	
10 kΩ	60	50 μA	1 mA	5	6.5	8	13	
19 kΩ	50	50 μA	1 mA	4.5	6	7.5	12.5	
100 kΩ	50	5 μA	250 μA	6	7.5	9	14	
190 kΩ	50	5 μA	250 μA	5.5	7	8.5	13.5	
1 ΜΩ	50	5 μA	50 μA	7.5	10	11.5	19	
1.9 ΜΩ	50	5 μA	25 μA	7.7	10	11.5	19	
10 ΜΩ	100	.5 μΑ	5 μA	16	24	26	50	
19 ΜΩ	100	.25 μA	2.5 μA	20	28	30	56	
100 ΜΩ	500	.05 μA	.5 μΑ	65	90	120	200	

#### Notes:

- 1. The above table assumes the 5450A was calibrated according to the procedure specified in Section 4-28.
- 2. The actual value of each resistance is determined to the accuracy given under ABSOLUTE UNCERTAINTY. These values can differ from the nominal values by the amount given under NOMINAL TOLERANCE.
- 3. Above 70% relative humidity the accuracy degrades for values above 190 k $\Omega$ . For these values use the following table for ABSOLUTE UNCERTAINTY to 80% RH:

Nominal Value	90 Days 23°C ±5°C	1 Year 23°C ±5°C
1 ΜΩ	12.5 ppm	20 ppm
1.9 ΜΩ	13.5 ppm	21 ppm
10 ΜΩ	36 ppm	60 ppm
19 ΜΩ	50 ppm	76 ppm
100 ΜΩ	140 ppm	220 ppm

4. The accuracy degrades for current outside the limits given under NORMAL I RANGE, which begins at the given lower limit (IL) and ends at the given upper limit (IU). For a description and derating factors, see FOUR-WIRE CURRENT DERATING FACTORS, below.

Table 1-1. Specifications (cont)

	OTHER FOUR-WIRE SPECIFICATIONS							
NOMINAL		STABILITY <sup>1,2</sup> (±ppm)			MAXIMUM CURRENT <sup>4</sup>	MAXIMUM VOLTAGE <sup>4</sup>	TEMPERATURE COEFFICIENT <sup>5</sup> 0°C-18°C	
VALUE	23°C ±1°	23°C ±1°C, 70% RH 23°C ±5°		C. 70% BH <sup>3</sup> (IMAX)	(IMAX) (direct	(V <sub>MAX</sub> ) (direct	& 28°C-50°C (±ppm/°C)	
	24 Hours	90 Days	90 Days	1 Year	current)	- 1	voltage)	(add to accuracy spec)
SHORT					500 mA	0.25V		
1Ω	30	42	55	90	400 mA	0.4V	6	
1.9Ω	20	32	45	80	300 mA	0.57V	6	
10Ω	7	9.5	14	22	100 mA	1V	2.5	
19Ω	5	7.5	12	20	100 mA	1.9V	2.5	
100Ω	2	3.5	5.5	10.5	25 mA	2.5V	1.5	
190Ω	1.5	3	5	10	25 mA	4.75V	1.5	
1 kΩ	2	3.5	5	10	10 mA	10V	1.5	
1.9 kΩ	1.5	3	4.5	9.5	10 mA	19V	1.5	
10 kΩ	2	3.5	5	10	2.5 mA	25V	1.5	
19 kΩ	1.5	3	4.5	9.5	2.5 mA	47.5V	1.5	
100 kΩ	2	3.5	5	10	500 μA	50V	1.5	
190 kΩ	1.5	3	4.5	9.5	250 μA	50V	1.5	
1 ΜΩ	2.5	5	6.5	14	50 μA	50V	1.5	
1.9 ΜΩ	2.5	5	6.5	14	25 μA	50V	1.5	
10 ΜΩ	10	18	20	44	5 <i>μ</i> Α∽	50V	3	
19 ΜΩ	14	22	24	50	2.5 μA	50V	4	
100 ΜΩ	40	65	95	175	.5 <i>μ</i> Α	50V	10	

#### Notes:

- 1. This table can be used to determine the absolute uncertainty for four-wire operation when the 5450A is calibrated by means other than the procedure presented in Section 4-28. For each resistance value, add the uncertainty of the calibration technique used to the stability figure given above for the desired conditions.
- 2. The accuracy degrades for current outside the limits given under NORMAL I RANGE in the FOUR-WIRE ACCURACY SPECIFICATIONS, above. For a description and derating factors, see FOUR-WIRE DERATING FACTORS, below.
- 3. Above 70% relative humidity the accuracy degrades for values above 190 k $\Omega$ . For these values use the following table for STABILITY to 80% RH:

Nominal Value	90 Days 23°C ±5°C	1 Year 23°C ±5°C
1 ΜΩ	7.5 ppm	15 ppm
1.9 ΜΩ	8.5 ppm	16 ppm
10 ΜΩ	30 ppm	54 ppm
19 ΜΩ	44 ppm	70 ppm
100 ΜΩ	115 ppm	195 ppm

- 4. The maximum current and voltage given are the maximum that will not permanently change any resistance values. Operation above these limits may require recalibrating the instrument afterwards.
- 5. The temperature coefficient applies to both the ABSOLUTE UNCERTAINTY and the STABILITY, above.

Table 1-1. Specifications (cont)

FOUR-WIRE CURRENT DERATING FACTORS			
NOMINAL		UNDER I IZATION K	
VALUE	l <l∟ Note 1</l∟ 	lu <l<lmax Note 2</l<lmax 	
SHORT	5x 10 <sup>-1</sup>	None	
1Ω	2x10²	1x10⁻⁴	
1.9Ω	1×10²	2x10⁻⁴	
10Ω	5x10¹	3x10 <sup>-</sup>	
19Ω	2.6x10 <sup>1</sup>	3x10⁻⁴	
100Ω	5×10°	2x10 <sup>-3</sup>	
190Ω	2.6x10°	2x10 <sup>-3</sup>	
1 kΩ	5x10 <sup>-1</sup>	2x10 <sup>-2</sup>	
1.9 kΩ	2.6x10 <sup>-1</sup>	2x10 <sup>-2</sup>	
10 kΩ	5x10¹	2x10 <sup>-7</sup>	
19 kΩ	2.6x10 <sup>1</sup>	2x10 <sup>-7</sup>	
100 kΩ	3x10°	2x10 <sup>-6</sup>	
190 kΩ	1.6x10°	Iu = Imax	
1 ΜΩ	5x10°	IU = IMAX	
1.9 ΜΩ	2.6x10°	IU = IMAX	
10 ΜΩ	1x10°	Iu = Imax	
19 ΜΩ	5.3x10 <sup>-1</sup>	Iu = Imax	
100 MΩ	1x10 <sup>-1</sup>	Iu = Imax	

#### Notes:

1. For I < IL, errors occur due to thermally generated voltages within the 5450A. Use the following equation to determine the error in ppm (or milliohms for the SHORT), and add this to the ABSOLUTE UNCERTAINTY or STABILITY, above.

ppm (or milliohms for SHORT) = Kx (IL - I) / (IL x I)

where: K is the constant from the above table;

I and I<sub>L</sub> are expressed in mA for SHORT to 1.9 kΩ;

I and I<sub>L</sub> are expressed in  $\mu$ A for 10 k $\Omega$  to 100 M $\Omega$ .

2. For  $Iu < I < I_{MAX}$ , error occurs due to self-heating of the resistors in the 5450A. Use the following equation to determine the error (in ppm), and add this to the ABSOLUTE ACCURACY or STABILITY, above.

$$ppm = K x (I^2 - I \cup^2)$$

where: K is the constant from the above table;

I and Iu are expressed in mA for SHORT to 1.9 k $\Omega$ ;

I and Iu are expressed in  $\mu A$  for 10 k $\Omega$  to 100 M $\Omega$ .

#### Table 1-1. Specifications (cont)

#### TWO-WIRE OPERATION

The two-wire specifications apply to the OUTPUT terminals and assume the use of the 2 WIRE COMPensation feature. The 2 WIRE COMPensation feature compensates for the connection resistance, which may include user cabling (such as test leads or system cabling). The compensation value can be calibrated at the time the instrument is calibrated, or for better accuracy, every 24 hours.

To find the two-wire accuracy specification, take the accuracy values from the TWO-WIRE SPECIFICATIONS table (below), add any error specified in notes 2 and 4 (below), convert the result to ppm, and add the result to the values given in the FOUR-WIRE ACCURACY SPECIFICATIONS and OTHER FOUR-WIRE SPECIFICATIONS tables, above. Above 190 k $\Omega$  the two-wire specifications are the same as the four-wire specifications.

	TWO-WIRE SPECIFICATIONS 1, 2, 3, 4					
NOMINAL VALUE	MINIMUM CURRENT	ADD TO 4-WIRE ACCURACY SPECIFICATION 23°C ±5°C 90 Days (±mΩ)	ADD TO 4-WIRE ACCURACY SPECIFICATION TOC ±5°C (18°C-28°C) 24 Hours (±mΩ)			
1Ω	1 mA	25 mΩ	12 mΩ			
1.9Ω	1 mA	25 mΩ	12 mΩ			
10Ω	1 mA	20 mΩ	10 mΩ			
19Ω	1 mA	20 mΩ	10 mΩ			
100Ω	1 mA	25 mΩ	12 mΩ			
190Ω	1 mA	25 mΩ	12 mΩ			
1 kΩ	.1 mA	100 mΩ	35 mΩ			
1.9 kΩ	.1 mA	100 mΩ	35 mΩ			
1.9 kΩ	10 μA	200 mΩ	125 mΩ			
19 kΩ	10 μA	200 mΩ	125 mΩ			
19 kΩ 100 kΩ	1 μA	1000 mΩ	1000 mΩ			
190 kΩ	1 μA	1000 mΩ	1000 mΩ			

#### Notes:

- Specifications apply for both front and rear panel terminals regardless of which terminals were used during calibration.
- 2. If user cabling is included in the 2-wire compensation value, add to the specification the variations in cabling resistance for the appropriate time period and temperature.
- 3. The 2-wire compensation value may be calibrated every 24 hours as described in Section 2 (see "2-Wire Enhancement"). The ambient temperature at which this calibration is performed (called temperature of calibration, or TOC) may be between 18°C and 28°C. The 24-hour specification applies for the range TOC ±5°C, but does not apply above 28°C or below 18°C.
- 4. Add to the 24-hour specification the error (in milliohms) of the DMM used when measuring the short.

Table 1-1. Specifications (cont)

	IEEE-488 INTERFACE FUNCTION SUBSETS			
SH1	Source handshake	Complete capability		
AH1	Acceptor handshake	Complete capability		
Т8	Talker	Basic talker, unaddress if MLA		
L4	Listener	Basic listener, unaddress if MTA		
SRO	Service request	No capability		
RL1	Remote/local	Complete capability		
PPO	Parallel poll	No capability		
DC1	Device clear	Complete capability		
DTO	Device trigger	No capability		
CO	Controller	No capability		
E2	Electrical interface	Three-state drivers		

GEN	ERAL SPECIFICATIONS
Interface	IEEE Standard 488-1978 (IEEE-488)
Warm-up time	2 hours from environment of 18°C to 28°C 4 hours from environment of 0°C to 40°C
Maximum Voltages:	
Any terminal to ground:	See VMAX under OTHER FOUR-WIRE SPECIFICATIONS
Altitude	0 to 10,000 feet (operating) 0 to 40,000 feet (storage)
Vibration	3.1g maximum
Power Requirements	100, 120, 220, or 240V ac $\pm$ 10% 50 to 60 Hz $\pm$ 5%, $<$ 50W
Weight	9.3 kg (20.4 lbs) 15.9 kg (35 lbs) shipping weight
Dimensions	See Figure 1-1 8.89 cm H x 55.37 cm L x 43.18 cm W (3.5 in H x 21.8 in L x 17.00 in W)
	Shipping dimensions: 25.4 cm H x 67.3 cm L x 53.8 cm W (10 in H x 26-1/2 in L x 21-3/16 in W)
Protection Class	Class 1 as defined in safety standards IEC 348 and ANSI C39.5

Table 1-1. Specifications (cont)

TE	MPERATURE AND HUMID	ITY
CONDITION	TEMPERATURE	RELATIVE HUMIDITY
Storage	-40°C to 0°C	Not controlled
-	0°C to 75°C	≤95% RH
Operating	0°C to 40°C	≤80% RH
,	40°C to 50°C	≤50% RH

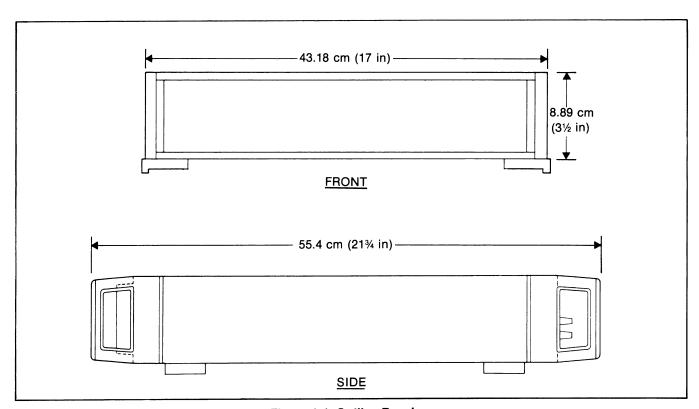


Figure 1-1. Outline Drawing

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# Section 2 Operating Instructions

#### 2-1. INTRODUCTION

This section contains installation and operating instructions for the 5450A Resistance Calibrator. It is recommended this section be read and understood before any attempt is made to operate the instrument. Should any difficulties arise during operation, contact the nearest John Fluke sales representative. A list of sales offices is given in Section 7.

#### 2-2. SHIPPING INFORMATION

The 5450A is packaged and shipped in a foam-packed container. Upon original delivery of the instrument, the purchaser should thoroughly inspect the product and check all materials in the container against the packing list. The manufacturer will not be responsible for shortages against the packing list unless notified immediately upon receipt. If the instrument is damaged in any way, a claim should be filed with the carrier immediately. (To obtain a quotation for repair of shipment damage, contact the nearest Fluke Technical Service Center. A list of Service Centers in given in Section 7.)

#### 2-3. INPUT LINE POWER

The 5450A can be internally configured to operate from line voltages of 100, 120, 220, or 240V ac  $\pm 10\%$  at frequencies of 50 to 60 Hz  $\pm 5\%$ . The factory-selected line voltage setting is marked on the rear panel immediately to the left of the fuse. If the 5450A is set to a voltage that is incorrect for the application, refer to the instructions in Section 4 for changing the setting.

#### 2-4. INSTALLATION

The 5450A is designed to be used either on the work bench or mounted in standard deep 24-inch equipment racks. For bench-top operation, the 5450A is equipped with tilt bails and non-marring feet. The tilt bails (located below

the front panel) can be used to tilt the instrument to a convenient operating position. Rack mounting accessories available for the 5450A are listed in Table 2-1. The rack mounting accessories include installation instructions.

**Table 2-1. Rack Mounting Accessories** 

NAME	MODEL NUMBER
3½" Rack Adapter with 22"-24" slides 3½" Rack Adapter only	Y8598 Y8599

#### 2-5. IEEE-488 CONNECTION

The IEEE-488 connector on the rear panel allows the 5450A to be connected to an instrument controller (such as the Fluke 1722A) for remote-controlled operation. The IEEE-488 connector mates with standard 24-conductor IEEE-488 cables. The cables may be stacked at the connector.

IEEE-488 connections are confined to the following limits:

- 1. No more than 15 devices may be connected in a single IEEE-488 bus system.
- 2. The total length of cable used in one IEEE-488 bus system must not exceed 20 meters.
- 3. The total length of cable must also not exceed 2 meters times the number of devices in the system.

Combinations of 1-, 2-, and 4-meter IEEE-488 cables can be used, to meet the cable length restrictions. For example, when a system consists only of the 5450A and a controller, the number of devices is 2. Multiplying by 2 meters, the maximum length of the cable that can be used is 4 meters (2 devices  $\times$  2 meters/device = 4 meters).

Therefore, a 1-, 2-, or 4-meter cable could be used to connect the 5450A to the controller. When connecting more than 10 devices, the length of cables used should not total more than 20 meters. Available cables are listed in Table 2-2.

Table 2-2. IEEE-488 Cable Accessories

MODEL NUMBER NAME	
Y8021	IEEE Standard Cable, 1 Meter
Y8022	IEEE Standard Cable, 2 Meters
Y8023	IEEE Standard Cable, 4 Meters

#### 2-6. CONTROLS AND INDICATORS

Front panel controls, indicators, and connectors are shown in Figure 2-1 and described in Table 2-3. Rear panel controls, indicators, and connectors are shown in Figure 2-2 and described in Table 2-4.

#### 2-7. OPERATING NOTES

#### 2-8. Rear Panel Settings

The rear panel switches provide for setting the IEEE-488 bus address, connecting the IEEE-488 cable shielding, enabling the calibration mode, and configuring the instrument for the type of calibration to be performed.

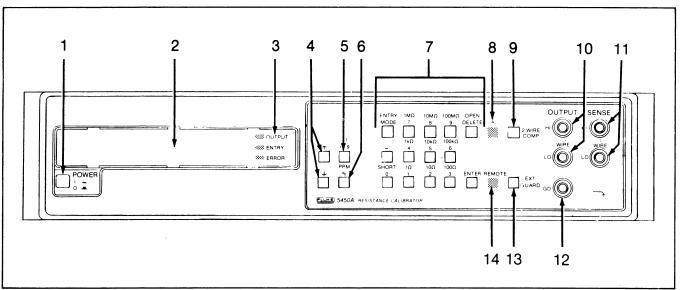


Figure 2-1. Front Panel Controls, Indicators, and Connectors

Table 2-3. Front Panel Controls, Indicators, and Connectors

ITEM NO.	NAME	FUNCTION
1	POWER switch	Turns the 5450A on (1) and off (0).
2	Display	Eight alphanumeric 14-segment LEDs.
		In the OUTPUT mode (OUTPUT light on), the display indicates the output resistance that the 5450A is actually sourcing. The last character is blank (for ohms), K (for kilohms), or M (for megohms).  In the ENTRY mode (ENTRY light on), the display may indicate K (for kilohms), M (for megohms), or blank (for ohms), prompting the operator to enter the UUT reading via the 5450A keyboard, and indicating the units the 5450A expects the reading to be in. The
		In the ERROR mode (ERROR light on), the display indicates the error of the UUT in parts-per-million (PPM) or percent (PCT).

Table 2-3. Front Panel Controls, Indicators, and Connectors (cont)

ITEM NO.	NAME	Controls, Indicators, and Connectors (cont)  FUNCTION
3	Mode annunciators	These indicate whether the 5450A is in the OUTPUT, ENTRY, or ERROR mode.
	OUTPUT light	When on, the OUTPUT mode is selected.
	ENTRY light	When on, the ENTRY mode is selected.
	ERROR light	When on, the ERROR mode is selected.
4	↑↓(up and down arrows)	Used in the OUTPUT mode. Pressing the up arrow selects the next higher decade of output resistance. If the output is presently 19 M $\Omega$ or 100 M $\Omega$ , pressing the up arrow selects an OPEN. Pressing the down arrow selects the next lower decade of output. If the output is presently 1 $\Omega$ or 1.9 $\Omega$ , pressing the down arrow selects a SHORT.
5	x1/x1.9 switch	Used in the OUTPUT mode. Toggles the output resistance between decade values (e.g., 10 k $\Omega$ , 100 k $\Omega$ , etc.) and x1.9 values (e.g., 19 k $\Omega$ , 190 k $\Omega$ , etc.). When the x1/x1.9 switch is lit, x1.9 values are selected.
6	PPM/% switch	Used in the ERROR mode. Toggles the displayed UUT error between percent (PCT) and parts-per-million (PPM). If the error is too large to display in PPM, the display indicatesPPM. When the PPM/% switch is lit, PCT is selected.
7	Main Keyboard	14 pushbutton switches. White functions are enabled in the OUTPUT and ERROR modes; dark orange functions are enabled in the ENTRY mode.
	OUTPUT mode	In the OUTPUT mode, pressing a switch selects the nominal resistor value shown in white. If the x1.9 switch is lit, the output is the nominal value times 1.9. Pressing the ENTRY MODE switch selects the ENTRY mode.
	ENTRY mode	In the ENTRY mode, the keyboard is shifted for entering the UUT reading. The DELETE switch deletes displayed characters. After the UUT reading is keyed in, pressing the ENTER switch selects the ERROR mode.
	ERROR mode	In the ERROR mode, the 5450A computes and displays the UUT error. Pressing any switch except ENTRY MODE, EXT GUARD, or PPM/% selects the OUTPUT mode again.
8	CAL LED	The CALibration LED indicates that the rear panel CALIBRATION switch is enabled.
		CAUTION
		Do not attempt to operate the 5450A while the CAL LED is lit unless you are calibrating the instrument, as the internal calibration constants may be inadvertently modified.

Table 2-4. Front Panel Controls, Indicators, and Connectors (cont)

ITEM NO.	NAME	FUNCTION
9	2 WIRE COMP switch	The 2 WIRE COMPensation switch is used to compensate the characterized value of the internally stored resistance to accommodate UUTs that have a 2-wire resistance measuring circuit (i.e., that do not have separate source terminals). When the 2 WIRE COMP switch is lit, 2-wire compensation is in effect.
		NOTE
		If set improperly, the 2 WIRE COMP switch can cause the 5450A display values to be incorrect.
10	OUTPUT terminals	Connects the UUT to the 5450A's internal resistors in either 2-wire or 4-wire configurations. In the 4-wire configuration, the UUT source terminals are connected to the 5450A OUTPUT terminals. The front panel OUTPUT terminals are wired in parallel with the rear panel OUTPUT terminals.
11	SENSE terminals	Connects the sense terminals of the UUT to the 5450A's internal resistors when the UUT is in the 4-wire configuration. The front panel SENSE terminals are wired in parallel to the rear panel SENSE terminals.
12	GD (guard) terminal	Connects to the internal guard chassis. The GD terminal is connected to the OUTPUT LO terminal via a $100\Omega$ resistor to reduce noise and leakage. If the EXT GUARD switch LED is on, the GD terminal is not tied to OUTPUT LO. The GD terminal is wired in parallel to the rear panel GUARD terminal.
13	EXT GUARD switch	The EXTernal GUARD switch toggles the 5450A between internal guard (LED off) and external guard (LED on). In internal guard, the GD and OUTPUT LO terminals are connected via a $100\Omega$ resistor. In external guard, these terminals are not tied together. This switch is used for special configurations such as occur when calibrating the 5450A.
14	REMOTE LED	The REMOTE LED indicates that the 5450A has been addressed by the IEEE-488 bus. The instrument may be returned to local mode by pressing any front panel switch unless LOCAL LOCKOUT has been remotely programmed.

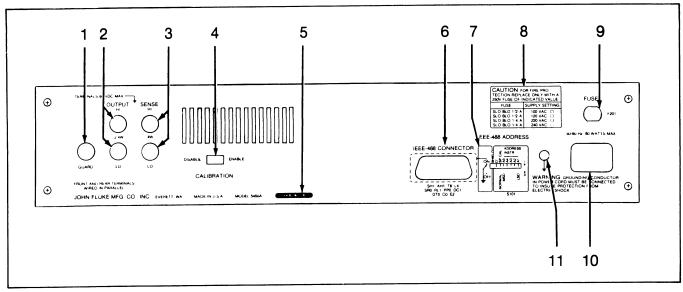


Figure 2-2. Rear Panel Controls, Indicators, and Connectors

Table 2-4. Rear Panel Controls, Indicators, and Connectors

ITEM NO.	NAME	FUNCTION
1	GUARD terminal	Connects to the internal guard chassis. The GUARD terminal is connected to the OUTPUT LO terminal via a $100\Omega$ resistor to reduce noise and leakage. If the EXT GUARD switch LED is on, the GUARD terminal is not tied to OUTPUT LO. The GUARD terminal is wired in parallel to the front panel GD terminal.
2	OUTPUT terminals	Connects the UUT to the 5450A's internal resistors in either 2-wire or 4-wire configurations. In the 4-wire configuration, the UUT source terminals are connected to the 5450A OUTPUT terminals. The rear panel OUTPUT terminals are wired in parallel with the front panel OUTPUT terminals.
3	SENSE terminals	Connects the sense terminals of the UUT to the 5450A's internal resistors when the UUT is in the 4-wire configuration. The rear panel SENSE terminals are wired in parallel to the front panel SENSE terminals.
4	CALIBRATION switch	When in the ENABLE position, allows the 5450A's internal calibration constants to be modified. Normally covered by a calibration sticker.
5	Serial number	
6	IEEE-488 CONNECTOR	Standard 24-pin IEEE-488 receptacle used to connect the 5450A to an instrument controller.
7	IEEE-488 ADDRESS switches	Eight recessed toggle switches.
	A1-A5	Used to set the IEEE-488 instrument address of the 5450A.
	NORMAL/SP CAL	NORMAL (down): Used for normal ratio calibration method specified in Section 4 of this manual.

Table 2-4. Rear Panel Controls, Indicators, and Connectors (cont)

ITEM NO.	NAME	FUNCTION
		SP CAL (up): Allows calibration by general calibration methods such as in a standards calibration laboratory.
	D1	Not used.
	SHIELD	OFF (down): Disconnects IEEE-488 interface cable shield (pin 12) from 5450A ground.
		ON (up): Normal position. Connects IEEE-488 interface cable shield (pin 12) to 5450A ground.
8	FUSE/SUPPLY SETTING	Indicates the internally configured line-voltage selection and fuse sizes for the 5450A.
9	FUSE	Line power fuse.
10	Line power receptacle	Receptacle for line power cord.
11	Ground	Chassis ground.

#### 2-9. IEEE-488 ADDRESS SWITCHES

The rear panel IEEE-488 address switches are used to set the 5450A to a known address so that the controller may talk and listen to the 5450A. In addition, one of the switches is used to specify the type of calibration to be performed. The switches operate as follows:

#### • SHIELD switch

Setting the SHIELD switch to the ON (up) position connects the shield on the IEEE-488 interface cable (pin 12) to the 5450A ground. Setting the switch to the OFF (down) position disconnects pin 12 from the 5450A ground. The SHIELD switch is typically ON to avoid instrument emissions and noise susceptibility.

#### D1 switch

The D1 switch is not used.

#### • NORMAL/SP CAL switch

The NORMAL/SP CAL switch configures the 5450A for the type of calibration to be performed. The switch is placed in the down (NORMAL) position when the 5450A is to be calibrated using the procedure described in Section 4 of this manual. If the user wishes to calibrate the 5450A by alternate methods, the switch should be placed in the up (SPecial CALibration) position. For details, refer to Section 4. The switch is active only when the CALIBRATION switch is enabled.

#### • INSTR ADDRESS switches (A1-A5)

Switches A1-A5 are used to select the IEEE-488 address of the 5450A. A1 sets the "1-bit", A2 sets the "2-bit", A3 sets the "4-bit", and so on. A bit is set to 1 when the respective switch is in the up position, and is set to 0 when the switch is in the down position. Table 2-5 shows the switch positions for each possible address.

#### 2-10. CALIBRATION SWITCH

The CALIBRATION switch places the 5450A in the calibration mode. The calibration mode should be enabled only when calibrating the 5450A. When the switch is in the DISABLE position, the calibration constants stored in the 5450A are protected from being changed. When the switch is in the ENABLE position, the front panel CAL LED illuminates and the internally stored calibration constants may be changed.

After the instrument has been calibrated and the switch has been set to the DISABLE position, it is important that the CALIBRATION switch be covered with a calibration sticker.

#### **CAUTION**

If the 5450A is operated with the CALIBRATION switch in the ENABLE position, the internal calibration constants may be inadvertently modified during normal operation. If this occurs, do not use the instrument; return it to the appropriate calibration facility for recalibration.

Table 2-5. IEEE-488 Addresses

DISPLAYED	INSTR ADDRESS			INSTR ADDR			
VALUE	<b>A</b> 5	<b>A</b> 4	А3	A2	A1		
00	DN	DN	DN	DN	DN		
01	DN	DN	DN	DN	UP		
02	DN	DN	DN	UP	DN		
02	DIN	DN	DN	OP	DN		
03	DN	DN	DN	UP	UP		
04	DN	DN	UP	DN	DN		
05	DN	DN	UP	DN	UP		
05	DN	ы	Or I	DIV	O1		
06	DN	DN	UP	UP	DN		
07	DN	DN	UP	UP	UP		
		UP	DN	DN	DN		
08	DN	UP	DN	DN	DIN		
09	DN	UP	DN	DN	UP		
10	DN	UP	DN	UP	DN		
11	DN	UP	DN	UP	UP		
11	DN	Or .	DIN	. 01	O1		
12	DN	UP	UP	DN	DN		
13	DN	UP	UP	DN	UP		
		UP	UP	UP	DN		
14	DN	UP .	Or I	OI .	DIA		
15	DN	UP	UP	UP	UP		
16	UP	DN	DN	DN	DN		
17	UP	DN	DN	DN	UP		
''	UP	DN	DIN	BIV	Oi.		
18	UP	DN	DN	UP	DN		
19	UP	DN	DN	UP	UP		
20	UP	DN	UP	DN	DN		
20	UP	DIN	Or .	DIV	DIN		
21	UP	DN	UP	DN	UP		
22	UP	DN	UP	UP	DN		
23	UP	DN	UP	UP	UP		
23	OF .	DIN	01	01	01		
24	UP	UP	DN	DN	DN		
25	UP	UP	DN	DN	UP		
26	UP	UP	DN	UP	DN		
20	01	Oi .			2.,		
27	UP	UP	DN	UP	UP		
28	UP	UP	UP	DN	DN		
29	UP	UP	UP	DN	UP		
20	<b>.</b>	<b>.</b>			<u>-</u> ,		
30	UP	UP	UP	UP	DN		
	<del>-</del> .	<del>-</del> ·	i	I	İ		

#### 2-11. Connecting to a Unit under Test

#### 2-12. SELECTING CABLES

Cables used to connect the 5450A to the unit under test (UUT) should be high-quality COAX or twisted pair shielded cables. Cables with a separate shield conductor are not necessary for typical bench applications, but can be advantageous in systems applications. Such cables are readily available in various lengths from manufacturers such as Pomona Electronics. The cables should generally be kept as short as possible.

#### 2-13. FRONT AND REAR TERMINALS

#### CAUTION

To prevent instrument damage, input potentials must not exceed 60V dc to chassis from any terminal. In addition, the voltage from HI to LO terminals must not exceed the maximum voltage shown in the specifications for the selected resistor.

The 5450A provides both front and rear panel terminals. The front panel terminals are wired in parallel with the rear panel terminals.

All of the terminals are low-thermal alloy and accept standard banana plugs. The SENSE and OUTPUT terminals may be connected directly to twin banana plugs with 3/4-inch spacing. The rear panel terminals may also be connected to spade type connectors, which offer lower contact resistance than banana plugs.

It is generally inadvisable to connect cables to the front and rear terminals at the same time. The unused cables may degrade the specifications of the 5450A.

#### 2-14. 4-WIRE CONNECTIONS

UUTs for the 5450A typically use a 4-wire circuit to measure resistance. Two leads from the UUT provide a constant current or a voltage source to the resistance being measured. Two other leads are used to measure (or sense) the voltage drop developed across the resistance. The advantage of the 4-wire technique is that it eliminates the effects of lead and contact resistance.

For UUTs that use the 4-wire measurement technique, the UUT source leads are connected to the 5450A OUTPUT terminals and the UUT sense leads are connected to the 5450A SENSE terminals.

#### 2-15. 2-WIRE CONNECTIONS

Some UUTs, usually meters with lower accuracy, use a 2-wire circuit to measure resistance. The disadvantage of this type of measurement is that the lead and contact resistance of the leads and paths add directly to the

resistance. (Consequently, some meters allow lead compensation.)

The 5450A features a 2 WIRE COMP switch which compensates for lead and contact resistance. The 2 WIRE COMP switch should normally be enabled (lit) when calibrating 2-wire UUTs. When the 2 WIRE COMP switch is enabled, the 5450A automatically adds the value of its internal lead and contact resistance to the value of the 4-wire resistance and displays this sum to the operator. For even higher accuracy, this internal compensation value may be updated daily. (For instructions, see the heading "2-Wire Enhancement" in this section.)

UUTs that use the 2-wire measurement technique are connected directly to the 5450A OUTPUT terminals.

#### 2-16. GUARD CONNECTIONS

If the 5450A is used in a systems environment, it may be advantageous to use shielded cables for OUTPUT and SENSE connections. If shielded cables are used, the shields of the cables may be connected to the GD (or GUARD) terminal of the 5450A. This helps shield the measurement system from EMI and may also provide additional protection from ground current errors.

#### 2-17. Powering Up the 5450A

#### **CAUTION**

Do not power up the 5450A while the CALIBRATION switch is in the ENABLE position, as the internal calibration constants may be inadvertently modified.

Before powering up the 5450A, ensure that the line voltage is properly selected and the line power cord is connected to a suitable outlet. To turn the 5450A on, press the front panel POWER switch.

When the 5450A is turned on, all display segments light briefly to test the display. If any segments are unlit, the 5450A should be repaired.

The display then indicates the instrument's IEEE-488 address for approximately 2 seconds. The address may be changed using the rear panel IEEE-488 ADDRESS switches. (For instructions, see the heading "IEEE-488 Address Switches" in this section.)

The display may then briefly indicate another message (such as "5450A"). If so, this message has been entered during a previous calibration. Contact the appropriate calibration facility if the instrument displays a message that you do not fully understand.

Once the preliminary messages have been displayed, the display should read OPEN and the OUTPUT light should turn on. This indicates that the 5450A is in the OUTPUT mode and is sourcing infinite resistance. The 5450A requires 2 hours of warm-up before meeting the specifications given in Section 1.

#### 2-18. Selecting an Output

To select an output resistance, the 5450A must be in the OUTPUT mode (the OUTPUT light should be on). If the 5450A is in the ENTRY mode, it is possible to return it to the OUTPUT mode by pressing either the up arrow, down arrow, or x1/x1.9 switches, or by pressing the DELETE switch several times. If the 5450A is in the ERROR mode, it is possible to return it to the OUTPUT mode by pressing any front panel switch except ENTRY MODE, EXT GUARD or PPM/%.

If the UUT is a 2-wire device, it should be connected to the OUTPUT terminals only. In this case, ensure that the 2 WIRE COMP switch is lit. If the UUT is a 4-wire device, it should be connected to both the OUTPUT and SENSE terminals. In this case, ensure that the 2 WIRE COMP switch is unlit.

#### **NOTE**

When 2 WIRE COMP is selected, internal lead and contact resistance is added to the 4-wire resistance value to create the displayed resistance. Consequently, calibration errors will occur if the 2 WIRE COMP switch is set incorrectly.

In typical applications, the EXT GUARD switch should not be lit. The EXT GUARD switch is provided to allow for special configurations such as those that occur when calibrating the 5450A.

To select a decade value (i.e.,  $1\Omega$ ,  $10\Omega$ , etc.), ensure that the x1/x1.9 switch is unlit. To select a x1.9 value (i.e.,  $1.9\Omega$ ,  $19\Omega$ , etc.), ensure that the x1/x1.9 switch is lit.

To select an output range, press the switch on the main keyboard that has the desired resistance written above it in white characters. For example, press the  $10~\mathrm{k}\Omega$  switch to cause the 5450A to source a nominal  $10~\mathrm{k}\Omega$ . If the x1/x1.9 switch is lit, the output will be a nominal  $19~\mathrm{k}\Omega$ .

To select a resistance that is one decade higher than that presently selected, press the up arrow switch. If the output is presently 100 M $\Omega$  or 19 M $\Omega$ , pressing the up arrow switch selects OPEN (infinite resistance).

To select a resistance that is one decade lower than that presently selected, press the down arrow switch. If the output is presently  $1\Omega$  or  $1.9\Omega$ , pressing the down arrow switch selects a SHORT (nominally zero resistance).

In the OUTPUT mode, the 5450A displays the actual, characterized value of its output resistance instead of the nominal value selected. For example, the 5450A might display 9.99987 when the nominal value 10.00000 is selected.

For 4-wire configurations (when the 2 WIRE COMP switch is unlit), the 5450A displays the resistance between the internal junction of the OUTPUT HI and SENSE HI terminals and the internal junction of the OUTPUT LO and SENSE LO terminals.

For 2-wire configurations (when the 2 WIRE COMP switch is lit), the 5450A displays the resistance between the OUTPUT HI and OUTPUT LO terminals at the front or rear panel. The 2 WIRE COMP offset may be characterized to include the resistance of a specific set of test leads. (See the heading "2-Wire Enhancement" in this section.)

#### 2-19. Computing Error

The 5450A can automatically calculate and display the error of the UUT in percent or parts-per-million (ppm). The 5450A computes the UUT error by comparing the UUT reading (entered into the 5450A by the operator) to the characterized value of the resistance presently being sourced by the 5450A.

To enter the UUT reading, press the ENTRY MODE switch. The LED in the ENTRY MODE switch will light, and the ENTRY light will turn on, prompting the operator for an entry.

#### NOTE

Any computed error would be meaningless when the 5450A is sourcing an OPEN. Therefore, the ENTRY MODE switch is inactive when an OPEN is selected.

Then, using the main keyboard, enter the UUT reading. The rightmost digit of the display indicates the units for which the 5450A expects the entry: blank for ohms, K for kilohms, and M for megohms.

Use of the decimal point (.) is optional. If the decimal point is not explicitly entered, the 5450A assumes that the decimal point is in the same position as it was when the output resistance was displayed.

If an error is made while entering the UUT reading, press the DELETE switch to erase the last character entered. If the leftmost character is erased, the 5450A returns to the OUTPUT mode.

The entry is concluded by pressing the ENTER switch. If any of the rightmost digits are not entered prior to pressing the ENTER switch, they are assumed to be zeros.

Once the ENTER switch has been pressed, the 5450A changes to the ERROR mode. (This is indicated by the ERROR light turning on.)

The error may be displayed in percent or ppm. If the PPM/% switch is lit, the error is displayed in percent; otherwise, the error is displayed in ppm. The percent sign is shown in the display as PCT, and ppm is shown as PPM.

While in the ERROR mode, pressing the ENTRY MODE switch reselects the ENTRY mode. (This is useful if the UUT reading has changed.) In this case, the 5450A displays the UUT reading previously entered. The entry may be changed after using the DELETE switch. Then, pressing the ENTER switch causes the 5450A to recompute the UUT error.

To return to the OUTPUT mode, press any switch except ENTRY MODE, EXT GUARD, or PPM/%. Another output resistance may then be selected.

#### 2-20. 2-Wire Enhancement

To decrease the uncertainty of the 5450A while calibrating 2-wire UUTs, the 5450A provides the capability of accommodating a short-term, user-performed calibration of the 5450A's lead and contact resistance. This short-term calibration updates the 2 WIRE COMP offset which is added to the 4-wire values of resistance. A selected test lead may be included in this characterization by making the measurement at the end of the test lead instead of directly at the 5450A OUTPUT terminals. If the measurement is made right at the terminals, it is valid for the other set of terminals as well.

Once characterized, the value may be used for 24 hours, or until the 5450A is turned off, whichever occurs first. For the specifications when using this optional capability, see Section 1.

To perform this characterization:

- 1. Connect a calibrated high-accuracy DMM (such as the Fluke 8505A or Fluke 8520A) to either the front or rear panel OUTPUT HI and LO terminals of the 5450A. Connect the DMM source HI and sense HI terminals together at the OUTPUT HI terminals of the 5450A. Connect the DMM source LO and sense LO terminals together at the OUTPUT LO terminal of the 5450A.
- 2. Select a SHORT from the 5450A.
- 3. Press 2 WIRE COMP. (The LED in the switch should light.)
- 4. Press ENTRY MODE.

- 5. After the external DMM is fully settled on the lowest resistance range, use the 5450A main keyboard to enter the value displayed by the external DMM.
- 6. Press ENTER.
- 7. The 5450A now displays the error of the new zero (in PPM or %) from the zero stored during the last complete calibration.
- 8. Press ENTRY MODE then press ENTER again. This stores the entered offset as the new offset to be used in subsequent 2-wire calculations.

The 5450A will use the new 2-wire compensation value until the 5450A is powered down. The improved specifications apply for 24 hours after enhancement. To erase the new compensation value and resume use of the offset value entered during the last complete calibration, cycle the POWER switch off and back on again.

#### 2-21. REMOTE OPERATION

#### 2-22. Introduction

The 5450A may be remotely controlled by an IEEE-488 controller, such as the Fluke Model 1722A Instrument Controller. A controller can facilitate the automated calibration of UUTs by operating the 5450A with a computer program.

Introductory literature about the IEEE-488 interface bus is available from local Fluke representatives, including "IEEE Standard 488-1978 Digital Interface for Programmable Instrumentation" (AB-36) and "Communication Over the IEEE-488 Bus" (B0079).

Remotely programming the 5450A involves a series of simple commands that correspond closely to front panel operation.

A typical automated sequence would begin by the controller programming the 5450A to provide a particular resistance. The controller would next request the characterized value of the 5450A resistance. The controller would then request the UUT to take a reading on the desired resistance range and transmit the reading back to the controller. Finally, the controller would compute the actual error and compare it to the error allowable for the particular UUT. The actual error could then be logged and the next range of the UUT could be verified.

A semi-automated sequence can be used for UUTs that are not remotely programmable. In this case, a typical sequence would be the same as above except that the UUT would be controlled manually, and the controller would solicit the UUT reading from the operator.

#### 2-23. Capabilities

All functions available on the front panel (except for the POWER switch) are available using remote commands. The 5450A can become a listener for responding to remote commands. The 5450A can also become a talker in order to send the controller the instrument's status and the actual values of the programmed resistance.

The 5450A supports the ANSI/IEEE Standard 488-1978 function subsets listed in the specifications in Section 1 of this manual.

#### 2-24. Device Clear

The universal bus command DCL (device clear) and the addressed bus command SDC (selective device clear) may be used to place the 5450A back to a known reset condition. The instrument is set back to the power-up conditions of OUTPUT MODE, OPEN, X1, PPM, EXT GUARD OFF, and 2 WIRE COMP OFF.

#### 2-25. Local/Remote Operation

In typical operation, the 5450A front panel REMOTE light turns on when the 5450A is addressed as a listener or talker and data is being transferred. The REMOTE light remains on if the instrument is in REMS (REMote State). If the user presses any of the front panel switches, the instrument returns to local control and the REMOTE light turns off.

The universal bus command LLO (local lockout) may be used to disable the controls on the front panel of the 5450A. The bus command LOCAL reenables the 5450A

controls. The front panel controls can also be reenabled by cycling the 5450A POWER switch.

#### 2-26. Command Separators and Terminators

The individual commands sent to the 5450A must be separated by either a comma (,) or a semicolon (;). Each sequence of commands must be terminated by a carriage return (CR), line feed (LF), and/or an "end or identify" (EOI). If spaces (blanks) are sent to the 5450A, they are ignored. As the 5450A is case-insensitive, any command can be sent in either uppercase, lowercase, or a combination of the two.

When the 5450A is addressed as a talker, the string sent back to the controller is concluded by an LF and an EOI.

#### 2-27. Remote Error Handling

If the 5450A receives a remote command containing an error, the remainder of the erroneous command is ignored. The 5450A responds to an error by setting two bits of the internal SPL byte: the least significant bit (with a binary weighting of 1), and RSV (bit 6, with a binary weighting of 64). When the SPL byte is read by the controller, the error bit is reset to zero.

#### 2-28. Command Set

The set of device-dependent commands for the 5450A is shown in Tables 2-6 and 2-7. Table 2-6 shows the commands that correspond on a one-to-one basis with the front panel switches. Table 2-7 shows the additional commands that correspond to pushing several front panel switches in sequence. Note that most of the commands use the same nomenclature as the front panel.

Table 2-6. Simple IEEE-488 Commands

COMMAND	RESULT
UP;	Increments the decade of the selected resistance, just like the up arrow switch.
DN; or DOWN;	Decrements the decade of the selected resistance, just like the down arrow switch.
X1/X1.9;	Toggles the selected resistance between x1 and x1.9 values, just like the x1/x1.9 switch.
X1;	Selects decade values of resistance (e.g., 10 k $\Omega$ , 100 k $\Omega$ , etc). Equivalent to setting the x1/x1.9 switch to x1 (LED off).
X1.9;	Selects x1.9 values of resistance (e.g., 19 k $\Omega$ , 190 k $\Omega$ , etc.). Equivalent to setting the x1/x1.9 switch to x1.9 (LED on).
PPM/%;	Toggles the error display units between % and PPM (parts-per-million), just like the PPM/% switch.
РРМ;	Selects display of UUT error in PPM (parts-per-million). Equivalent to setting the PPM/% switch to PPM (LED off).

Table 2-6. Simple IEEE-488 Commands (cont)

COMMAND	RESULT
%; or PCT;	Selects display of UUT error in percent. Equivalent to setting the PPM/% switch to % (LED on).
ENTRY MODE;	Selects the ENTRY mode. Conditions the 5450A to receive a following numeric value which represents the UUT display. Performs the same function as the ENTRY MODE switch.
SHORT;	Programs the 5450A to a SHORT (near 0 ohms).
OPEN;	Programs the 5450A to OPEN (infinite resistance).
0; - 9; and .;	The commands 0; 1; 2; 3; 4; 5; 6; 7; 8; 9; and .; perform the same function as the corresponding front panel switches:
	If the 5450A is in the OUTPUT or ERROR mode, the digits 0 through 9 select the corresponding decade of resistance to be sourced (i.e., $0=$ short, $1=1\Omega$ , $2=10\Omega$ , $3=100\Omega$ , $4=1$ k $\Omega$ , $5=10$ k $\Omega$ , $6=100$ k $\Omega$ , $7=1$ M $\Omega$ , $8=10$ M $\Omega$ , $9=100$ M $\Omega$ ). The resistance actually sourced may be modified by the X1.9 command.
	If the 5450A is in the ENTRY mode (selected using the ENTRY MODE command), the digits 0 through 9 may be used in conjunction with the decimal point (.) to enter the UUT display for computation of UUT error. In this case, the digits are followed by the ENTER command to select the ERROR mode.
DELETE;	If the 5450A is in the ENTRY mode, the DELETE command causes the 5450A to delete the last numeric character entered. This command performs the same function as the DELETE switch.
ENTER;	Active in the ENTRY mode. Enters numbers programmed in the ENTRY mode, computes UUT error, and selects the ERROR mode. Performs the same function as the ENTER switch.
2 WIRE COMP;	Toggles the 2 WIRE COMP function on or off, depending on the previous state of this function. Performs the same function as the 2 WIRE COMP switch.
2 WIRE COMP ON;	Causes 2 WIRE COMP to be active.
2 WIRE COMP OFF;	Causes 2 WIRE COMP to be deactivated.
EXT GUARD;	Toggles the EXT GUARD function on or off, depending on the previous state of this function. This command performs the same function as the EXT GUARD switch.
EXT GUARD ON;	Causes EXT GUARD to become active.
EXT GUARD OFF;	Causes EXT GUARD to be deactivated.

Table 2-7. Extended IEEE-488 Commands

COMMAND	RESULT
OUTPUT (number);	Selects the OUTPUT mode, and programs the 5450A to source the resistance specified by \(\(\text{number}\)\). \(\(\text{number}\)\) is expressed in ohms and may consist of 0-9, ., + or E.
ENTRY (number);	Enters the $\langle$ number $\rangle$ into the display and computes the resulting error. The instrument is left in the ERROR mode, displaying error in PPM or PCT. $\langle$ number $\rangle$ is expressed in ohms and may consist of 0-9, ., $+$ or E.
PERSONALITY (string);	The personality string is displayed to the operator for approximately 2 seconds during power-up and may also be read by the STAT or STATUS command. When the instrument is originally delivered, the string is set to "5450A". The string may be redefined using the PERSONALITY (string) command. (string) may consist of up to eight alphanumeric characters. Since the 5450A ignores spaces, spaces must be indicated by a % character in the string. The 5450A must be in the CALIBRATION ENABLE mode to successfully execute this command.
CLEAR;	Programs the 5450A to the power-up condition (OUTPUT MODE, OPEN, X1, PPM, 2 WIRE COMP OFF and EXT GUARD OFF). The CLEAR command works exactly like the interface messages DCL and SDC.
VALUE; or ?;	Requests the 5450A to return the characterized value of the resistance presently selected. The number returned is the value in ohms and may contain a decimal point and "E". The number is preceded by a space and is terminated by an LF and an EOI. If an OPEN is presently selected, the number returned is 1E50.
STAT; or STATUS;	Requests a 50-character message from the 5450A which describes the state of the instrument. The message is followed by an LF and an EOI. The character assignments are:
	1-10 5450A display including decimal point (1st character is sign if negative) 11-16 5450A mode ("OUTPUT", "ENTRY" or "ERROR") 17-20 "X1" or "X1.9"
	21-23
	(Blanks mean CALIBRATION DISABLED) 29-31 "EXT" or " " 32-37 "2 WIRE" or " "
	38-45 Personality message 46-47 00 if no error has occurred; 01 if an error has occurred
- No.	48-49 "D1" or " " 50 50 Not used
ERR; or ERROR;	Requests a numeric value of the most recently computed UUT error in PPM. If an error has not been computed since power up, or the result of the last calculation resulted in an error greater than or equal to 2E6 PPM, 1E50 is returned.

#### 2-29. Programming Examples

The following examples show programs for the Fluke 1722A Instrument Controller. For the purpose of these examples, it is assumed that the 5450A is set to the IEEE-488 address of 7.

Example 1: Initializing the 5450A to a power-up condition.

```
PRINT @7, "CLEAR;"
```

Example 2: Programming the 5450A to 10 k $\Omega$ .

```
PRINT @7, "5;"
```

or

PRINT @7, "OUTPUT 10000;"

Example 3: Determining the actual value of the 10 k $\Omega$  nominal resistance selected in Example 2.

```
PRINT @7, "VALUE;" INPUT @7, A1
```

or

PRINT @7, "?;" INPUT @7, A1

Example 4: Stringing the previous commands together.

```
PRINT @7, "CLEAR; OUTPUT 10000; ?;" INPUT @7, A1
```

# Section 3 Theory of Operation

#### 3-1. INTRODUCTION

This section describes the internal operation of the 5450A Resistance Calibrator in two steps. First, a functional description describes the various functional blocks of the instrument and how they are related. Then, a circuit description examines each functional block in more detail, describing how each block works.

#### 3-2. FUNCTIONAL DESCRIPTION

A functional block diagram for the 5450A is shown in Figure 3-1. As this figure shows, the 5450A consists of four functional blocks: the display circuit, the digital circuit, the analog circuit, and the power supply circuit.

The display circuit receives information from the digital circuit and displays this information via the front panel annunciators and alphanumeric display. The display circuit also sends signals to the digital circuit when the operator presses the front panel switches.

The digital circuit controls the 5450A. The microprocessor on the digital circuit decodes operator inputs from the display circuit and translates this information into signals to be sent back to the display circuit and the analog circuit. The digital circuit also contains the IEEE-488 interface circuitry which provides for remote operation of the 5450A.

The analog circuit provides the stimulus to the unit under test (UUT). The circuit contains the relays, relay logic, and resistors to provide the appropriate resistance source to the external terminals.

The power supply circuit of the 5450A provides supply voltages to all other circuits in the 5450A.

#### 3-3. CIRCUIT DESCRIPTION

The four major circuits of the 5450A shown in Figure 3-1 are laid out on five printed circuit assemblies (PCAs): the Digital PCA, the Memory PCA, the Analog PCA, the Display PCA, and the Power Supply PCA. For reference, the location of the five PCAs is shown in Section 5. Detailed schematics are provided in Section 9.

The digital circuit is laid out on the Digital PCA and the Memory PCA. The Digital PCA consists of the microprocessor and IEEE-488 controller circuitry. The Memory PCA contains the ROMs and the EAROM for use by the microprocessor.

The power supply circuit is distributed throughout the instrument. The major components are the Power Supply PCA and power transformer, which are housed together in the transformer compartment towards the rear of the instrument.

The display circuit is contained on the Display PCA, which is located behind the front panel bezel. The Display PCA contains the front panel switches and light emitting diode (LED) displays.

The analog circuit is contained on the Analog PCA, which contains resistors, relays, and relay decoding logic.

#### 3-4. Power Supply Circuit

The 5450A power supply circuit consists of the power supply assembly (which consists of the the transformer and Power Supply PCA) and also consists of circuitry on the Digital PCA (providing regulation for the digital +5V dc voltage supply), the Analog PCA (providing rectification, regulation, and filtering of voltages for the logic and relays), and the Display PCA (providing regulation for the Display PCA supply).

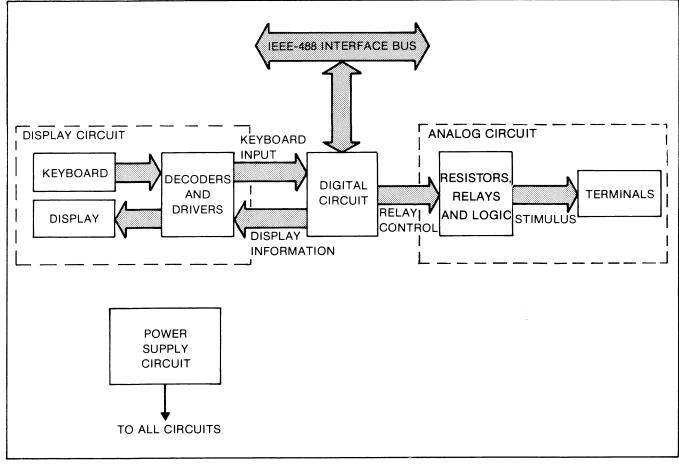


Figure 3-1. Functional Block Diagram

Power transformer T1 provides all voltages to the circuits of the 5450A. The input voltage comes from plug/line filter J10. The voltage is fed through rear panel fuse F201. A spare F201 is contained on the side of the transformer compartment inside the 5450A. The supply is switched via the front panel POWER switch.

#### 3-5. ANALOG SUPPLY CIRCUIT

One secondary winding of power transformer T1 goes to the Analog PCA via J2. This signal is full-wave rectified via CR4 and CR5. Prior to rectification, the low and high leads are fused by 2-amp fast-blow fuses (F1 and F2). A spare fuse is located on the power supply chassis to replace either F1 or F2. The voltage is filtered by C8 and regulated to +5V dc by VR1. The resulting +5V dc voltage is called VCC and is used to supply the digital components on the Analog PCA.

VCC is also indirectly used to energize the relays on the Analog PCA. An unregulated voltage of approximately 8V dc is applied for about 15 ms across the relay coils to energize the relay. The pulse is provided by turning on Q1 and by limiting the amplitude with R2, Q1, CR1, and CR3. To eliminate the heating of the coils (and the subsequent increase of thermal EMFs), the voltage across

the relay coils is then reduced to approximately 3.5V dc by sending VCC through CR2 and turning off Q1 to provide a relay holding voltage. The signal to drive the relays is referenced as RELAY PWR. The logic to create the pulse is discussed under the Analog Circuit heading later in this section.

#### 3-6. DIGITAL SUPPLY CIRCUIT

The primary +5V dc signal for the logic components on the Digital PCA and Memory PCA is called VCC. The return for this voltage is called VSS. To indicate when power is applied, CR201 lights up when VCC is present. (CR201 is located near the large filter capacitor on the Digital PCA.)

On the Digital PCA, regulator U202 takes the rectified output from CR1 and CR2 on the Power Supply PCA and, with its associated components, provides the unguarded +5V operating voltage for the logic circuits. R210, in parallel with U202, provides an increased load capability. Test jumper J201 disconnects the supply, R210, and the load, so that the supply can be checked without a load. The variable resistor R204 is used to adjust VCC to the nominal +5V dc. The voltage may be

easily set while monitoring the voltage across TP201 (VCC) and TP202 (VSS).

#### 3-7. DISPLAY SUPPLY CIRCUIT

The Display PCA uses the VUNREG output from the Power Supply PCA. This rectified and filtered signal is regulated on the Display PCA by U14 to create a +5V regulated signal called VLED. This supply is used for all the components on the Display PCA. The signal is returned via VUNREG RETURN.

#### 3-8. Display Circuit

The Display PCA has two primary functions. First, it displays information to the operator. Second, it accepts the operator's instructions via the front panel switches. The display consists of four, two-character, fourteen-segment displays. The switches provide digital inputs to the microprocessor and operate completely in full duplex mode. All information is transferred to the microprocessor, and the resultant front panel indication is sent back to the display.

#### 3-9. DISPLAYS

The alphanumeric display characters (DS1-DS4) are duplexed. Each character has two cathodes and eight anodes. Segments A1-G1 are enabled (lit) when cathode K1 or K3 is low and the appropriate anode(s) are high. Segments A2-G2 are enabled when the appropriate anode(s) are high and K2 or K4 is low. DP2 is lit when the DP anode is high and K2 is low. DP4 is lit when DP anode is high and K4 is low.

The characters are turned on in pairs. The cathode K1 of DS1 is enabled at the same time as cathode K1 of DS3. Next, cathodes K2 of DS1 and K2 of DS3 are enabled. This sequence continues until K4 of DS2 and K4 of DS4 are enabled, after which the cathodes of DS5-DS4 are enabled. The sequence is then repeated. This process is interrupt-driven by the microprocessor, and as such, periodically begins based on time-synchronized interrupts to the microprocessor. Cycles begin about each 20 ms for a 60 Hz line source.

Data lines D0-D7 provide input information to the display. As a sequence begins, data is strobed into decoder U12. The output from pins 5 and 6 of U12 enable latches U11 or U1. The output is gated from U12 to U11 and U1 by U9 when the input to U9 (\$\overline{SEG}\$) is low. Data indicating the anodes to be lit is latched into U11 and U1 before each is disabled. Once these latches have stored the information to be displayed, U10 is used to decode the data lines into a cathode signal to drive the desired cathodes of DS1-DS4 low. The latch U10 is enabled by the signal \$\overline{DIGIT}\$ going low. Source drivers U15 and U2 provide the current to the display characters, and U7, U8, and U15 sink the current. The LEDs in the switches are

driven similarly, with the unary bit of U12 (U12-4) activating the cathodes, and U11 or U1 driving the anodes.

#### 3-10. SWITCHES

The switches S1-S20 on the display are read once during each refresh cycle of the Display PCA. U10 sequentially enables each column of switches (e.g., S2 and S1 or S6, S5, S4, and S3). As the column is enabled, the  $\overline{SW}$  signal is set low to gate the column through three-state driver U13. The column is then read by the microprocessor via D0, D1, D3, D4, and D5 lines.

#### 3-11. Digital Circuit

The digital circuit is contained on the Digital PCA and the Memory PCA. The Memory PCA contains ROMs and EAROM. The Digital PCA is divided into three separate sections: the digital controller, the IEEE-488 interface, and the power supply. The first two sections of the Digital PCA are described below; the third section (the power supply) is discussed under a previous heading. Certain portions of the Digital PCA circuitry are not used in this particular application of the PCA; this circuitry is not discussed.

#### 3-12. CLOCK CIRCUIT

The microprocessor is provided with a 4 MHz square wave by the clock circuit. Crystal Y1 is an 8 MHz oscillator that clocks the standard binary counter, U20. Outputs from the counter, in addition to the 4 MHz output, are 2 MHz, which is not used in this particular application, and 1 MHz, for use in the IEEE-488 circuitry. The parallel inverters at U19-10 and U19-12, with R6, keep the rise and fall time of the clock within specifications. Ringing is kept within acceptable limits by R7.

#### 3-13. MICROPROCESSOR

The microprocessor is a single component with fully decoded and timed output signals to control standard memory or peripheral circuits. Supply voltage for the device is 5V dc. The only clock required is a single-phase, 5V, 4 MHz square wave.

The internal register configuration of the microprocessor contains 208 bits of read/write memory accessible to the program. The registers include two sets of general-purpose accumulator and flag registers that may be used as four 16-bit pairs or as eight 8-bit individuals. Also included is a 16-bit stack pointer that allows implementation of multiple-level interrupts and unlimited subroutine nesting. Tabular data manipulation and relocatable code implementation is accomplished with the two 16-bit registers. The memory refresh and interrupt vector registers are not used in this application. The last register is a 16-bit program counter.

#### 3-14. RESET

The instrument circuitry is automatically reset at power up. When one-shot U36-12 is triggered, it provides U22-13 and 12 with a high and low signal respectively to provide a reset to the controller assembly. Line voltage (FLINE) input from U23-11 triggers U36, releasing the counter U28, which in turn clocks U22 on a count of eight to remove the reset. This places the reset on the circuit for a predetermined time during power up.

When power is removed, U36-12 times out and sets U22 to apply a reset to the circuitry. Resistor R19 and capacitor C29 provide a timeout for resetting the instrument by shorting test points TP9 and TP10 together.

#### 3-15. WAIT

A WAIT signal is input to the microprocessor at U18-24 to indicate that the addressed memory or I/O device needs an extended bus cycle. The signal can be generated by the IEEE-488 circuit or by U9 as a result of an M1 output from the microprocessor. M1 is active when the microprocessor is performing an op code fetch cycle. The M1 memory access is shorter than all other cycles, in order to provide for the refresh cycle (not used). One WAIT state is added to allow for the access time of the ROMs.

#### 3-16. INTERRUPT

Two sources of interrupts are used in the 5450A. These are the IEEE-488 circuit and the phase-locked loop circuit. The interrupt signals are combined in a wired-OR circuit for an input to the microprocessor at U18-16. R26 is a passive pull-up for the line, and in conjunction with C31, filters high frequency chassis noise (10 MHz) to prevent spurious inputs. U17-4 in the OR circuit is active (low) from an interrupt from the IEEE-488. Line-synchronous interrupts (MARK) are generated by U17-6 each time the phase-locked loop output has a positive transition. The MARK is enabled and cleared by signal MARK EN at U35-1 from U8-19.

The microprocessor output is synchronized to the line frequency by the phase locked loop (PLL). The line frequency input (U29-14) is multiplied by 8 by the PLL circuitry U28 and U29. The PLL output (U29-4), which is eight times the input frequency, provides a clock for MARK interrupts (U35-3) and display blanking (U21-1).

#### 3-17. ANALOG PCA DATA TRANSFER

The Digital PCA transfers information to the Analog PCA via the cable connected to J15 on the Digital PCA. Data transferred to the Analog PCA decodes to relay calls and to a signal to toggle the voltage used to energize the relays. Data transferred from the Analog PCA is a digital indication that the CALIBRATION switch is either enabled or disabled. The data lines of J15 (D0-D7) are tri-state and may be used to send or receive data.

To send information to the Analog PCA, the following sequence occurs: U18 conditions address lines A0, A1, A2, A3, A6, and A7 to indicate that data is to be sent to the Analog PCA. At the same time, D0-D7 of U18 are set to indicate the particular relay that is to be called. Once this data is available, U18 pulls  $\overline{IOREQ}$  (U18-20) low. At this time, the address information (A0, A1, A2, A3, A6, A7) is decoded in U25, U16, and U34 to set the WRITEO signal low at U25-14 and J15-3. This signal is sent to the Analog PCA via J15-2 and indicates that the data on J15-9 through J15-16 is valid to be decoded.

Once all the relay data is latched into the Analog PCA, the address lines of U18 are set to make the WRITE1 line (J15-6) go low. This enables the relay drivers and pulses relay voltage high (approximately 8V dc). About 15 ms later, the same sequence occurs again. This time, the result of the WRITE1 line going low is to toggle the voltage for the relays low (approximately 3.5V dc). The STATUS1 signal (J15-1) is used to clear all of the relay latches on the Analog PCA.

To receive information about the status of the CALIBRATION switch from the Analog PCA, the following sequence occurs: U18 conditions data lines A0, A1, A2, A3, A6, and A7 to indicate that data is to be received from the Analog PCA. Once this data is available, U18 pulls  $\overline{IOREQ}$  low. At this time, this address information is decoded in U25, U16, and U34 to set the  $\overline{READO}$  signal low at U25-13 and J15-2. This signal is sent to the Analog PCA and passes the status of the CALIBRATION switch to D7 (J1-9). U18 then reads the data lines to input this information.

#### 3-18. MEMORY SELECT

The memory for the digital controller consists of four RAMs (random access memory), three ROMs (actually PROMS, programmable read-only memory), one EAROM (electrically alterable read-only memory), and the necessary control circuits. The ROMs and EAROM and decoder circuit are located on the Memory PCA above and towards the front of the Digital PCA.

The A10, A11, A12, A13, and A15 address lines of U18 are decoded by U2 to generate the chip select signal for the RAM ICs (pin 8 on U4, U5, U6, and U7). The lower address lines of U18 (A0-A9) are directly decoded by the RAMs.

The ROMs and EAROM located on the Memory PCA are connected by two cables to the Digital PCA. The first cable connects to U14 on the Digital PCA and carries the data (D0-D7) and lower order addresses (A0-A10). The second cable connects to P16 on the Digital PCA and carries the upper address lines (A11-A15), MEMREQ, WR, RFSH, and RESET. The high-order address signals from P16 are decoded by U1 on the Memory PCA to set  $\overline{OE}$  on the appropriate ROM to enable output.

In addition to the two digital signal cables that originate on the Digital PCA, the Memory PCA has another cable that originates on the Analog PCA. This cable normally connects VCC to  $\overline{WE}$  (write enable) on the EAROM. This disables the possibility of writing data to the EAROM. This is particularly important to prevent undesired writes during instrument power-up sequences. When the rear panel CALIBRATION switch is in the ENABLE position, the  $\overline{WE}$  line to the EAROM is connected to  $\overline{WR}$  from the Digital PCA. This allows data to be written into the EAROM.

#### 3-19. DISPLAY BLANK

The phase-locked loop clocks U21-1 to count toward eight. If it reaches that count without being reset by the SW signal from U25-8, U21-6 goes high and blanks the front panel display. This prevents the possibility of the microprocessor stopping for some reason when data is latched into the front panel and destroying the LEDs by allowing too long a duty cycle.

#### 3-20. IEEE-488 INTERFACE

All utilized device interface functions defined by the IEEE-488 interface standard (IEEE Standard 488-1978) are performed within U101. The bidirectional data lines (D101-D108) from U108 and U109 are normally three-state drivers; however, when parallel poll is selected (not used in the 5450A), they change to open collector. The control lines in U110 are bidirectional under the control of U101. Control lines NRFD, NDAC, and SRQ have open collector outputs; the remaining control lines are three-state.

The interface is addressed from the digital controller with A7 high and A6 low in conjunction with  $\overline{IOREQ}$  low. A0, A1, and A2 are used to address the internal registers in U101. Switch S103 places the instrument IEEE-488 address on the bus when U102 is enabled from U101. Timing is provided by a 1 MHz clock derived from the 4 MHz clock on the Digital PCA. The 1 MHz clock is synchronized to the microprocessor I/O access cycle by U103, U104, and U105.

#### 3-21. Analog Circuit

The analog circuit is contained on the Analog PCA, which resides on the right when the instrument is viewed from the front. The Analog PCA contains the resistors, the relays to select the resistors, and the logic necessary to decode and energize the relays from signals received from the Digital PCA. The nature of the digital signals originating from the Digital PCA is discussed in the digital circuit description earlier in this section. The Analog PCA also contains regulation and filtering for its 5V dc power supply, logic, and relays. This circuitry is discussed in the power supply circuit description earlier in this section.

The Analog PCA has two sections, the digital section and the analog section. Each is described below.

#### 3-22. DIGITAL SECTION

The digital section of the Analog PCA consists of the logic to decode the logic commands from the Digital PCA and energize the relays.

To deenergize relays, the following sequence occurs: STATUSO (J1-1) becomes low. This signal goes to U1-4 to provide a larger drive capability. The signal originating from U1-6 clears all the latches on the Analog PCA, which deenergizes all of the relays. Simultaneously, the signal resets U2, which is the flip-flop that specifies whether the relays will receive a high or low voltage to the coils. The reset sets low voltage.

Data lines D0-D7 are then set to the desired relay call by the microprocessor. This data is transferred to the output of U4 (U4-11 through U4-18) by WRITEO going low. WRITEO, which is low, goes through U1 to cause EN (U4-12) to go low. The output of U4 is decoded two ways. The output of U4-11 through U4-14 is decoded by U5 to chip-enable either U6, U9, U14, or U16 to receive the data. The output of U4-15 through U4-18 consists of the data that will be latched into the enabled IC (U6, U9, U14, or U16). The actual latch occurs when the output of decoder U5 goes low, thus setting EN low for the desired latch. The actions described in this paragraph are repeated until all of the relays to be energized are latched into U6, U9, U14, and U16.

Once the relay data is clocked into the latches, WRITEI goes low momentarily. This not only toggles U2 to condition the relay voltage to be high (RELAY PWR), it transfers the data held in U6, U9, U14, and U16 to U8, U10, U13, and U15. These latches directly drive U7, U11, U12, U17, and U18, which are the relay current sinks. These current sinks also contain transient suppressor diodes. Approximately 15 ms after WRITEI goes low to energize the relays, WRITEI goes low again. This time, the result is to toggle flip-flop U2 to condition the relay power supply to go to the lower-voltage holding state. This supply is discussed in more detail in the description of the power supply, above.

The status of the CALIBRATION switch is read back to the Digital PCA periodically. The setting of the CALIBRATION switch (S1) ties U4-11 either to VCC via a pull-up resistor (R1), or to low. The read occurs when READO goes low, indicating that U4 is to be a driver to the Digital PCA as opposed to a receiver. READO also asserts EN on U4. The Digital PCA then reads D7 (U4-9).

#### 3-23. ANALOG SECTION

The analog section of the Analog PCA contains the resistors, relays, and cabling to provide the appropriate

stimulus. There are three special concerns that need to be addressed with the printed circuit board layout, cabling, and relays: thermal EMFs, contact resistance, and leakage.

Thermal EMFs are small voltages (in the microvolt region) that are generated when dissimilar metals are placed together and the temperature is elevated (similar to the effect of a thermocouple). In the lower resistance values, where measurement voltage levels are small, the effect of thermal EMFs on total measurement uncertainty can become significant. EMFs are combated in the 5450A via several techniques. First, when possible, similar metals are used in critical areas. For example, the front panel terminals and lugs are both made of similar, low thermal alloy to minimize thermal EMFs. Secondly, to reduce the temperature gradient in the relays, the energization voltages are reduced once the relay is once actuated.

Contact resistance inside the relays can be significant for 2-wire units under test (UUTs). Generally, the effect of contact resistance is minimal for 4-wire resistance measurements because the input impedance of the measuring device is high on the sense leads, and the source leads typically carry constant current. Since the contact resistance of the 5450A is compensated in the 2-wire configuration (by the 2 WIRE COMP feature), repeatability is the primary concern. To minimize contact resistance and maximize repeatability, in most signal paths, the relays are high-quality, gold-plated, cross-bar type relays with paralleled contacts.

Another consideration of the analog connection circuitry is that of leakage. Leakage is the parasitic resistance between the high and low leads of a connection due to the resistance of dielectric materials such as the printed circuit board itself. The effects of leakage are increased in

high-humidity environments. The 5450A specifications allow for this effect. In addition, in the higher resistance ranges, reed-type relays are used to greatly reduce the leakage. The reed-type relays offer higher levels of thermal EMFs and contact resistance than the telephone-type relays used in the lower resistance ranges. However, the effects of thermal EMFs and contact resistances are minimized since typical UUTs measure higher resistances with commensurately higher voltages.

The other leakage reduction techniques used include the use of Teflon standoffs and hand wiring in critical areas. Because of leakage, it is important that the special handling outlined in the service section of this manual be strictly observed.

The resistors used in the 5450A are of the highest quality. In most cases, several resistors (two or four) are matched by TC (temperature coefficient) in equal and opposite amounts to allow a lower temperature coefficient for the value. The primary resistances of the 5450A are achieved by a "chain" of resistors. For example, to achieve 19 $\Omega$ , a 9 $\Omega$  resistor (actually four 36 $\Omega$  resistors in parallel) is connected in series with a 10 $\Omega$  resistor (actually four 40 $\Omega$  resistors in parallel). 100 $\Omega$  is achieved by putting a 81 $\Omega$  resistor in series with the previous two, and so on. The exceptions to this in the 5450A are the 1 $\Omega$  and 1.9 $\Omega$  resistors, which are not chained together or used in conjunction with higher resistances.

Another function of the 5450A Analog PCA is to program the external guard resistor. This low-accuracy  $100\Omega$  resistor ties OUTPUT LO terminal to the GUARD terminal during normal operation. In the event that external guard is requested (by the EXT GUARD switch), K34 is energized to disconnect this resistor. The resistors that are called to generate any particular configuration are shown in Table 3-1.

Table 3-1. Relay Truth Table

K1  K2  K3  K4  K5  K6  K7  K8  K9  K10  K11  K12  K13  K14  K12  K13  K14  K15  K16  K17  K18  K14  K15  K16  K17  K18  K19  K20  K21  K22  K23																				
K1       K2         K3       K4         K5       K6         K7       K8         K9       K10         K11       K12         K13       K14         K15       K16         K17       K18         K19       K20         K22       K23		HORT	Ohm	9 Ohm	Ohm O	9 Ohm	00 Ohm	30 Ohm	KOhm	9 KOhm	) KOhm	9 KOhm	00 KOhm	30 KOhm	MOhm	9 MOhm	) MOhm	9 MOhm	00 MOhm	XT GUARD
K1       K2         K3       K4         K5       K6         K7       K8         K9       K10         K11       K12         K13       K14         K15       K16         K17       K18         K19       K20         K22       K23		S	-	÷.	7	16	1(	16	-	÷	1(	ì	$ \tilde{-} $	F	-	-	F	iL	F	Ш
K8         K9         K10         K11         K12         K13         K14         K15         K16         K17         K18         K20         K21         K22	K1																			
K8         K9         K10         K11         K12         K13         K14         K15         K16         K17         K18         K20         K21         K22	K2																			
K8         K9         K10         K11         K12         K13         K14         K15         K16         K17         K18         K20         K21         K22	K3																			
K8         K9         K10         K11         K12         K13         K14         K15         K16         K17         K18         K20         K21         K22	K4																			
K8         K9         K10         K11         K12         K13         K14         K15         K16         K17         K18         K20         K21         K22         K23	K5																			Щ
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K31 K32	K32									Γ										П
K33	K33									Γ		Г								
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BLACK AREA INDICATES ENERGIZED RELAY

	3.	

J

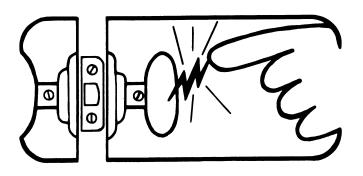


# static awareness



A Message From

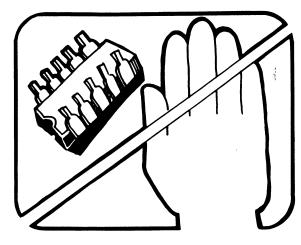
John Fluke Mfg. Co., Inc.



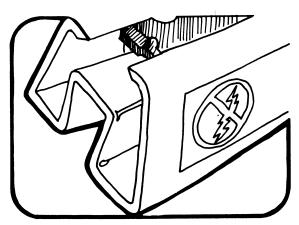
Some semiconductors and custom IC's can be damaged by electrostatic discharge during handling. This notice explains how you can minimize the chances of destroying such devices by:

- 1. Knowing that there is a problem.
- 2. Learning the guidelines for handling them.
- 3. Using the procedures, and packaging and bench techniques that are recommended.

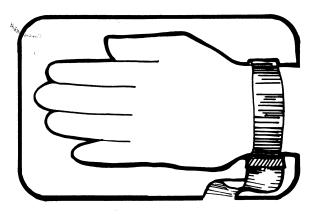
The following practices should be followed to minimize damage to S.S. devices.



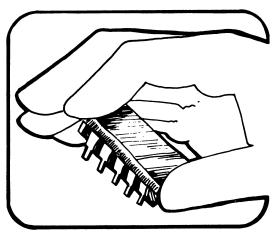
1. MINIMIZE HANDLING



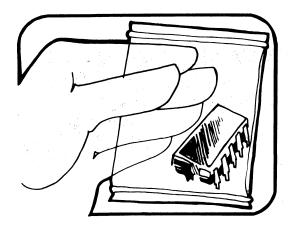
2. KEEP PARTS IN ORIGINAL CONTAINERS UNTIL READY FOR USE.



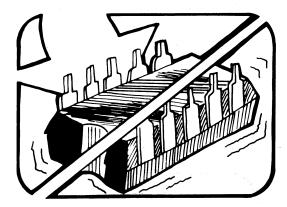
3. DISCHARGE PERSONAL STATIC BEFORE HANDLING DEVICES. USE A HIGH RESISTANCE GROUNDING WRIST STRAP.



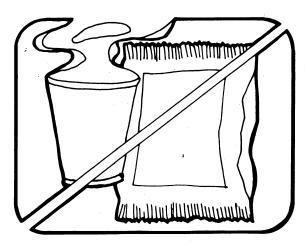
4. HANDLE S.S. DEVICES BY THE BODY



5. USE STATIC SHIELDING CONTAINERS FOR HANDLING AND TRANSPORT

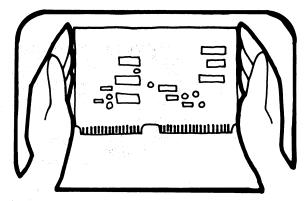


6. DO NOT SLIDE S.S. DEVICES OVER ANY SURFACE

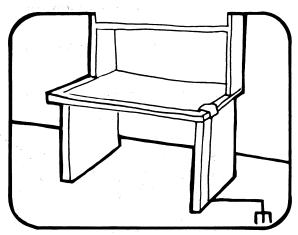


7. AVOID PLASTIC, VINYL AND STYROFOAM® IN WORK AREA

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AND GENERAL DYNAMICS, POMONA DIV.



8. WHEN REMOVING PLUG-IN ASSEMBLIES, HANDLE ONLY BY NON-CONDUCTIVE EDGES AND NEVER TOUCH OPEN EDGE CONNECTOR EXCEPT AT STATIC-FREE WORK STATION. PLACING SHORTING STRIPS ON EDGE CONNECTOR HELPS TO PROTECT INSTALLED SS DEVICES.



- 6. HANDLE S.S. DEVICES ONLY AT A STATIC-FREE WORK STATION
- 10. ONLY ANTI-STATIC TYPE SOLDER-SUCKERS SHOULD BE USED.
- 11. ONLY GROUNDED TIP SOLDERING IRONS SHOULD BE USED.

A complete line of static shielding bags and accessories is available from Fluke Parts Department, Telephone 800-526-4731 or write to:

JOHN FLUKE MFG. CO., INC. PARTS DEPT. M/S 86 9028 EVERGREEN WAY EVERETT, WA 98204

## Section 4 Maintenance

#### WARNING

THESE SERVICE INSTRUCTIONS ARE FOR USE BY QUALIFIED PERSONNEL ONLY. TO AVOID ELECTRIC SHOCK, DO NOT PERFORM ANY SERVICING OTHER THAN THAT CONTAINED IN THE OPERATING INSTRUCTIONS UNLESS YOU ARE QUALIFIED TO DO SO.

#### 4-1. INTRODUCTION

This section contains information regarding the maintenance of the 5450A Resistance Calibrator. The section includes routine maintenance, disassembly, performance test/calibration, and troubleshooting.

A 90-day or one-year calibration cycle may be used depending on the specifications necessary for the particular application of the 5450A. The test equipment required for the calibration procedure is given in Table 4-1. Test equipment with equivalent specifications may also be used. To aid in troubleshooting, a DMM (such as a Fluke 77) and a logic probe are also recommended.

Suggested cables to interconnect the test equipment during calibration are listed in Table 4-2. As many of the cables are non-standard, the table includes construction details.

#### 4-2. SERVICE INFORMATION

The 5450A is warranted for a period of one year upon shipment of the instrument to the original purchaser. Conditions of the warranty are given in the front of this manual. Malfunctions that occur within the limits of the warranty will be corrected at no cost to the user. For inwarranty repair, call the Fluke Technical Service Center designated to service your area. A complete list of Technical Service Centers is given in Section 7.

Instruments returned for repair or calibration should be shipped via United Parcel Service or "Best Way" prepaid. The instrument should be shipped in the original packing carton, or if it is not available, in any suitable container that is rigid and of adequate size. If a substitute container is used, the instrument should be wrapped in paper and surrounded with at least four inches of excelsior or similar shock-absorbing material. Dated proof-of-purchase may be required for in-warranty repairs. If requested, an estimate will be provided before work is begun on instruments that are beyond the warranty period.

#### 4-3. GENERAL MAINTENANCE

#### 4-4. Setting Line Voltage

#### **WARNING**

TO AVOID SHOCK HAZARD, PERFORM THE VOLTAGE SELECTION PROCEDURE EXACTLY AS PRESENTED. LINE POWER VOLTAGE MAY BE PRESENT IF THE PROCEDURE IS NOT FOLLOWED EXACTLY.

If the line voltage set at the factory is incorrect for your application, it may be reset to 100, 120, 220, or 240V ac. To reset the power supply to a new voltage:

1. Ensure that the instrument is disconnected from all power sources.

- 2. Remove the top cover by removing the six Philips head screws on the top of the instrument and lifting the cover free of the instrument.
- 3. Remove the power supply cover at the left rear of the instrument by loosening the four Philips heads screws securing the top cover to the remainder of the power supply chassis. Lift the cover free of the instrument.
- 4. Two slide switches located on the PCA are used to select the desired line voltage. Figure 4-1 shows the switch settings for the four possible input line voltages. Set the switches using the dot and slot as pictured.

- 5. Reinstall the power supply cover and the instrument top cover.
- 6. It is also important to change the rear panel indication of voltage to prevent a future user from connecting the instrument to the incorrect voltage. The existing mark can be removed with toluene.
- 7. After the line voltage settings are changed, ensure that the correct fuse is installed for the setting selected. For 100 or 120V ac, use a sloblo 1/2-amp fuse. For 220 or 240V ac, use a sloblo 1/4-amp fuse. The following section describes how to change the line fuse.

Table 4-1. Required Test Equipment

EQUIPMENT TYPE	REQUIRED SPECIFICATIONS	RECOMMENDED MODEL
DC Voltage Calibrator (Source 2)	Linearity: 2.5 ppm from 0.1V to 0.2V 0.8 ppm from 5V to 10V 0.2 ppm for a 0.05% change in output at 1V, 5V, 10V, and 19V	Fluke 5440A
	Short-Term Stability (1 min) and Noise (0.1 Hz to 10 Hz): 0.2 $\mu$ V from 0.1V to 1V 1.0 $\mu$ V from 5V to 10V 2 $\mu$ V at 19V	
	Output Voltage Range: 0.1V to 19V (Note 1)	
DC Voltage/Current Source (Source 1)	Short-Term Stability (1 min) and Noise (0.1 Hz to 10 Hz): 0.25 ppm from 5V to 19V 0.5 ppm at 10 mA 2 ppm at 70 mA and 100 mA Output Uncertainty: 0.05% at 500 mA	Fluke 5100B
	Voltages used: 5V to 19V (Note 1) Current used: 10 mA to 500 mA (Note 1)	
Precision Divider	Ratio Accuracy (at 10:1): 0.2 ppm with up to 19V input Input Resistance: ≥100 kΩ	Fluke 752A
Null Detector	Short-Term Stability (1 min) and Noise (0.1 Hz to 10 Hz): 0.2 $\mu$ V on 3 $\mu$ V range	Fluke 845AB
	Input Resistance: ≥5x10¹² HI input to Ground ≥2x10¹¹ LO input to Ground	
10 kΩ Standard Resistor	Absolute Uncertainty: ≤2 ppm	ESI SR104
1 Ω Standard Resistor	Absolute Uncertainty: ≤5 ppm	Guildline 933 0/1

NOTE 1: For sources with a 10V range but no 20V range, the calibration can be performed with 10V instead of 19V.

#### **Table 4-2. Required Cables**

#### CABLES NEEDED:

2 Each:

1-meter (36-inch) cable with a copper spade lug on each end of the center conductor and a 15-cm (6-inch) shield wire on each end terminated with a banana plug.

1 Each:

1.25-meter (48-inch) cable same as above.

2 Each:

50-cm (18-inch) cable same as above.

1 Each:

50-cm (18-inch) cable same as above except on one end use a solid copper insulated alligator clip or a copper circuit-hook type clip instead of a spade lug. Beware of using copper-plated steel alligator clips; test the clip by filing it to ensure it is solid copper.

#### MATERIAL NEEDED FOR CONSTRUCTION:

Cable: Coaxial cable RG58U or RG58A/U, 4.25 meters (14.5 feet).

Connectors: 11 copper or gold-plated copper spade lugs (Fluke P/N 271718).

12 banana plugs

#### **CONSTRUCTION HINTS:**

After soldering the spade lugs on the cable's center conductor, use heat-shrink tubing to cover the spade-lug solder connection and exposed cable conductor. Add a stranded 22-gauge or larger wire for the 15-cm (6-inch) shield extension wire.

#### ADDITIONAL CABLES NEEDED:

2 Each:

1-meter (36-inch) dual-banana to dual-banana shielded cable. Available from Pomona Electronics as part number 1167-36.

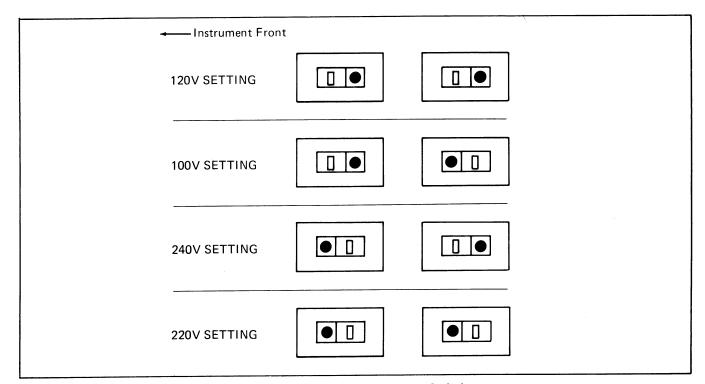


Figure 4-1. Line Voltage Selection Switches

#### 4-5. AC Line Power Fuse Replacement

#### WARNING

TO AVOID SHOCK HAZARD, PERFORM THE FUSE REPLACEMENT PROCEDURE EXACTLY AS PRESENTED. LINE POWER VOLTAGE MAY BE PRESENT IF THE PROCEDURE IS NOT FOLLOWED EXACTLY.

Ensure that the new fuse is the proper type for the line voltage being used. Fuse types according to line voltage are as follows:

For 100 or 120V ac, use slo-blo 1/2A For 220 or 240V ac, use slo-blo 1/4A

To replace the line power fuse, use the following procedure:

- 1. On the front panel of the 5450A, push the POWER switch to the off (out) position.
- 2. Disconnect the 5450A power cord from line power.
- 3. On the rear panel of the 5450A, locate the fuse holder in the upper right corner of the rear panel.
- 4. Turn the fuse holder about 1/4-turn counterclockwise.
- 5. Pull the fuse holder from the 5450A. The fuse is attached to the fuse holder.
- 6. Place a new fuse in the fuse holder.
- 7. Insert the fuse and fuse holder back into the rear panel.
- 8. Push the fuse holder in as far as it will go and turn it about 1/4 turn clockwise.
- 9. Connect the line power cord to line power.
- 10. On the front panel of the 5450A, push the POWER switch to the on (in) position. If the line power fuse blows when power is on, the instrument requires service; contact your nearest Fluke service center (a complete list of service centers is given in Section 7 of this manual).

#### 4-6. Cleaning

Use the following procedure periodically to clean the 5450A of dust, grease, and other contamination:

- 1. Clean the front panel and case with a soft cloth dampened with a mild solution of detergent and water.
- 2. Clean the surface of each PCA using clean dry air at low pressure (<20 psi). If grease is encountered, spray the PCA with SPRAYON T.M.C Cleaner (rosin flux remover) or equivalent and remove grime with dry, low-pressure air.

#### CAUTION

Rosin flux remover can damage plastic parts such as the relays and front panel lens. Use caution to avoid damage. Remove relays from sockets before applying to Analog PCA.

#### 4-7. Handling Precautions

4-8. STATIC-SENSITIVE DEVICES

#### **CAUTION**



This instrument contains CMOS components which can be damaged by static discharge. Static-sensitive components in the instrument are indicated on the parts list by the symbol above. To prevent damage, take the following precautions when troubleshooting and/or repairing the instrument:

- Perform all work at a static-free work station.
- Do not handle components or PCAs by their connectors.
- Wear static ground straps.
- Use conductive foam to store components.
- Remove all plastic, vinyl, and styrofoam from the work area.
- Use a grounded, temperature-regulated soldering iron.

#### 4-9. LEAKAGE-SENSITIVE HANDLING

The Analog PCA is sensitive to dirt and contamination. Consequently, all efforts must be made to prevent handling this assembly. If the PCA must be handled, hold it only by the edges. Be very careful to keep fingers away from the PCA in the relay and resistor area. If a component that is to be replaced requires soldering, use the following special precautions:

Use a mildly activated rosin-core solder if available.

After any soldering/desoldering activity, immediately clean the solder joint with Freon T.F.

Degreaser. Hold the PCA so the Freon drips off the nearest edge. Do not let it drip over the resistor and relay area any more than is unavoidable.

#### 4-10. Replacement of Selected Components

Some of the resistors used in the 5450A are selected to meet the required temperature coefficient specifications of the instrument. For example, there are four 40Q resistors used to generate the 10Q resistance, four 36Q resistors used in conjunction with the 10Q set to generate 19Q, and so on. These resistors are carefully matched with ones of similar resistance, but with equal and opposite temperature coefficients, to meet the rigid requirements of the 5450A specifications. Should a resistor need replacement, order a set for the specific value to ensure that the instrument specifications are not degraded.

#### 4-11. Disassembly and Reassembly

#### 4-12. INTRODUCTION

Each of the following procedures deals with the removal/replacement of a single part or printed circuit assembly (PCA) from the instrument. The procedures are listed in a disassembly sequence; however, all steps may not be required to obtain access to the desired part or assembly of the instrument. Reassembly can be accomplished by performing the steps of the applicable procedure in a logically reversed sequence.

#### 4-13. TOP/BOTTOM COVER

Use the following procedure to remove the top and/or bottom cover. (To access the bottom cover, place the instrument upside down.)

- Remove the three screws across the front edge of the cover.
- 2. Remove the three screws across the rear edge of the cover.
- 3. Lift the cover straight up, guiding it out of the grooves in the side panels.

#### 4-14. TOP ANALOG GUARD COVER

Use the following procedure to remove the top analog guard cover from the analog section of the instrument. Prior to starting, the top cover must be removed from the instrument.

- 1. Remove the two screws at the front of the top analog guard cover securing the cover to the guard chassis.
- Using the finger hole located in the front center
  of the guard cover, slide the cover toward the
  rear of the instrument, then lift up the front of
  the guard chassis top to a position above the

front of the instrument and slide the cover forward to disengage the tabs at the rear of the guard chassis.

#### 4-15. INNER ANALOG GUARD COVER

Use the following procedure to remove the inner analog guard cover from the  $1\Omega$  and  $1.9\Omega$  resistors in the analog section. Prior to starting, the top cover and top analog guard cover must be removed.

- 1. Remove the two screws at the top corners of the top analog guard cover securing the cover to the Analog PCA.
- Lift the cover up and away from the Analog PCA.

#### 4-16. FRONT PANEL DISPLAY ASSEMBLY

Use the following procedure to remove the Display Printed Circuit Assembly (PCA). The front panel must be removed to gain access to the Display PCA and its components. To remove the two screws mentioned below, the top cover and top analog guard cover must be removed. The memory assembly must also be hinged up and out of the way.

- 1. Remove the two screws located on the rear upper corners of the front panel. These screws are located inside the front metal frame.
- 2. Using both hands, squeeze the top and bottom of the panels together to clear the tabs holding the panel in place.
- 3. Once the panel is clear of the tabs, move the panel forward to clear the tabs.
- 4. Remove the wires connecting the front panel input connectors to the Analog PCA using an 11/32-inch open-end wrench.

#### **NOTE**

To facilitate the replacement of wires on terminals during assembly, the color codes are molded into the panel next to each terminal. Wires must be positioned so that adjacent sleeving does not touch.

- 5. Disconnect the ribbon cable connecting the Display PCA to J11 on the Digital PCA.
- 6. Remove the four screws securing the Display PCA to the frame and lift the PCA clear of the instrument.

#### 4-17. ANALOG ASSEMBLY

#### **WARNING**

TO AVOID SHOCK HAZARD, ENSURE THAT POWER HAS BEEN REMOVED FROM THE INSTRUMENT PRIOR TO WORKING IN THE TRANSFORMER AND POWER SUPPLY AREA.

Use the following procedure to remove the Analog Printed Circuit Assembly (PCA). Prior to starting, the top cover, the top analog guard cover, the power supply cover, and the front panel must be removed.

- 1. Unplug the cables connecting to J1, J2, J3, and J4 of the analog assembly. Route the cables through the opening in the bulkhead into the power supply compartment.
- 2. Unplug the ribbon cable at the rear of the Analog PCA connected to the VR1 connecter.
- 3. About 3 inches from the front and 3-1/2 inches from the left of the PCA, remove a nut connecting a black wire to the Analog PCA.
- 4. Remove the seven screws holding the Analog PCA to the guard chassis. One screw is located near each corner, one is on the middle of each of the left and right sides, and one is located in the center of the PCA. Note that a green wire is attached to the screw in the front right corner and also the right rear corner.
- 5. Lift up the front of the PCA, with the left side raised slightly higher than the right. Slide the PCA forward and to the left until it is free of the chassis.
- 4-18. TRANSFORMER AND POWER SUPPLY ASSEMBLY

#### **WARNING**

TO AVOID SHOCK HAZARD, ENSURE THAT POWER HAS BEEN REMOVED FROM THE INSTRUMENT PRIOR TO WORKING IN THE TRANSFORMER AND POWER SUPPLY AREA.

Use the following procedure to remove the transformer and Power Supply Printed Circuit Assembly (PCA) from the instrument. Prior to starting, the top cover, the bottom cover, and the top analog guard cover must be removed from the instrument.

1. Loosen the four retaining screws and remove the cover from the transformer compartment.

(The transformer compartment, which houses the Power Supply PCA and power transformer, is located at the left rear of the instrument.)

- 2. Disconnect the cable clamp on the front of the transformer compartment.
- 3. Disconnect the green and red wires connecting to TP1 and TP2 of the Power Supply PCA. The green and red wires are housed in a cable; pull this cable through the grommet at the rear of the transformer compartment.
- 4. Disconnect the wire connected to J4 on the Analog PCA. Pull this wire through the bulkhead and out the grommet at the rear of the transformer compartment.
- 5. A cable from the Analog PCA terminates within the transformer compartment. Bring this cable back through the center bulkhead and into the transformer compartment.
- 6. Remove the six screws attaching the transformer compartment to the frame. Four are used on the outside frame and two on the center frame. This process is slightly easier if K15 is temporarily unplugged from the Analog PCA.
- 7. Lift the transformer compartment containing the transformer and Power Supply PCA straight up and out of the instrument compartment.

#### 4-19. MEMORY ASSEMBLY

Remove the Memory Printed Circuit Assembly (PCA) from the instrument compartment using the following procedure. Prior to removing the Memory PCA, remove the top cover.

- 1. Remove the two screws at the corners of the Memory PCA that are near the center bulkhead of the instrument.
- 2. At this time the Memory PCA may be hinged up to facilitate access to the underside of itself or the Digital PCA below. To completely remove the Memory PCA, continue with the following steps.
- 3. Unplug the cables connected to J1, J2, and J3.
- 4. Remove the two screws attaching the hinges to the outside frame.
- 5. Lift the Memory PCA clear from the 5450A.

#### 4-20. DIGITAL ASSEMBLY

Use the following procedure to remove the Digital Printed Circuit Assembly (PCA) from the instrument compartment. Prior to starting, the top and bottom covers must be removed from the instrument.

- 1. Remove the three screws holding C201 to the outside frame.
- 2. Remove the two hex-head bolts and nuts securing the IEEE-488 connector to the rear panel.
- 3. Disconnect the green input line-power ground wire from the lug on the rear of the Digital PCA (P30).
- 4. Disconnect the four cables that are plugged into the top of the Digital PCA. (Two go to the Memory PCA, one to the Display PCA, and one to the Analog PCA.)
- 5. Remove the nut from the screw along the center bulkhead securing the ground wire. Lift the wire from the screw.
- 6. From below, remove the five screws securing the Digital PCA to the tabs in the instrument compartment.
- 7. Lift up the front of the Digital PCA and slide it forward and out of the instrument compartment.

#### 4-21. BOTTOM ANALOG GUARD COVER

Use the following procedure to remove the bottom analog guard cover from the analog section. Prior to starting, the bottom cover, top cover, and top analog guard cover must first be removed from the instrument.

- 1. Remove the screw from the center of the Analog PCA. (The screw is accessible from the top of the instrument.)
- 2. Turn the instrument so that the top side is facing downward.
- 3. Remove the six screws holding the bottom analog guard cover to the instrument guard chassis.
- 4. Using the finger hole in the bottom analog guard cover, lift the cover up and away from the 5450A chassis.

## 4-22. PERFORMANCE TEST/CALIBRATION PROCEDURE

#### 4-23. Introduction

Since the calibration procedure and performance verification test require virtually the same procedure, the

two are presented together. (Even the CALIBRATION switch is in the ENABLE position for both.) The only difference is that, in the calibration procedure, new calibration constants are entered into the instrument. In the performance verification test, calibration constants are not entered, even though the CALIBRATION switch is in the ENABLE position.

Two features make the 5450A easy to calibrate. First, the 5450A is calibrated by storing calibration constants in EAROM (electrically alterable read only memory). Consequently, calibration does not require that the covers be removed from the instrument. Second, the 5450A has been designed so that it can be calibrated without heavy dependence on special equipment and standards. The necessary equipment is shown in Table 4-1.

To enter calibration constants, the 5450A is used just as if calibrating a UUT. The UUT reading is entered by pressing the ENTRY MODE switch, then the numeric switches, and then the ENTER switch (at which time the ERROR light turns on). However, when the UUT reading is entered while the CALIBRATION switch is in the ENABLE position, the 5450A EAROM is automatically programmed with the entered value. This value then becomes the number that is displayed when that particular resistance is requested during normal operation. Similarly, when a value is entered for a SHORT while 2 WIRE COMP is enabled, the value becomes the new 2 WIRE COMP offset. To completely calibrate the 5450A, it is necessary to enter data for 17 resistors  $(1\Omega-100 \text{ M}\Omega \text{ and } 1.9\Omega-19 \text{ M}\Omega)$  and a short.

To support the calibration procedure that is presented in this manual, the 5450A uses special internal configurations. These configurations are selected when the CALIBRATION switch is in the ENABLE position and the rear panel SP CAL/NORMAL switch is in the NORMAL position. The configurations are indicated in the display by CAL, CAL1, CAL2, 4W 0CAL, or 2W 0CAL when the instrument is in the OUTPUT mode.

The 5450A requires a calibration environment of 23°C±1.5°C (70.7°F-76.1°F) and less than 70% RH. The calibration procedure goes faster if a calculator with a minimum resolution and accuracy of eight digits is used for the necessary calculations.

#### 4-24. Calibration Philosophy

#### 4-25. TRANSFER OF STANDARDS

The 5450A is calibrated by transferring the accuracy of two external standard resistors to the 5450A. A  $1\Omega$  standard is used to calibrate the 5450A's  $1\Omega$  and  $1.9\Omega$  resistors, and a  $10~k\Omega$  standard is used to calibrate the 5450A's  $10~k\Omega$  and  $19~k\Omega$  resistors. The setup used for this transfer is shown in Figure 4-2.

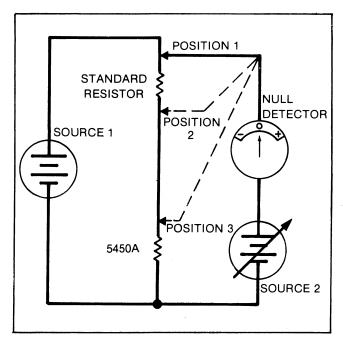


Figure 4-2. Transfer of Standard Resistance

Source 1 creates a voltage across the standard resistor and the 5450A. When calibrating the  $1\Omega$  and  $1.9\Omega$  resistors, Source 1 is set to a constant current. When calibrating the  $10~k\Omega$  and  $19~k\Omega$  resistors, Source 1 is set to a constant voltage. The accuracy of Source 1 is not important; however, Source 1 must be stable and have low noise content.

Source 2 is used in conjunction with the Null Detector as a highly accurate voltmeter to measure the ratio of the voltage divider created by the standard resistor and the 5450A. The excellent linearity of Source 2 assures an accurate transfer of the standards.

The Null Detector is first connected to the top of the standard resistor (position 1), and is zeroed by adjusting the voltage of Source 2. The Null Detector is then connected to the bottom of the standard resistor (position 2), and is zeroed by adjusting the output voltage of Source 2. Finally, the Null Detector is connected to the top of the 5450A resistor (position 3), and is zeroed by adjusting the output of Source 2.

The three voltages measured in the above paragraph are then used in conjunction with the known value of the standard resistor to compute the value of the 5450A resistor. The absolute accuracy of Source 2 is unimportant, but a high accuracy of linearity is required. The above steps are repeated for the 5450A's  $1\Omega$ ,  $1.9\Omega$ , 10 k $\Omega$ , and 19 k $\Omega$  resistors.

#### 4-26. RATIO CALIBRATION

Once the values of the 10 k $\Omega$  and 19 k $\Omega$  resistors are known, a ratio technique is used to obtain the values of

the remaining resistors in the 5450A. The  $10~k\Omega$  value is ratioed up to the  $100~k\Omega$ ,  $1~M\Omega$ ,  $10~M\Omega$ , and  $100~M\Omega$  ranges, and down to the  $1~k\Omega$ ,  $100\Omega$  and  $10\Omega$  ranges. The  $19~k\Omega$  value is ratioed up to the  $190~k\Omega$ ,  $1.9~M\Omega$ , and  $19~M\Omega$  ranges, and down to the  $1.9~k\Omega$ ,  $190\Omega$ , and  $19\Omega$  resistors.

To accommodate this ratio technique, the 5450A uses special internal configurations that connect its internal resistors as a ratio divider. These special configurations are automatically selected when the rear panel NORMAL/SP CAL switch is in the NORMAL position and the 5450A rear panel CALIBRATION switch is in the ENABLE position.

The 5450A SENSE terminals are switched between the upper and lower taps of the resistor divider using the front panel up-arrow and down-arrow switches. (See Figure 4-3.) To sense the upper tap (CAL1), press the up-arrow switch; the 5450A will display "CAL1". To sense the lower tap (CAL2), press the down-arrow switch; the 5450A will display "CAL2". While ratioing up, the 5450A displays the cardinal value of the combined resistor value (10X). While ratioing down, the 5450A displays the cardinal value of the lower decade resistor (X).

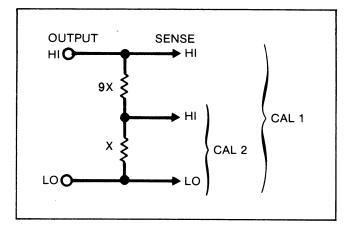


Figure 4-3. Internal Ratio Divider

The configuration for ratio calibration is shown in Figure 4-4. Initially, Source 1 is set to provide a constant voltage or current, depending on the range. The 5450A is set to a non-divided output (CAL1), and the Reference Divider is connected to provide a non-divided output. Source 1 is set to an appropriate voltage or current, and then Source 2 is adjusted to zero the Null Detector. The 5450A is then set to a divided output (CAL2) by pressing the down-arrow switch, and the Reference Divider is connected to provide a divided output of 10:1. The Null Detector is then zeroed by adjusting Source 2. In this case the first and second settings of Source 2 are very near one another.

The two voltage measurements are used to calculate the actual 5450A divider ratio. This divider ratio is in turn

applied to the known upper or lower resistance (depending on whether the ratio is made in an upward or downward direction) to determine the unknown resistance value. This procedure is applied upward and downward from both the  $10\,\mathrm{k}\Omega$  and  $19\,\mathrm{k}\Omega$  cardinal points until the remainder of the calibration points are known.

#### 4-27. CALIBRATING THE SHORT

The 5450A SHORT may be accessed in either a 2-wire or 4-wire measurement configuration. The 4-wire access is accurate enough that it is not calibrated; instead, it is specified from nominally zero ohms.

The 2-wire access is calibrated in the 2-wire configuration. This configuration uses only the OUTPUT terminals of the 5450A and includes the lead and contact resistance within the 5450A. The calibrated resistance of the access to the 2-wire short is algebraically added to the displayed output when the 2 WIRE COMP switch is enabled (LED on).

The 2-wire access to the short is calibrated by applying a known constant current to the OUTPUT terminals. The voltage is then measured at the OUTPUT terminals, and the resistance is calculated by applying Ohm's law.

The 2-wire access to the short may optionally be calibrated with a cabling of the user's choice, using either the front panel or rear panel terminals. If this approach is used, the ultimate user of the instrument must be advised that errors will occur if the same cable is not used when calibrating 2-wire UUTs.

#### 4-28. Calibration Procedure

4-29. SETUP

Before beginning the 5450A calibration procedure, obtain a copy or make a functional duplicate of the Calibration Worksheet shown in Table 4-3. Enter the date and serial number of the 5450A.

Ensure that the environment is at 23°C±1.5°C (70.7°F-76.1°F) and less than 70% RH, and that the required calibration equipment is ready. The 5450A requires a 2-hour warm-up time. Ensure that the other required equipment is adequately warmed up and self-calibrations are performed where necessary, such as for the 5440A Direct Voltage Calibrator and 752A Reference Divider. (It is necessary to self-calibrate the 752A only for the 10:1 ratio.) Also zero the offset of the Null Detector. During the calibration procedure, frequently recheck the offset of the Null Detector.

Remove the rear panel calibration sticker of the 5450A and place the CALIBRATION switch in the ENABLE position. Ensure that the rear panel NORMAL/SP CAL switch is placed in the NORMAL position. The front panel CAL LED should light.

To improve calibration results, it is recommended that during the calibration procedure the calibrator wear a grounding wrist strap as is normally used to prevent electrostatic damage to static-sensitive components. Otherwise, static voltage generated by body movement can affect the more-sensitive calibrations, especially at high resistances.

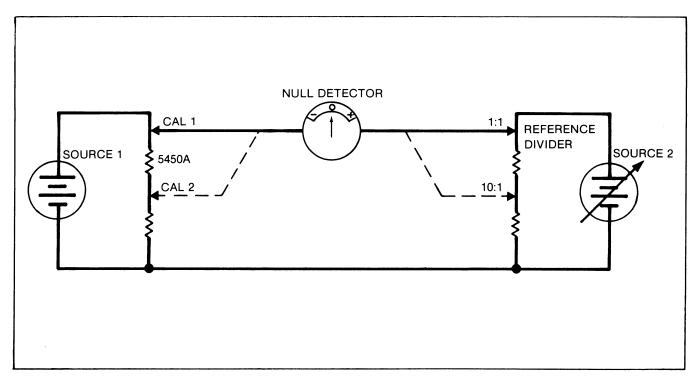


Figure 4-4. Ratio Calibration

Table 4-3. Calibration Worksheet

50A SERIAL NU	JMBER				
5450A RESISTOR	CERTIFIED VALUE OF STANDARD RESISTOR	READING #1	READING #2	READING #3	5450A ACTUAL VALUE
1Ω			-		
1.9Ω					
10 kΩ					-
19 kΩ					-
5450A RESISTOR	REFERENCE VALUE <sup>1</sup>	CAL1 READING	CAL2 READING		5450A ACTUAI VALUE
100 kΩ .	10 kΩ				
1 ΜΩ	100 kΩ				
10 MΩ	1 ΜΩ				
100 ΜΩ	10 MΩ				
190 kΩ	19 kΩ		-		
1.9 ΜΩ	190 kΩ				
19 ΜΩ	1.9 ΜΩ				
1 kΩ	10 kΩ				
100Ω	1 kΩ				ALEXANDER OF THE PROPERTY OF T
1.9 kΩ	19 kΩ				
190Ω	1.9 kΩ				
10Ω	100Ω				
19Ω	190Ω				
		CURRENT	VOLTAGE		5450A ACTUA VALUE
2W 0Ω					

#### 4-30. Connections

The connections necessary for the normal calibration procedure are presented in Figures 4-5, 4-6, and 4-7. Each of these figures show both a connection diagram and a physical layout of the test equipment.

Except where noted, use shielded cables with spade-type connectors for all cabling connections. Spade-type connectors are important because they help keep lead and contact resistance low and consistent. Banana plugs are used where contact integrity is less critical. Also, a cable with a clip on one end is used for the Null Detector input when the connection is changed repeatedly. The clip clips over a small piece of copper wire connected to the appropriate terminal.

#### 4-31. TRANSFER OF STANDARDS

To transfer the accuracy of the external resistance standards to the 5450A, refer to Table 4-4. For each line of Table 4-4, use the following procedure. Items in bold (e.g., **STANDARD RESISTOR**) refer to headings in Table 4-4.

#### NOTE

The EXT GUARD feature of the 5450A should be selected (LED on) during the entire transfer.

If Source 2 is a 5440A, it should be set to the INTERNAL guard mode which connects OUTPUT LO to GUARD through a relay.

- 1. Connect the 5450A as shown in Figure 4-5. Note that the GUARD is connected to GROUND only at Source 2. Initially, connect the Null Detector HI input to the Standard Resistor SENSE HI terminal (shown as connection 1 in Figure 4-5). Use the STANDARD RESISTOR referenced in Table 4-4.
- 2. Enter the actual value of the **STANDARD RESISTOR** into the worksheet.
- 3. Select the **5450A RESISTOR** on the front panel of the 5450A. The 5450A will display the nominal value of the resistor followed by "CAL".
- 4. Ensure that the Null Detector ZERO/OPR switch is set to ZERO.
- 5. Set Source 1 to provide **SOURCE 1 SETTING** output, and set Source 1 to operate.
- 6. Set Source 2 to provide **SOURCE 2 SETTING Reading 1**, and set Source 2 to operate.

- 7. Perform the nulling procedure shown in Table 4-5. (Use the values from Table 4-4 for NULL DETECTOR Sensitivity and NULL DETECTOR Zero Limit.) Record the final Source 2 voltage on the calibration worksheet as READING #1.
- 8. Set Source 2 to provide **SOURCE 2 SETTING Reading 2**. Change the connection at the input to the Null Detector to that shown as connection 2 in Figure 4-5.
- 9. Perform the nulling procedure shown in Table 4-5. Record the final Source 2 voltage on the calibration worksheet as READING #2.
- 10. Change the connection at the input to the Null Detector to that shown as connection 3 in Figure 4-5.
- 11. Perform the nulling procedure shown in Table 4-5. Record the final Source 2 voltage on the calibration worksheet as READING #3.
- 12. Change the connection at the input to the Null Detector to that shown as connection 1 in Figure 4-5. Set Source 2 to the voltage entered as Reading #1 on the worksheet. Set the Null Detector to OPR (operate) and to the NULL DETECTOR Sensitivity range as shown in Table 4-4. Allow time for the reading to settle. If the Null Detector reading differs from zero by more than ALLOWABLE DRIFT, go to the next step. Otherwise, set the Null Detector to ZERO and go to step 19.

#### NOTE

If the reading has changed, it is due to shortterm drift of Source 1. To minimize this drift, be sure that Source 1 has had plenty of warmup time. It helps to keep Source 1 operating as much as possible to keep its internal temperature constant. Errors can be minimized by taking measurements as quickly as possible.

- 13. Adjust Source 2 until the Null Detector reading is again zero within the limits given in **NULL DETECTOR Zero Limit**. Set the Null Detector to ZERO. Record the final Source 2 voltage on the calibration worksheet as the new Reading #1.
- 14. Set Source 2 to the voltage entered as Reading #2 on the worksheet. Change the connection at the input to the Null Detector to that shown as position 2 in Figure 4-5. Set the Null Detector to OPR.

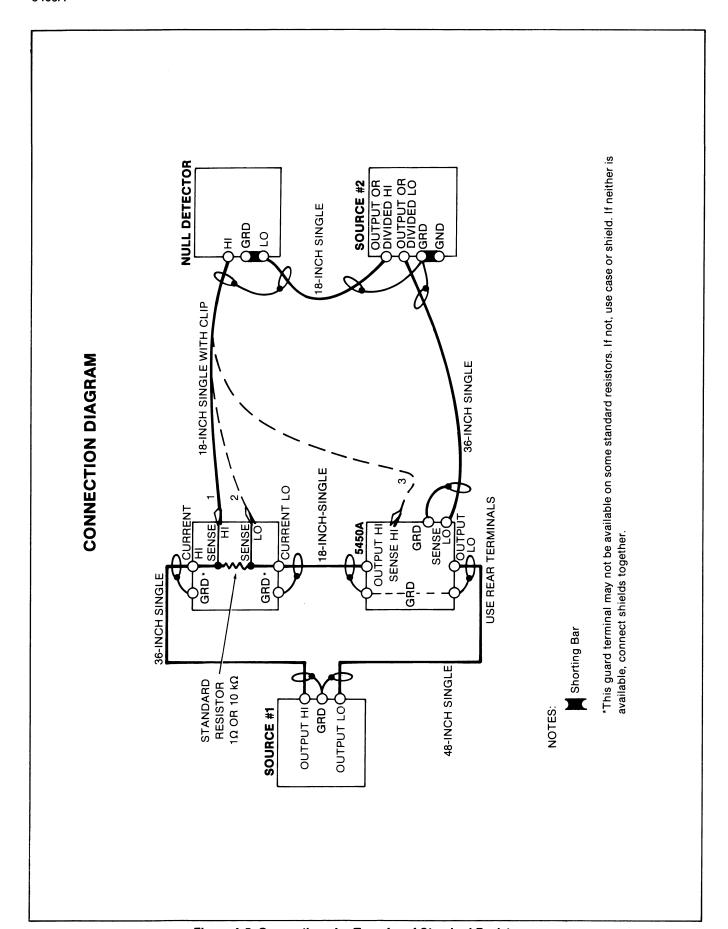


Figure 4-5. Connections for Transfer of Standard Resistance

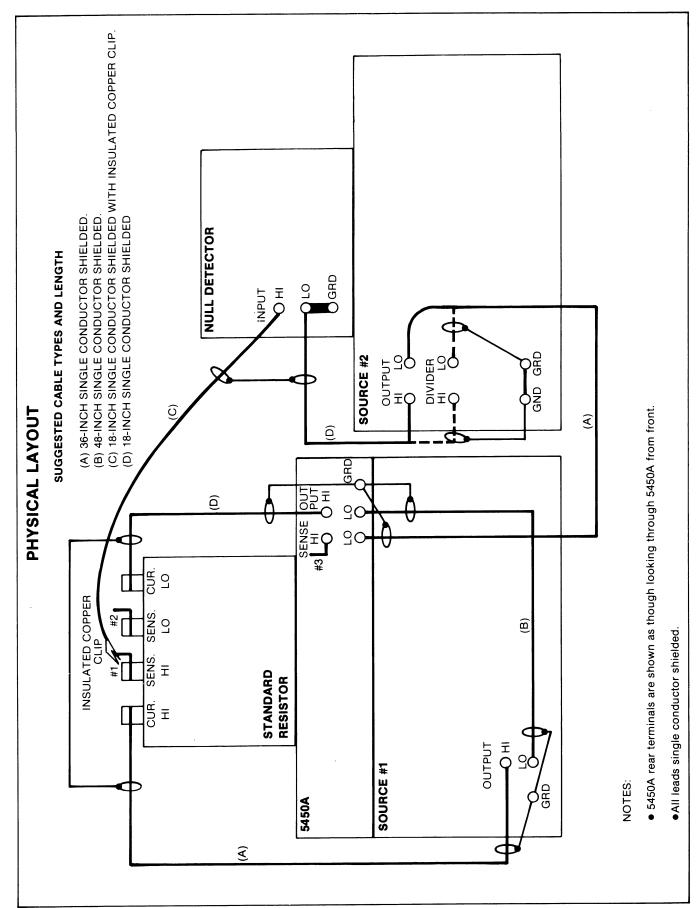


Figure 4-5. Connections for Transfer of Standard Resistance (cont)

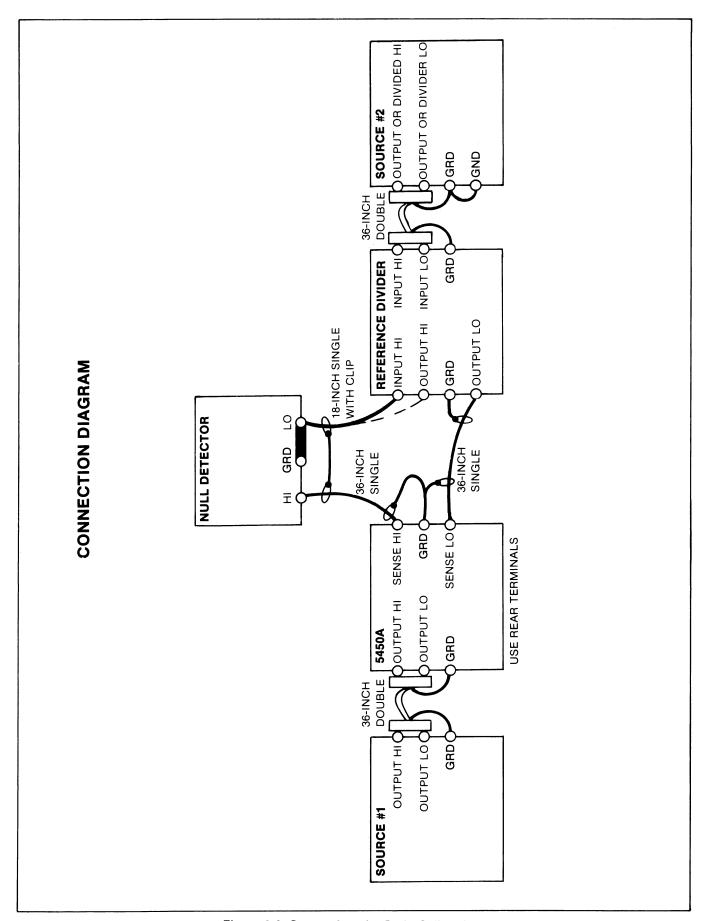


Figure 4-6. Connections for Ratio Calibration

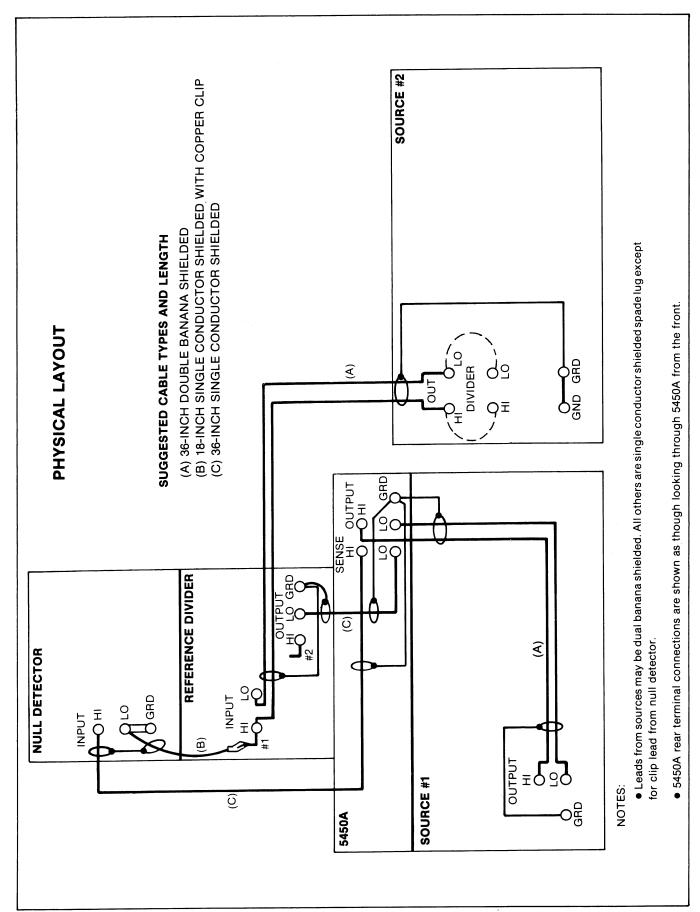


Figure 4-6. Connections for Ratio Calibration (cont)

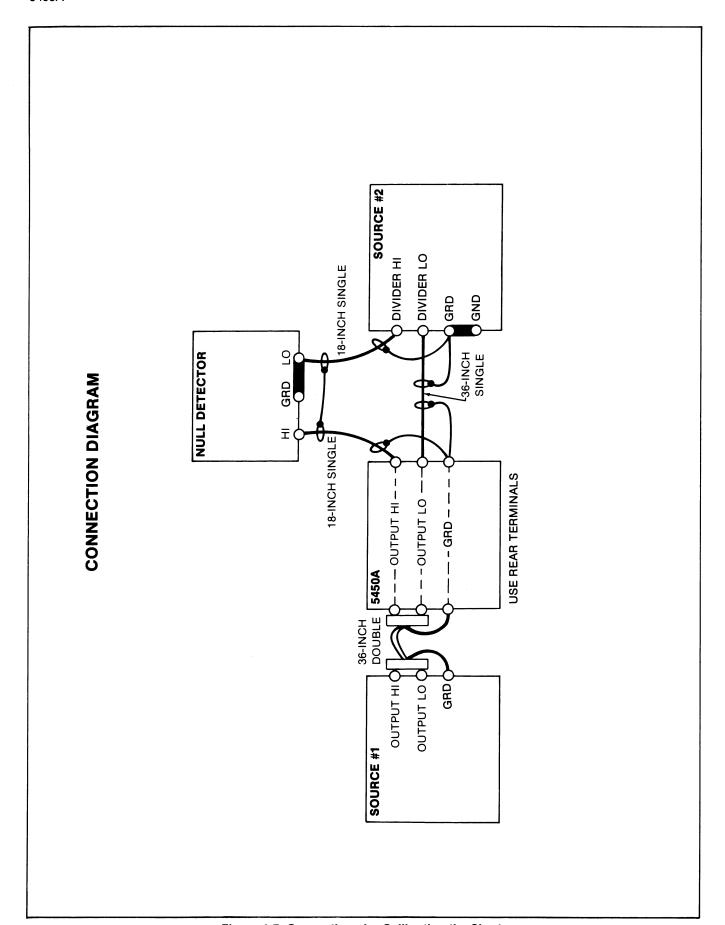


Figure 4-7. Connections for Calibrating the Short

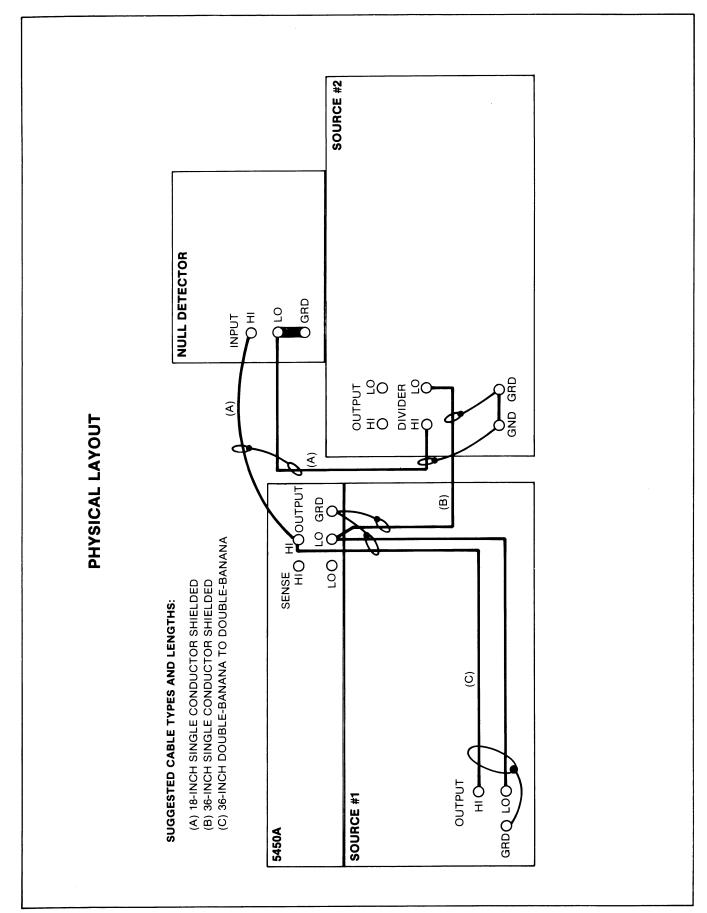


Figure 4-7. Connections for Calibrating the Short (cont)

Table 4-4. Calibration Data for Transfer of Standards

5450A	STANDARD	SOURCE 1	SOURCE 1 SOURCE 2 SETTING		NULL DE	ALLOWABLE	
RESISTOR	RESISTOR	SETTING	Reading 1	Reading 2	Sensitivity Zero Lin		DRIFT
1Ω*	1Ω	100 mA	0.2V	0.1V	3 <i>μ</i> V	0.2 μV	0.4 <i>μ</i> V
1.9Ω*	1Ω	70 mA	0.2V	0.14V	3 <i>μ</i> V	0.2 <i>μ</i> V	0.4 μV
10 kΩ	10 kΩ	10V	10V	5V	10 <i>μ</i> V	1 μV	3 <i>μ</i> V
19 kΩ	10 kΩ	10V	10V	6.552V	10 <i>μ</i> V	1 μV	3 <i>μ</i> V

Table 4-5. Nulling Procedure

STEP	ACTION
1.	Set the Null Detector to the 3 mV range and then to OPR (operate).
2.	Decrement the range switch until the reading on the Null Detector is no longer near zero.
3.	Adjust the output of Source 2 until the Null Detector is again near zero.
4.	Decrease the Null Detector Range and again zero the Null Indicator.
5.	Repeat steps 2 through 4 until the Null Detector <b>NULL DETECTOR Sensitivity</b> range is reached. On that range, adjust Source 2 until the Null Detector zero is obtained within the limits given in <b>NULL DETECTOR Zero Limit</b> .
	NOTE
	The reading on this range may vary back and forth due to noise in the Sources. Under these conditions, adjust Source 2 until the average reading is zero within the limits given.
6.	Set the Null Detector ZERO/OPR switch to ZERO.

- 15. Adjust Source 2 until the Null Detector reading is again zero within the limits given in NULL **DETECTOR Zero Limit.** Set the Null Detector to ZERO. Record the final Source 2 voltage on the calibration worksheet as the new Reading #2.
- 16. Set Source 2 to the voltage entered as Reading #3 on the worksheet. Change the connection at the input to the Null Detector to that shown as position 3 in Figure 4-5. Set the Null Detector to OPR.
- 17. Adjust Source 2 until the Null Detector reading is again zero within the limits given in NULL **DETECTOR Zero Limit.** Set the Null Detector to ZERO. Record the final Source 2 voltage on the calibration worksheet as the new Reading #3.

- 18. Go back to step 12.
- 19. Compute the value of the 5450A resistor by applying the following formula to the values in the calibration worksheet:

$$R = \frac{\text{STANDARD RESISTANCE x READING #3}}{\text{READING #1 - READING #2}}$$

- 20. Enter R into the worksheet as the 5450A ACTUAL VALUE.
- 21. Perform the next line of Table 4-4. If the transfer of standards is complete, set the Sources to standby and the Null Detector to ZERO. Then proceed to the ratio calibration procedure.

#### 4-32. RATIO CALIBRATION

Connect the 5450A as shown in Figure 4-6. Note that the GUARD is connected to GROUND only at Source 2. Initially, connect the Null Detector LO input to the Reference Divider HI input, shown as position 1 in Figure 4-6. Select the 10:1 output from the Reference Divider. Select the 5450A EXT GUARD feature (LED on). Ensure that the Null Detector OPR/ZERO switch is set to the ZERO position.

For each line in Table 4-6, perform the following procedure. Information shown in bold (e.g., **5450A RESISTOR**) refers to headings in Table 4-6.

- 1. Select the **5450A RESISTOR** from the front panel of the 5450A. The 5450A will display the nominal value of the resistor followed by "CAL1".
- 2. Set Source 1 to provide **SOURCE 1 SETTING** dc output and set Source 1 to operate. If Source 1 is already set appropriately, leave it set to operate.
- 3. Set Source 2 to provide **SOURCE 2 SETTING** dc output and set Source 2 to operate.
- Perform the Nulling Procedure shown in Table 4-5, using the values from Table 4-6 under CAL1 for NULL DETECTOR Sensitivity and NULL DETECTOR Zero Limit. Record the

- final Source 2 voltage on the calibration worksheet as the CAL1 READING.
- 5. Connect the Null Detector LO input to the HI output of the Reference Divider, shown as connection 2 in Figure 4-6.
- Press the down-arrow switch on the 5450A front panel. The 5450A will display the nominal value 5450A RESISTOR followed by "CAL2".
- 7. Perform the Nulling Procedure shown in Table 4-5 using the data from Table 4-6 for CAL2. Record the final Source 2 voltage on the calibration worksheet as the CAL2 READING.
- 8. Press the up-arrow switch on the 5450A front panel. The 5450A should now display the nominal value **5450A RESISTOR** followed by "CAL1".
- 9. Connect the Null Detector LO input to the HI input of the Reference Divider. Set Source 2 to the CAL1 READING value recorded earlier. Set the Null Detector to the NULL DETECTOR Sensitivity CAL1 Range. Allow time for the reading to settle. If the Null Detector reading differs from zero by more that ALLOWABLE DRIFT, then go to the next step. Otherwise, set the Null Detector to ZERO and go to step 15.

Table 4-6. Ratio Calibration Data

		2011705.0	NULL DETECTOR				ALLOWABLE	REFERENCE	EQUATION	
5450A RESISTOR	SOURCE 1 SETTING	SOURCE 2 SETTING	Sens	itivity	Zero	Limit	DRIFT	VALUE	NUMBER	
RESISTOR	SETTING	SETTING	CAL1	CAL2	CAL1	CAL2				
100 kΩ	19V	19V	10 <i>μ</i> V	3 <i>μ</i> V	2 μV	0.3 <i>μ</i> V	5 <i>μ</i> V	10 kΩ	1	
1 ΜΩ	19V	19V	10 μV	3 <i>μ</i> V	2 μV	0.3 <i>μ</i> V	5 <i>μ</i> V	100 kΩ	1	
10 MΩ	19V	19V	10 μV	3 µV	2 μΝ	0.3 <i>μ</i> V	5 <i>μ</i> V	1 ΜΩ	1	
100 MΩ	19V	19V	10 μV	3 <i>μ</i> V	4 μV	0.3 <i>μ</i> V	7 با√	10 ΜΩ	1	
190 kΩ	19V	19V	10 μV	3 μV	2 μV	0.3 <i>μ</i> V	5 <i>μ</i> V	19 kΩ	1	
1.9 ΜΩ	19V	19V	10 μV	3 μV	2 μV	0.3 μV	5 μV	190 kΩ	1	
19 ΜΩ	19V	19V	10 μV	3 μV	2 μV	0.3 <i>μ</i> V	5 <i>μ</i> V	1.9 ΜΩ	1	
1 kΩ	10V	10V	10 μV	3 μV	1 µV	0.2 ہا	2.5 <i>μ</i> V	10 kΩ	2	
100Ω	5V	5V	10 μV	3 μV	1 μV	0.2 μV	1 μV	1 kΩ	2	
1.9 kΩ	10V	10V	10 μV	3 µV	1 μV	0.2 <i>μ</i> V	2.5 <i>μ</i> V	19 kΩ	2	
190Ω	5V	5V	10 μV		1 μV	0.2 <i>μ</i> V	1 μV	1.9 kΩ	2	
10Ω*	10 mA	1V	3 μV	3 μV	0.2 μV	0.2 <i>μ</i> V	0.5 <i>μ</i> V	100Ω	2	
19Ω*	10 mA	1.9V	10 μV	3 μV	0.2 μV	0.2 μV	0.5 <i>μ</i> V	190Ω	2	

<sup>\*</sup>If a Fluke 5440A is used as Source 2, use the DIVIDED OUTPUT for these steps.

- 10. Adjust Source 2 until the Null Detector reading is again zero within the limits given in NULL DETECTOR Zero Limit CAL1. Set the Null Detector to ZERO. Record the final Source 2 voltage on the calibration worksheet as the new CAL1 READING.
- 11. Press the down-arrow switch on the 5450A front panel. The 5450A will display the nominal value of **5450A RESISTOR** followed by "CAL2".
- 12. Connect the Null Detector LO input to the HI output of the Reference Divider, shown as position 2 in Figure 4-6.
- 13. Set Source 2 to the CAL2 READING recorded earlier. Set the Null Detector to the **NULL DETECTOR Sensitivity CAL2** range. Set the Null Detector to OPR.
- 14. Adjust Source 2 until the Null Detector reading is again zero within the limits given in NULL DETECTOR Zero Limit CAL2. Set the Null Detector to ZERO. Record the final Source 2 voltage on the calibration worksheet as the new CAL2 READING. Go back to Step 8.
- 15. Compute the actual value of the 5450A resistor by applying the equation specified in Table 4-6 by **EQUATION NUMBER**:

#### **EQUATION 1:**

 $R = \frac{10 \times 5450A \text{ ACTUAL VALUE} \times \text{CAL1 READING}}{\text{CAL2 READING}}$ 

#### **EQUATION 2:**

 $R = \frac{5450A \text{ ACTUAL VALUE x CAL2 READING}}{10 \text{ x CAL1 READING}}$ 

#### NOTE

The "5450A ACTUAL VALUE" used in the above equations is a value already determined in a previous step of the calibration procedure and entered on the worksheet. The column labeled REFERENCE VALUE on the calibration worksheet indicates which previous value to use. For example, to determine the 100 k $\Omega$  value, the equation uses the 5450A ACTUAL VALUE determined by using the 10 k $\Omega$  standard. The 1 M $\Omega$  value is determined by using the 100 k $\Omega$  value, and so forth.

16. Enter the computed resistance into the 5450A ACTUAL VALUE column of the worksheet.

#### 4-33 CALIBRATING THE SHORT

Connect the 5450A as shown in Figure 4-7. Ensure that the Null Detector OPR/ZERO switch is set to the ZERO position.

#### **NOTE**

It may be desired to calibrate the 2-wire short with a particular cable to be used with the 5450A when calibrating 2-Wire UUTs. If this is the case, include the desired cable between the 5450A OUTPUT terminals and the connections shown.

- Select the 2-wire zero calibration configuration by selecting the 2 WIRE COMPensation feature (LED on) and selecting the SHORT. (The 5450A will display "2W 0CAL".) The EXT GUARD should also be selected (LED on).
- 2. Set Source 1 to provide 500 mA.
- 3. Initially set Source 2 to 80 mV. (If Source 2 is a Fluke 5440A, use the DIVIDED OUTPUT.)
- Perform the nulling procedure shown in Table
   4-5 until a null is obtained on the 100 uV range of the Null Detector with an error of less than
   40 uV. Set the Null Detector to ZERO.
- 5. Enter the current (0.5 amps) into the CURRENT position of the worksheet.
- 6. Enter the voltage of Source 2 as the VOLTAGE in the calibration worksheet.
- 7. Set Source 1 and Source 2 to standby.
- 8. Compute the ACTUAL VALUE using the formula:

 $ACTUAL VALUE = \frac{VOLTAGE}{CURRENT}$ 

9. Enter the ACTUAL VALUE on the worksheet.

#### 4-34. CONCLUDING THE PERFORMANCE TEST

If only performing the performance verification test, return the rear panel CALIBRATION switch to the DISABLE position. Then, perform the following procedure for each resistor on the worksheet:

- Select the output resistance from the 5450A indicated in the first column.
- 2. Press the ENTRY MODE switch and then, using the main keyboard, enter the ACTUAL

VALUE indicated in the worksheet. If an incorrect number is pressed, it can be deleted by pressing the DELETE switch. Press the ENTER switch to compute the error.

3. Verify that the displayed error (in PPM) is within the 4-wire accuracy specifications given in Section 1. Use the specification for the appropriate calibration period. For example, if calibrating the 5450A on a 90-day cycle, select the 90-day accuracy specification.

This concludes the performance verification test. If a full calibration is to be performed, return the rear panel CALIBRATION switch to the ENABLE position and continue with the following procedure.

#### 4-35. ENTERING THE CALIBRATION CONSTANTS

The worksheet now contains all of the 5450A actual values to be entered into the non-volatile memory of the 5450A. For each ACTUAL VALUE shown on the worksheet:

- 1. Select the output from the 5450A indicated in the worksheet as 5450A RESISTOR. The 5450A should display the resistance followed by CAL, CAL1, or CAL2. When entering the  $0\Omega$  value, the display should indicate 2W 0CAL.
- Press the ENTRY MODE switch and then, using the main keyboard, enter the ACTUAL VALUE indicated in the worksheet. If an incorrect numeric is pressed, it can be deleted by pressing the DELETE switch. Press the ENTER switch to enter the constant into non-volatile 5450A memory.
- 3. The 5450A will display the change in the constant as an error in PPM or % (depending whether PPM or % is selected). To exit the ERROR mode and enter the OUTPUT mode, press any front panel switch except ENTRY MODE or PPM/%.

### 4-36. CONCLUDING THE CALIBRATION PROCEDURE

To conclude the calibration procedure, first disconnect all leads to the 5450A.

At this time, it is possible to change the "personality" message of the 5450A if an IEEE-488 instrument controller, such as the Fluke 1722A, is available. The personality message appears on the display of the 5450A when the instrument is powered-up, and may also be read by an IEEE-488 instrument controller. As delivered from the factory, the personality message is "5450A".

The personality message is programmed using the PERSONALITY command. For example, using the Fluke 1722A, and with the 5450A set at bus address 4, the command

#### PRINT@4,"PERSONALITY A3045;"

would replace the personality message with "A3045". In this example, A3045 could represent an asset number. Other uses of the personality message could include the 5450A serial number, a user code, a due date for the next calibration, or other facility-dictated needs. The personality message can be written into the 5450A only when the CALIBRATION switch is in the ENABLE position.

After the calibration procedure (or performance test) has been completed, set the rear panel CALIBRATION switch to the DISABLE position. In this position, the EAROM hardware and firmware are both protected from inadvertent changes. Place a calibration sticker over the CALIBRATION switch to provide calibration integrity until the next calibration. Since it is possible to remove the top cover and change the CALIBRATION switch setting without disturbing the rear panel sticker, it may be desirable to place a calibration sticker over one of the six screws that secure the top cover to the frame.

#### 4-37. Alternate Calibration Procedures

The preceding paragraphs describe the normal calibration procedure. This procedure was designed to meet the necessary accuracy requirements with test equipment that calibration facilities are likely to already have in place.

If desired, the calibration facility may use a different selection of equipment or procedure to calibrate. Such alternate procedures are supported by placing the 5450A rear panel switch to SP CAL (SPecial CALibration) and the rear panel CALIBRATION switch to the ENABLE position. When this configuration is used, the 5450A functions exactly the same as when the CALIBRATION switch is in the DISABLE position. However, the non-volatile calibration memory can still be altered by ENTERing values.

The exact value of each resistor may be determined as the particular facility sees fit, and entered into non-volatile memory by the following sequence:

- 1. Select the particular resistance from the front panel.
- 2. Press the ENTRY MODE switch.
- 3. Use the numeric switches to enter the known value of the resistor.
- 4. Press the ENTER switch.

The 5450A displays the error from the last entered value for the resistor in PPM or %, depending on the state of the PPM/% switch. Pressing any switch except ENTRY MODE, EXT GD and %/PPM returns the 5450A to the OUTPUT mode of operation.

To determine the specifications of the 5450A when an alternate calibration technique has been used, use the stability specifications shown in Section 1 of this manual. The total uncertainty of each resistance value is determined by adding the stability specification to the calibration uncertainty of the particular resistance.

#### 4-38. TROUBLESHOOTING

#### 4-39. Introduction

The following discussion is intended to help in the repair of an instrument that is malfunctioning or completely non-operational. The discussion seeks to isolate the problem to one of the four major circuits, and then to one or more components. While proceeding, refer to the schematic drawings in Section 9. If further troubleshooting is required, refer to the theory of operation in Section 3.

#### 4-40. Power Supply Isolation

If the 5450A is completely non-operational after power is applied and the POWER switch is pressed, there may be a fault in either the rear panel input fuse (F201) or the front panel POWER switch. Remove fuse F201 and check for continuity. If it is good, remove the cover to the power supply, power up the instrument, and measure the voltage between TP1 and TP2 on the Power Supply PCA. If line voltage is not present, check that the front panel POWER switch is functioning properly.

Check for approximately 5V dc between TP202 and TP201 on the Digital PCA. If a voltage is present, but not 5V dc  $\pm$ 50 mV, adjust R204 on the Digital PCA to obtain the proper voltage. Check the voltage between TP2 and TP3 on the Display PCA. It should be 5V dc  $\pm$ .25V dc. Check the voltage between TP2 and TP4 on the Analog PCA. It should be 5V dc  $\pm$ .25V. If any of these voltages are present, the primary circuit to the power transformer is functional. If none are present, analyze the circuit of the Power Supply PCA and then the power transformer itself. If all of these voltages are correct, the power supply circuitry is operating properly.

If the voltage is incorrect on the Analog PCA, check fuses F1 and F2 on the Analog PCA. (A spare fuse is located on the transformer chassis.) If the voltage is still not correct, check VR1, CR4, CR5, and associated circuitry. If the voltage is not correct on the Display PCA, check regulator U14. If the voltage is not correct on the Digital PCA, check the operation of U202 and associated circuitry. Note that on the 5V regulated supply VCC, the

load may be disconnected at J201 so that the effects of circuit loading can be eliminated.

#### 4-41. Analog Assembly Isolation

If the 5450A switches and display operate normally, but the output of the SENSE and OUTPUT terminals is incorrect, it is likely that the problem is on the Analog PCA. If the relays do not move at all when different front panel switches are pressed (the relays can be checked visually or aurally), the problem may be the relay coil voltage supply. Check the voltage between TP3 and TP4 on the Analog PCA. The voltage should be about 4.5-5V dc when OPEN is selected, and about 3.5-4.5V dc when other outputs are selected. If the voltage is incorrect, check U2, Q1, CR1, CR2 and the associated circuitry.

If relay energization voltage is present, the problem may be the digital commands from the Digital PCA; however, the output of the same data lines is used for the Display PCA. The inputs READO, WRITEO, STATUSO, and WRITEI should each change states in the course of a programming cycle. Observe each with a logic probe. READO cyclically changes states regardless of whether the instrument has been commanded to change outputs, so the logic probe should show oscillation. If any of these control lines do not change state properly, suspect U25 on the Digital PCA. If none of the relays operate, suspect the Analog PCA components U1, U4, or U5 and their associated circuitry.

To isolate the problem to a minimum number of relays, use the truth table shown in the schematic diagrams and on the top analog guard cover. Examine the specific relays used for the resistance value that does not function properly. Next, for each of the relays, choose another output resistance that uses that same relay. If the other resistance works properly, the relay in question may be eliminated from consideration. Continue this process until the number of suspect relays cannot be reduced. Most of the relays are mounted on sockets for easy substitution. To help locate the faulty relay, substitute a similar relay from elsewhere in the instrument.

If one of the relays does not actuate, suspect the relay itself, the associated sink (U7, U12, or U18), or the associated latches (U6, U8, U9, U10, U13, U14, U15, or U16). If all of the relays from a particular latch do not actuate, suspect the latch (e.g., U6, U8, or U12) or decoder U5.

#### 4-42. Display Assembly Isolation

If the display or front panel switches do not work properly, this may indicate a problem on the Display PCA or the Digital PCA. If the Digital PCA is not functioning properly (i.e., the microprocessor is not functioning), the front panel REMOTE light will be on, and all other front panel displays will be off. If any of the

display digits, LEDs, or switches work at all (with the exception of the REMOTE LED), it is likely that the Digital PCA is working properly.

If the left four display digits work properly, but not the right four, suspect U1 or U2. If the right four display digits work properly, but not the left four, suspect U11 or U15. If one-half of a character is not functioning properly, suspect one of the sink components (U7, U8, or U16) or the decoder U10. If the switches do not work properly, suspect Z1, the switches themselves, or U13. If only one of the segments on only one of the displays does not work, it is very likely that the problem lies within the display itself (DS1, DS2, DS3, or DS4). The data lines are paralleled to the instrument memory so it is unlikely that the data lines are malfunctioning if the instrument is otherwise functioning.

#### 4-43. Digital Assembly Isolation

#### NOTE

A troubleshooting tape is available to troubleshoot the digital circuitry of the 5450A using the Fluke 9010A Micro-System Troubleshooter and a 9000-Z80 Pod. The tape may be ordered as Fluke Model Number 9000-9713.

If the instrument is completely non-operational or seriously malfunctioning, it is probable that the problem lies in the Digital PCA or the power supply. See the discussion regarding power supply isolation prior to continuing with troubleshooting the Digital PCA. As noted in the previous paragraphs, if the microprocessor is not operating properly, the display of the instrument will normally be blank with the exception of the REMOTE LED.

For the microprocessor to operate normally, interrupt signals must be present at TP6. These can be observed with a logic probe. If the interrupt is not present, examine the circuitry surrounding and including U29, U28, U32, U23, U33, U35, and U17 that creates the INT signal. Another prerequisite for the microprocessor is the 4-MHz clock signal, which can be observed at TP7 with a logic probe. If this signal is not present, examine the circuitry involving Y1, U19, and U20. RESET at TP3 should be examined to verify that it is high. If RESET is low, examine the circuit consisting of U22, U36, and U28.

To ensure that the ROMs are being enabled, examine  $\overline{OE}$  (output enable), pin 22 of U2, U3, and U4 on the Memory PCA. If the logic probe does not indicate a changing signal, consider problems with the circuitry of U1 on the Display PCA. If all of these signals look proper, use a logic probe on each remaining data, address, and output pin of the microprocessor (U18) to ensure that all outputs are changing. A pin that does not change could indicate a fault in U18 or in the associated circuitry pulling the pin high or low.

If it is necessary to replace the EAROM (U5 on the Memory PCA), it is necessary to initialize the new part with nominal values. This is performed by placing the rear panel CALIBRATION switch in the ENABLE position, then powering up the 5450A. The instrument must be recalibrated after the EAROM is replaced.

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# Section 5 List of Replaceable Parts

#### **TABLE OF CONTENTS**

	ASSEMBLY NAME		ABLE PAGE		GURE PAGE
5450	A Resistance Calibrator	5-1	5-2		
Fina	ıl Assembly	5-2	5-3	5-1	5-4
A1	Analog Assembly	5-3	5-8	5-2	5-9
A2	Digital Assembly	5-4	5-10	5-3	5-12
	Display Assembly			5-4	5-14
	Memory Assembly			5-5	5-15
	Power Supply Assembly			5-6	5-16

#### 5-1. INTRODUCTION

This section contains an illustrated parts breakdown of the 5450A Resistance Calibrator. Components are listed alphanumerically by assembly. Both electrical and mechanical components are listed by reference designation. The location of each listed part is shown in an accompanying illustration.

Parts lists include the following information:

- 1. Reference designation.
- 2. Description of each part.
- 3. Fluke stock number.
- 4. Federal supply code for manufacturers. (See Section 7 for list of names and codes.)
- 5. Manufacturer's part number.
- 6. Total quantity of components per assembly.
- 7. Recommended quantity. This entry indicates the recommended number of spare parts necessary to support one to five instruments for a period of 2 years. This list presumes an availability of common electronic parts at the maintenance site. For maintenance for 1 year or more at an isolated site, it is recommended that at least one of each assembly in the instrument be stocked. In the case of optional subassemblies, plug-ins, etc., that are not always part of the instrument, or are deviations from the basic instrument model, the REC QTY column lists the recommended spares quantity for the items in that particular assembly.

#### 5-2. HOW TO OBTAIN PARTS

Components may be ordered directly from the manufacturer by using the manufacturer's part number or from the John Fluke Manufacturing Co., Inc, or its authorized representatives by using the Fluke Part Number. In the event that the part ordered has been replaced by a new or improved part, the replacement will be accompanied by an explanatory note and installation instructions if necessary.

To ensure prompt and efficient handling of your order, include the following information.

- 1. Quantity.
- 2. Fluke part number.
- 3. Description.
- 4. Reference designation.
- Printed circuit board part number and revision letter.
- 6. Instrument model and serial number.

A Recommended Spare Parts Kit for your basic instrument is available from the factory. This kit contains those items listed in the REC QTY column of the parts list in the quantities recommended.

Parts price information is available from the John Fluke Mfg. Co., Inc. or its representatives. Prices are also available in a Fluke Replacement Parts Catalog which is available on request.

CAUTION
Or \*\*
INDICATED DEVICES ARE SUBJECT TO
DAMAGE BY STATIC DISCHARGE.

TABLE 5-1. RESISTANCE CALIBRATOR 5450A

REFERENCE DESIGNATOR A->NUMERICS>	SDESCRIPTION	FLUKE STOCK NO	MFRS SPLY CODE-	MANUFACTURERS PART NUMBEROR GENERIC TYPE	TOT QTY	8 2 -Q	0 T -E
A 1 A 2 A 3 A 4 A 5	* 5450A FINAL ASSEMBLY  * ANALOG ASSEMBLY  * DIGITAL ASSEMBLY  * DISPLAY ASSEMBLY  * MEMORY PCA  * POWER SUPPLY PCB	713545 713578 713602 713610 713644	89536 89536 89536 89536 89536	713545 713578 713602 713610 713644	001 001 001 001 001		

7	í	3	4	4	É

REFERENCE DESIGNATOR A->NUMERICS>	(SEE FIGURE 5-1.) 5450A-7201  SDESCRIPTION	FLUKE STOCK NO	MFRS SPLY CODE-	MANUFACTURERS PART NUMBEROR GENERIC TYPE	TOT QTY	R 2 -Q	N O T -E
	POST PINETIC COGNIDING TYPE	225623	20584	3576	001	05	
E 1 E 2	POST, BINDING, GROUNDING TYPE POST, BINDING, GROUNDING TYPE	225615	20584	3575	001		
E 3, 4	LUG, SOLDER	101022	8 <b>9536</b>	101022	002		
F 1	FUSE,1/4 X 1-1/4, SLOW, 0.5A, 250V	109322	71400 05245	MDL1-2 3EF2	001 001		
FL 1	FILTER, LINE NUT, METAL 4-40 CAPTIVE EXT LOCKWASHER	321273 195255	89536	195255	002		
H 1 H 2	WASHER, LOCK, EXTRNL, STEEL, 0.500 ID	175943	89536	175943	001		
H 3	WASHER, LOCK, INTRNL, STEEL, 0.267 ID	110817	89536	110817	002		
H 4	NUT, METAL 1/4-28 HEX STEEL SCREW, CAP, LH SCKT, STEEL, 8-32X3/8	110619 295105	89 <b>536</b> 89536	110619 295105	002 008		
H 5 H 6	SCREW, MACHINE, FHP, S STEEL, 8-32X3/8	320085	89536	320085	998		
H 7	SCREW, MACHINE, FHP, STEEL, 8-32X1/2	114355	89536	114355	998		
H 8	SCREW, MACHINE, PHP SEMS, STEEL, 6-32X1/2	177030 185918	89536 89536	177030 185918	004 011		
H 9 H 10	SCREW, MACHINE, PHP SEMS, STEEL, 4-40X1/4 SCREW, MACHINE, PHP SEMS, STEEL, 6-32X3/8	177022	89536	177022	042		
H 11	SCREW, MACHINE, PHP, STEEL, 4-40X3/16	129882	73734	19022	002		
H 12	WASHER, SHLDR, NYLON, #3	485417 110403	89536 89536	485 <b>417</b> 110403	001 002		
H 13 H 14	WASHER,LOCK,INTRNL,STEEL,#4 SCREW,MACHINE,FHUP,S STEEL,6-32X1/4	320093	89536	320093	012		
H 16	SCREW, 6-32 X 3/4	309963	89536	309963	001		
H 17	NUT, KEP \$6	152819	89536 89536	152819 721688	0 <b>01</b> 010		
H 18	WASHER, COPPER, #8 NUT,BRASS, 8-32	721688 631 <b>614</b>	89536	631614	995		
H 19 J 1, 2	CONN, CABLE, RIBBON, SCREW LOCK	494773	89536	494773	002		
MP 1	PANEL, REAR	713479	89536	713479	991 999		
MP 2	POLYOLEFIN #14 .120ID EXPAND BLK BINDING-POST-ASSY-CP-RED	149450 637 <b>892</b>	89536 89536	149450 6378 <b>92</b>	002		
MP 3 MP 4	BINDING-POST-ASSY-CP-BLK	637900	89536	637900	002		
MP 5			7536	637876	001 001		
MP 6	BINDING-POST-ASSY-I NAMEPLATE, SERIAL - 26523 FRONT PANEL MOLDED DISPLAY LENS DECAL FRONT PANEL DECAL, PWR ON-OFF		7536 7536	472795 713651	001		
MP 7 MP 8	DISPLAY LENS 2054	29	7536	732545	001		
MP 9	DECAL FRONT PANEL	~ !	7536	721951	001 001		
MP 10	DECAL, PWR ON-OFF GUARD CHASSIS ASSY  2714	पि	7536 7536	721969 713677	001		
MP 11 MP 12	CHASSIS SIDE, LEFT		>536	491084	001		
MP 13	CHASSIS SIDE, RIGHT	_	7536	491092	0 <b>01</b> 001		
MP 14	BULKHEAD CENTER 15903	88	7536 7536	713685 491175	991		
MP 15 MP 16	PANEL, FRONT 20 CORNER HANDLE, FROM	•	7536	656157	004		
MP 17	DECAL, REAR CORNER		?536	685230	002		
MP 18	DECAL, FRONT CORNEL		7536 7536	659201 65392 <b>3</b>	002 004		
MP <b>19</b> MP 20	SIDE TRIM 18" 15902	$\wedge$	7536	525998	002		
MP 21		1	7536	713511 SJ-5003	001 <b>001</b>		
MP 22	FOOT, GREY COVER, TOP GUARD COVER, TOP 18" PACK! 5450 71357	\e	?152 ?536	713495	001		
MP 23 MP 24	COVER, TOP 18" PACKI /450"		1536	491183	001		
MP 25	COVER, BOTTOM 18' P/5 7/357	<b>X</b> /	7536	491191	001 000		
MP 26	PULTULEFIN TO SLEET	`.	7536 7536	113837 172080	005	01	
MP 27 MP 28	ACTUATOR, SWITCH	0	1536	502690	001		
MP 29	THROUGH TOK, PHRIENET		1536	372342	996 996		
MP 30	SPACER, INSULATED		'536 '536	372334 529461	001		
MP 31 MP 32	FLAT CABLE CLAMP GROMMET MATL., NATURAL	285254	89536	285254	999		
MP 33	CLAMP, CABLE	165951	31827	3-4-1	001		
MP 34	COVER PLATE,TRANSFORMER SLEEVING, POLYOL, #1/2,.500 I.D.,CLR	496 <b>893</b> 218529	89536 89536	496893 218529	0 <b>01</b> 001		
MP 35 MP 36	BAIL, INSTRUMENT	707877	89536	707877	002		
MP 37	INSULATOR, TRANSISTOR 7403-09FR-51	508630	89536	5086 <b>30</b>	001		
MP 38	FUSE HLDR, BODY W/NUT,	375188 460238	89536 89536	375188 460238	001 001		
MP 39 P 1	FUSE HLDR,CAP,1/4 X 1-1/4 FUSE FLEX STRIP	605212	89536	605212	001	91	
s i	SWITCH, POWER	507871	71468	SCH.1XF.0003-01	991		
TM 1	INSTRUCTION MANUAL 5450A	713800 428847	89536 04713	713800 MC7805CT	001 001		
U 1 1 1	* IC, VOLT REG, FIXED, +5 VOLTS, 1.5 AMPS WIRE	578609	89536	578609	001		
W 2	\$18 AWG TEFLON GREEN/YELLOW	386177	89536	386177	001		
<b>W</b> 3	#22 HOOK-UP PER MIL-W-16878C, BLACK	115774 496877	89536 89536	115774 496877	001 001		
W 4 W 5	CABLE, POWER SWITCH, ASSY. WIRE, BUS, 14 AWG, TINNED COPPER	115451	89536	115451	001		
W 3 W 6	HARNESS ASSY, OUTPUT	713735	89536	713735	001		
₩ 7	CABLE ASSY, CAL SWITCH	713750	89536	713750 73337	001 <b>001</b>		
W 8 W 9	CABLE ASSY, SHIELDED CABLE, GROUND	733337 732495		733337 732495	001		
W 9	RECOMMENDED SPARE PARTS KIT 5450A	717157	89536	717157	AR		
	(CUSTOMERS OPTION)						

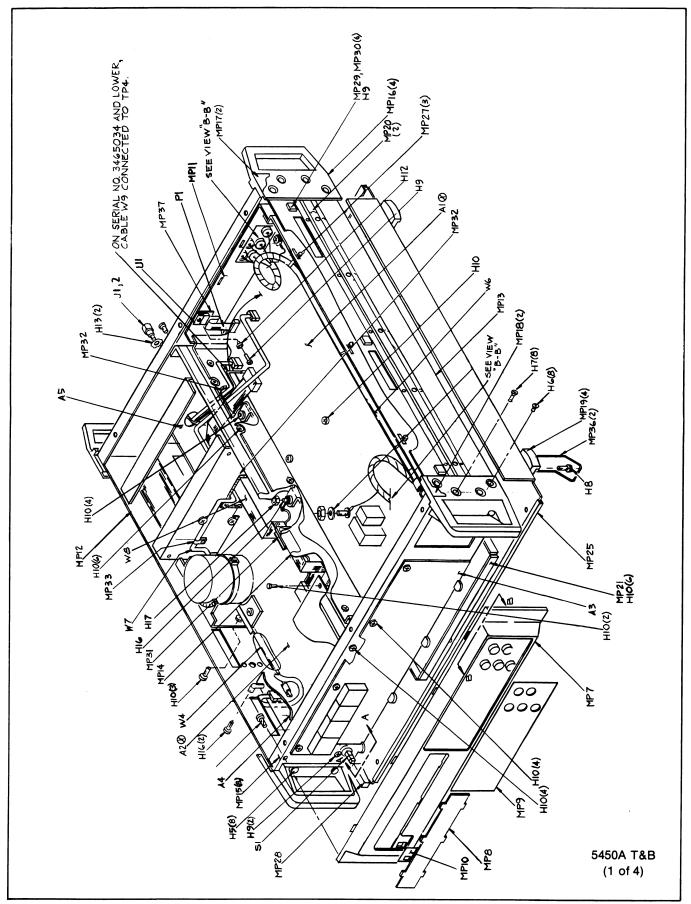


Figure 5-1. Final Assembly

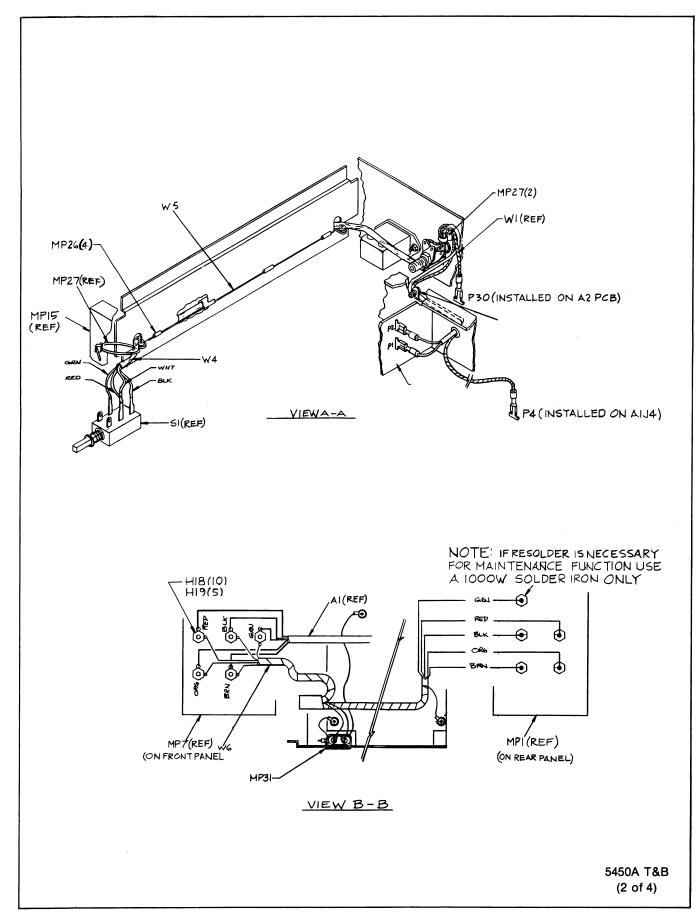


Figure 5-1. Final Assembly (cont)

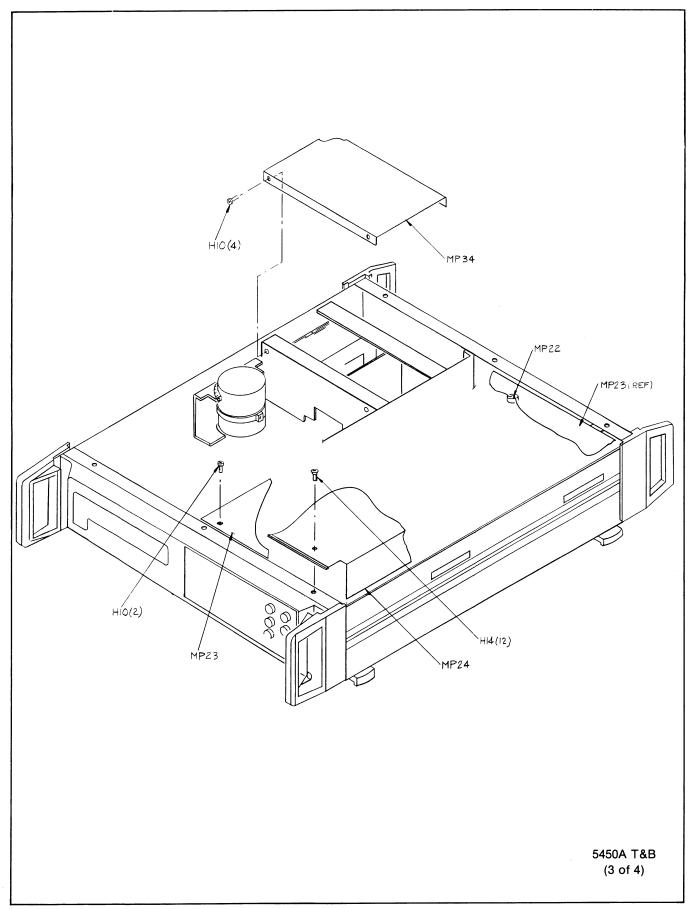


Figure 5-1. Final Assembly (cont)

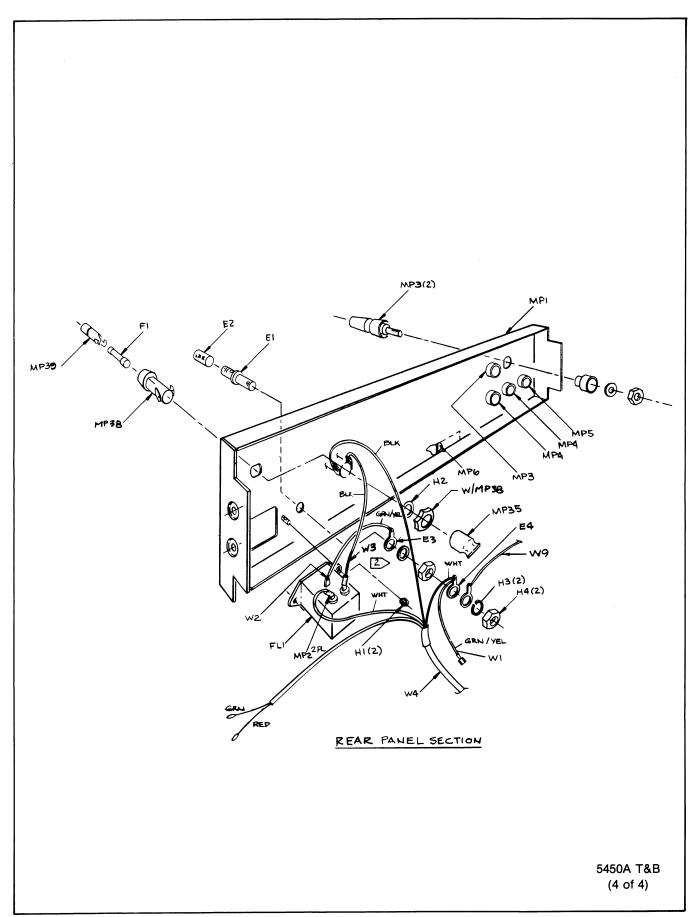


Figure 5-1. Final Assembly (cont)

TABLE 5-3. A1 ANALOG ASSEMBLY (SEE FIGURE 5-2.) 5450A-4001 o MERS. MANUFACTURERS REFERENCE FLUKE STOCK SPLY PART NUMBER TOT S T DESIGNATOR CODE- --OR GENERIC TYPE----NO--A->NUMERICS---> S -----DESCRIPTION-----QTY -0 -F 72982 8121-A050-651-223Z Θ1 CAP, CER, 0.022UF, +100-20%, 40V, Z5U CW3COC224K CAP, CER, 0.22UF, +-20%, 50V, Z5U CAP, CLER, 0.1220F, 4-202, 150V, 250 CAP, AL, 15000UF, +30-20X, 16V \*\* DIODE, SI, 100 PIV, 1.5 AMP 116111 \*\* ZENER, UNCOMP, 9.1V, 5%, 28.0MA, 1.0W 459917 \*\* DIODE, SI, 100 PIV, 5.0 AMP 483701 STANDOFF, TERM, TEFLON 271650 FUSE, 1/4 X 1-1/4, FAST, 2A, 250V 109173 C 1N4817 CR 1. UZ8709 CR 5FF10 CR 4, SL890823 Ε 1. ١. SCREW, MACHINE, PHP, STEEL, 6-32X3/16 н SCREW, MACHINE, PHP, STEEL, 8-32X3/8 NUT, HEX DBL CHAMFER 8-32NNC-2B BRASS 631614 WASHER, FLAT, BRASS, \$8,0.032 THK 631606 SCREW, MACHINE, PHP, STEEL, 4-40X5/16 NUT, METAL 4-40 CAPTIVE EXT LOCKWASHER 195255 TERMINAL, TEFLON TERMINAL 720797 SOCKET, DIP, 0.100 CTR, 16 PIN 387324 н н 133-59-02-062 J POST CONTACT W/CARRIER STRIP 2. TERMINAL, CONNECTOR RELAY, ARMATURE, 6 FORM C, 4.5VDC 7-1. 4, 2, 3, 6 17- 22, 31 RELAY, ARMATURE, 2 FORM C, 4.5VDC 6. RELAY, ARMATURE, 4 FORM C, 4.5VDC RIO-E3713-3 5, 16, 30 RELAY, REED, 1 FORM A, 4.5VDC RELAY, REED, 1 FORM A, 4.5V, HI IR RELAY, REED, 1 FORM B, 5VDC 23-32. 33 E-9174 ANALOG PCB, BARE SPACER, SWAGED, RND, BRASS, 8-32X0.437 MP MP FUSE HLDR, CLIP, PCB, 1/4 DIA FUSE SPACER, SWAGED, RND, BRASS, 6-32X1.450 MP MP CAN, RESISTOR SET MP SLEEVING #22 TEFLON, .027ID SPRING, RELAY SOCKET, 2 POLE SPRING, RELAY SOCKET, 4 POLE MP 20C249 20C250 MP SPRING, RELAY SOCKET, 6 POLE \* TRANSISTOR, SI, BV= 60V, 65W, TO-220 T1P120 CR251-4-5P1K RES,CF,1K,+-5%,0.25W Θ1 RES,CC,360,+-5%,0.5W RES,CF,100,+-5%,0.25W CR251-4-5P100E 5- 39 RESISTOR SET RES, CAR. COMP, 1K +/-10%, 1/8W 153916
MINI,PCB MTG. .3AMP AT 125V, \$\pmu\subsetem \text{SS2250R} 354878
CONN, TAB, FASTON, PRESS-IN, 0.110 WIDE 512889 BB1021 40- 43 TP \* IC, TTL, QUAD 2-INPUT AND GATE \* IC, LSTTL, DUAL D F/F, +EDG TRG, W/CLR \* IC, TTL, HEX INVERTER W/OPEN COLLECTOR SN74LS74N SN7406N \* IC,LSTTL,OCTL BUS TRNSCVR W/3-ST OUT \* IC,LSTTL,3-8 LINE DCDR W/ENABLE SN74LS245N Aí SN74LS138N \* IC, LSTTL, 8BIT ADDRSABLE LATCH, W/CLR SN74LS259N 6. 01295 ULN2003 \* IC, ARRAY, 7 TRANS, NPN, DARLINGTON PAIRS 454116 7, 11, 12, 17, 18 8, 10, 13, \* IC, LSTTL, OCTAL D F/F, +EDG TRG, W/CLEAR 454892 01295 SN74LS273N ΘΘ4 Θ1 WIRE, BUS, 24 AWG, TINNED COPPER AA1 CABLE ASSY, FRONT OUTPUT 

583773-1

R10273606

27E501

SOCKET, SIP, 0.100 CTR, 4 PIN

RELAY SOCKET, 6 POLE

RELAY SOCKET, 2 POLE

RELAY SOCKET, 4 POLE

RELAY SOCKET, 4 POLE

RELAY SOCKET, 2 POLE

ХK

XK

XK

XK 

XK XK

2, 3, 6,

17-

5.

4. 1.

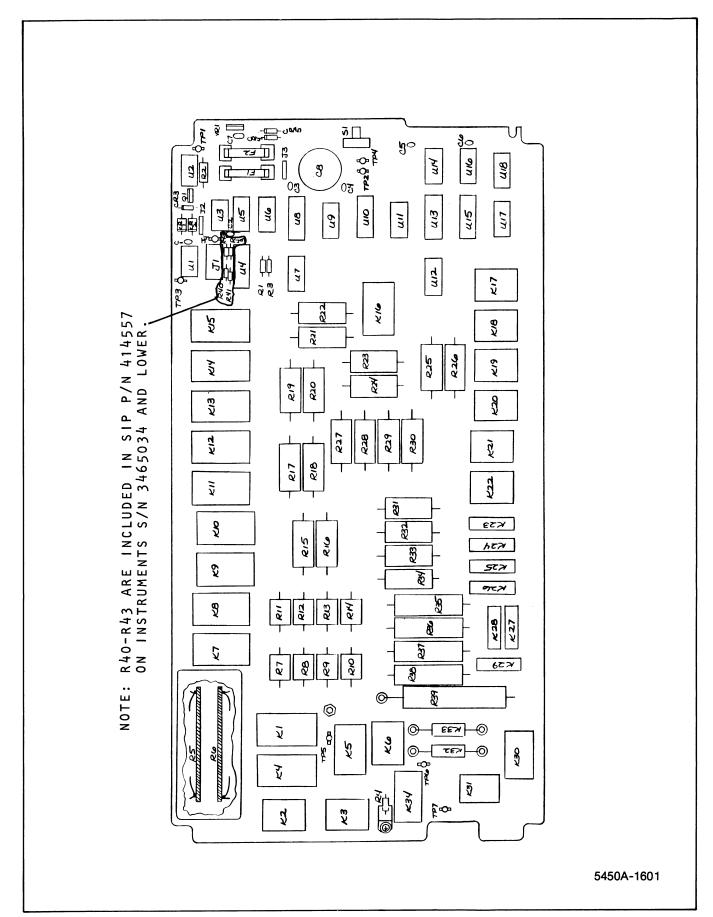


Figure 5-2. A1 Analog Assembly

TABLE 5-4. A2 DIGITAL ASSEMBLY (SEE FIGURE 5-3.) 5450A-4002

	SDESCRIPTION			MANUFACTURERS PART NUMBEROR GENERIC TYPE	TOT QTY	R S -Q	N T -E
WD /			0057/				
MP 6 U 4,5	5450A-LABEL * IC, 1K X 4 STAT RAM	713818 483479	89536 34649	713818 834444-5	001	01	
C 201	CAP, AL, 22000UF, +75-10%, 15V		56289		002	^4	
H 1	SCREW, MACHINE, PHP, S STEEL, 4-40X5/16	479220 335141	89536	32DX223G015BA2A 335141	001	01	
H 2	SCREW, MACHINE, PHP, S STEEL, 4-40X1-7/16		89536	403782	001 001		
H 3	SCREW, MACHINE, PHP, STEEL, 10-32X5/16	417105	89536	417105	001		
н 4	WASHER, SHLDR, NYLON, #4	436386	86928	5607-45	001		
H 5	P-NUT, 4-40	380196	24347		002		
Н 6	MACHINE, THD FORM, PWHP, STEEL, 5-20X5/16		89536	494641	002		
J 8	CONN, CABLE, RIBBON, PLUG, PWB EDGE, 24	485763	00779	552791-1	001		
MP 1	BRACKET, CAPACITOR/REGULATOR	490490	89536	490490	001		
MP 2	NYLON GROMMET MATL, "CATERPILLAR"	102277	89536	102277	000		
MP 3	CABLE TIE, NYLON, 11X.180, SST-35	501734	89536	501734	001		
MP 4	INSULATOR, TRANSISTOR 7403-09FR-51	508630	89536	508630	001		
MP 7	KEY, POLARIZINGOLARIZING	424572	89536	424572	001		
P 201	CONNECTING LINK	523167		523167	001		
R 210	RES, WW, 13, +-5%, 5W	520197		520197	001		
U 6, 7	* IC, 1K X 4 STAT RAM	483479	34649	P2114A5	002	01	
U 18 U 25	* IC,NMOS,8 BIT MICROCOMPUTER	478073	50088		001		
U 25 U 108-110	* IC,TTL,4-16 LINE DCDR W/DUAL STROBE	293217		SN74154N	001	01	
U 202	* IC,LSTTL,QUAD BUS XCVR W/3-STATE OUT	453480 460410	04713 12040	MC3448P LM317T	003	01	
XF 11	* IC, VOLT REG, ADJ, 1.2 TO 37 V, 1.5 AMPS SOCKET, SIP, 0.100 CTR, 20 PIN	443077	00779	583773-9	001 001	91	
CR 1, 2 CR 202,203	* DIODE,SI,BV= 75.0V,IO=150MA,500 MW	203323	07910	1N4448	002	01	
	* DIODE,SI, 100 PIV, 1.0 AMP RES,CF,1K,+-5%,0.25W	343491 573170	01295 80031	1N4002 CR251-4-5P1K	002	01	
R 1, 2, 4, R 5, 8, 14,	KES, CF, IK, TTS&, O.ZSW	573170	60031	CR231-4-3FTK	998		
R 15, 26		573170					
R 7, 23, 24	RES, CF, 51, +-5%, 0.25W	572990	80031	CR251-4-5P51E	003		
R 9, 16, 19,		573394	80031	CR251-4-5P10K	005		
R 101,102		573394					
R 10	RES, MF, 100K, +-1%, 0.125W, 100PPM	248807	91637	CMF551003F	001		
R 11	RES,MF,681K,+-1%,0.125W,100PPM	381517	91637	CMF556813F	001		
R 12	RES,MF,1.5K,+-1%,0.125W,100PPM	313098	91637	CMF551501F	001		
R 13	RES, MF, 10K, +-1%, 0.125W, 100PPM	168260	91637	CMF551002F	001		
R 17	RES, CF, 100K, +-5%, 0.25W	573584	80031	CR251-4-5P100K	001		
R 18	RES, MF, 28K, +-1%, 0.125W, 100PPM	291385	91637	CMF552802F	001		
R 21	RES, MF, 82.5K, +-1%, 0.125W, 100PPM	246223	91637	CMF558252F	001		
R 25 R 27	RES,CF,270,+-5%,0.25W RES,CF,750,+-5%,0.25W RES,CF,100,+-5%,0.25W RES,CF,470,+-5%,0.25W	573071 573162	80031 80031	CR251-4-5P270E	001		
R 104	RES, CF, 100, +-5%, 0.25W	573014	80031	CR251-4-5P750E CR251-4-5P100E	001 001		
R 105	RES, CF, 470, +-5%, 0.25W	573121	80031	CR251-4-5P470E	001		
R 202	RES, MF, 249, +-1%, 0.125W, 25PPM	448043	91637	CMF552490F	001		
R 203	RES, MF, 649, +-1%, 0.125W, 100PPM	309955	91637	CMF556490F	001		
R 205	RES, CF, 390, +-5%, 0.25W	573105	80031	CR251-4-5P390E	001		
U 1, 35	* IC,CMOS,DUAL D F/F,+EDG TRG	418830	12040	MM74C74	002	01	
U 2, 3	* IC,LSTTL,3-8 LINE DCDR W/ENABLE	407585	01295	SN74LS138N	002		
U 8		454892	01295	SN74LS273N	001	91	
U 9,103,105	* IC, LSTTL, DUAL D F/F, +EDG TRG, W/CLR	393124	01295	SN74LS74N	003	01	
U 16	* IC,LSTTL,QUAD 2 INPUT NAND GATE	393033	01295	SN74LS00N	001	01	
U 17 U 19	* IC,LSTTL,HEX INVERTER W/OPEN COLLECT * IC,TTL,HEX INVERTER	394536 292979	01295	SN74LS05 SN7404N	991	01	
U 20	* IC, LSTTL, SYNC DIV BY 16 BINARY COUNTR	495598	01295 01295	SN74LS163N	001 001	01 01	
U 21, 26	* IC, CMOS, DUAL SYNC BINRY UP CNTR	355164	04713	MC14520BCP	002	01	
U 22	* IC, CMOS, DUAL D F/F, +EDG TRIG	340117	02735	CD4013AE	001	01	
U 23	* IC, CMOS, QUAD 2 IN NAND W/SCHMT TRIG	404632	02735	CD4039BE	001	01	
U 24	* IC, LSTTL, OCTL BUS TRNSCVR W/3-ST OUT	477406	01295	SN74LS245N	001	01	
U 27, 31	* IC,CMOS,HEX BUFFER W/3-STATE OUTPUT	407759	12040	MM80C97N	002	01	
U 30	* IC, LSTTL, QUAD 2 INPUT NOR GATE	393041	01295	SN74LS02N	001	01	
U 32	* IC,CMOS,PRESETTABLE DIV BY N COUNTER	478313	02735	CD4018BE	001	01	
U 33	* IC,TTL,QUAD 2 INPUT AND GATE	393066	01295	SN74LS08N	001	01	
U 34,106	* IC,LSTTL,HEX INVERTER	393058	01295	SN74LS04N	002	01	
U 36	* IC, LSTTL, RETRG MONOSTAB MULTIVB W/CLR	404186	01295	SN74LS123N	001	91	
U 37	* IC, TTL, DUAL AND DRVR W/OPEN COLLECTOR	329706	01295	SN75452P	991	01	
U 39 U 102	* IC,LSTTL,QUAD 2 IN NAND W/SCHMT TRIG * IC,LSTTL,OCTL BUFFER W/3-ST&NOR ENABL	504449	01295	SN74LS132N	001	01	
U 104	* IC,LSTTL,QUAD 2 INPUT OR GATE	429902 393108	12040 01295	DM81LS95N SN74LS32N	001 001	91	
U 107	* IC,LSTTL,QUAD INTERFACE BUS TRANSCVR	428649	04713	MC3446P	001 001	01 01	
•		·	w		1		

TABLE 5-4. A2 DIGITAL ASSEMBLY (CONT)

REFERENCE DESIGNATOR A->NUMERICS>	SDESCRIPTION	FLUKE	MFRS SPLY CODE-	MANUFACTURERS PART NUMBEROR GENERIC TYPE	TOT QTY		0 T -E
	CAR TA SOUR A-207 SOV	176214	56289	196D106X0010KA1	001 024		
C 24,101-108 C 12,204 C 13, 32 C 15 C 19, 22	CAP, TA, 22UF, +-20%, 15V CAP, TA, 22UF, +-20%, 15V CAP, MICA, 47PF, +-5%, 500V CAP, CER, 0.05UF, +80-20%, 25V, Y5U CAP, CER, 0.01UF, +-20%, 100V, X7R CAP, TA, 1UF, +-10%, 35V  CAP, TA, 39UF, +-20%, 6V CAP, MICA, 100PF, +-5%, 500V CAP, MICA, 100PF, +-5%, 500V CAP, TA, 10UF, +-20%, 15V CAP, CER, 0.01UF, +80-20%, 100V, Z5V CAP, TA, 0.22UF, +-20%, 35V  **LED, RED, CLIP MOUNT, LUM INT=0.8MCD CONN, PWB EDGE, REC, 0.150 CTR, 8 PDS SOCKET, SIP, 0.100 CTR, 16 PIN SOCKET, DIP, 0.100 CTR, 16 PIN	423012 148536 148924	56289 72136 72982	196D226X0015KA1 CM15E470J 5855-000-Y5U0-503Z	002 002 001		
C 19, 22 C 20, 27, 28, C 30	CAP, CER, 0.01UF, +-20%, 100V, X7R CAP, TA, 1UF, +-10%, 35V	407361 161919 161919	72982 56289	8121-A100-W5R-103M 196D010X0035G	002 004		
C 23 C 25, 31 C 29,203	CAP,TA,39UF,+-20%,6V CAP,MICA,100PF,+-5%,500V CAP.TA.10UF,+-20%,15V	163915 148494 193623	56289 72136 56289	196D394X0020KA1 DM15F101J 196D106X0015A1	001 002 002		
C 109 C 202 CR 20	CAP,CER,0.01UF,+80-20%,100V,Z5V CAP,TA,0.22UF,+-20%,35V * LED,RED,CLIP MOUNT,LUM INT=0.8MCD	149153 161331 429555	56289 56289 12040	C0238101F103M 196D224X0035HA1 NL\$5053 583407-5	001 001 001	01	
J 12 J 13 J 14, 15	* LED,RED,CLIP MOUNT,LUM INT=0.8MCD CONN,PWB EDGE,REC,0.150 CTR,4 PIN SOCKET,SIP,0.100 CTR,4 PIN SOCKET,DIP,0.100 CTR,4 PIN CONN,PWB,HEADER,SIP,0.100,11 PIN CONN,PWB,SCKT STRIP,SIP,0.100,5 POS CHOKE,6TURN SPACER,RND,SOLUBLE,0.062IDX0.1560D CONN,TAB,FASTON,PRESS-IN,0.110 WIDE TRANSISTOR,SI,PNP,SMALL SIGNAL RES,CF,150,+-5%,0.25W RES,WW,0.1,+-10%,2W RES,VAR,CERM,200,+-10%,0.5W RES,NET,SIP,10 PIN,9 RES,10K,+-2% RES,NET,SIP,10 PIN,9 RES,10K,+-5% SWITCH,MOMENTARY SHADOW MD DISC SWITCH,DIP,8-POSITION SOCKET,DIP,0.100 CTR,18 PIN	354951 461756 276535	00779 00779 91506	583407-5 583773-1 316-AG39D	001 001 002		
J 16 J 201 L 1	CONN,PWB,HEADER,SIP,0.100,11 PIN CONN,PWB,SCKT STRIP,SIP,0.100,5 POS CHOKE.6TURN	494666 417899 320911	00779 52072 89536	583773-1 316-AG39D 1-640456-1 CA-05S-TSD 320911	001 001 001		
MP 5 MP 8	SPACER, RND, SOLUBLE, 0.0621DX0.1560D CONN, TAB, FASTON, PRESS-IN, 0.110 WIDE	296319 512889 195974	32559 02660 64713	T0806 62395 2N3906 CR251-4-5P150	018 001	01	
Q 1 R 6 R 201	RES, CF, 150, +-5%, 0.25W RES, WW, 0.1, +-10%, 2W	343442 485672 275743	80031 89536 89536	CR251-4-5P150 485672 275743	001 001 001		
R 204 R 209 RN 1,101	RES, CC, 10K, +-10%, 0.5W RES, NET, SIP, 10 PIN, 9 RES, 10K, +-2% RES, NET, DIP, 14 PIN, 15 RES, 10K, +-5%	108118 414003 355305	01121 89536 89536	EB1031 414003 355305	001 002 001		
RN 2 S 1 S 103	SWITCH, MOMENTARY SHADOW MD DISC SWITCH, DIP, 8-POSITION	507319 495168	89536 95146 91504	507319 DYS-8 318-AG39D	001 001 004		
XU 4- 7 XU 10- 14, 25 XU 18, 26,101	SOCKET, DIP, 0.100 CTR, 24 PIN SOCKET, DIP, 0.100 CTR, 40 PIN	376236 429282	91506 09922	324-AG39D DILB40P-108	005 003 001	01	
Y 1	* CRYSTAL,8MHZ,+-0.5%,HC-18/U	702000	3,550				

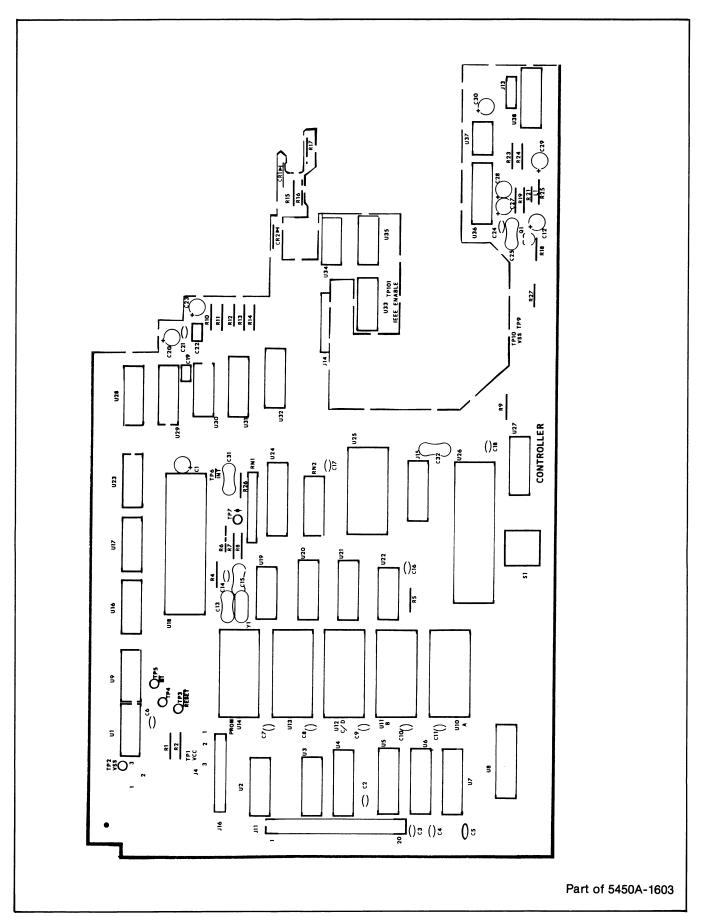


Figure 5-3. A2 Digital Assembly

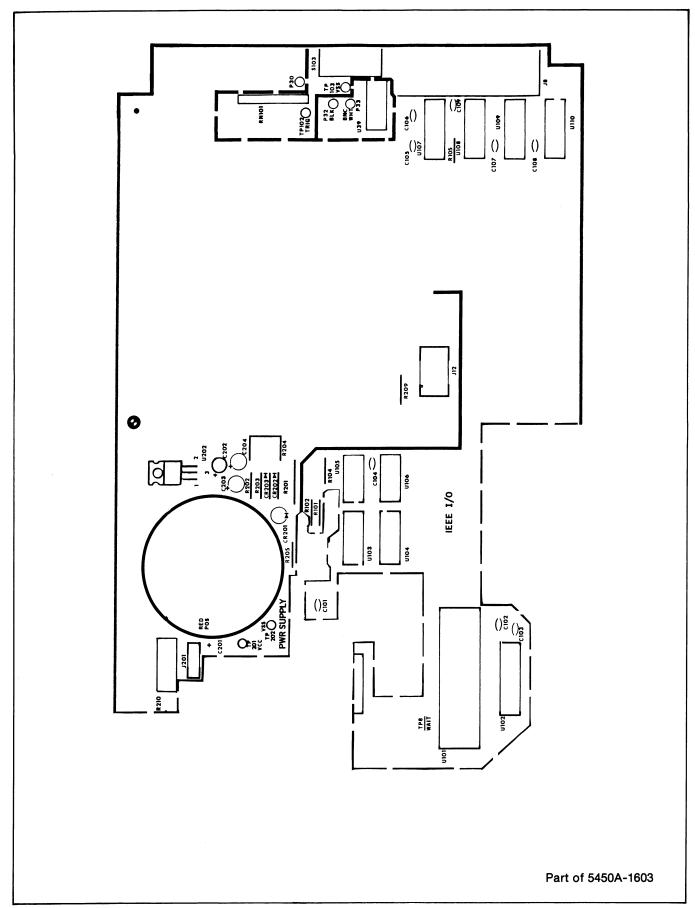


Figure 5-3. A2 Digital Assembly (cont)

					TABLE 5-5. A3 DISPLAY ASSEMBLY (SEE FIGURE 5-4.) 5450A-4003	713594					
REFERENCE DESIGNATOR A->NUMERICS>		SDESCRIPTION		FLUKE STOCK NO	CDLV	CODEOR GENERIC TYPE		R S -Q	N 0 T -E		
C	1-			-	CAP, CER, 0.022UF, +100-20%, 40V, Z5U	358325	72982	8121-A050-651-223Z	004		
Č	5	7			CAP. TA. 2.2 HF. +/-20%. 35V	585185	89536	585185	001		
DS		4			CAP, TA, 2.2 UF, +/-20%, 35V DIODE, LED, RED, 2 X 14 SEGMENTS	478065	50579	DL02614	004	Ð1	
DS					LED RED RECTANGLE PCB MOUNT	504761	14936	MV57124	005	01	
H	ĩ	•			LED, RED, RECTANGLE, PCB MOUNT SCREW, MACHINE, PHP, STEEL, 4-40X1/4	129890	73734	19022	001		
H	2				NUT, METAL 4-40 HEX, BRASS	152074	89536	152074	001		
H	3					185710		185710	001		
ij	1				CABLE, JUMPER, 20COND, 2.100, 1-86946-9	500975	89536	500975	001		
MP	i				DISPLAY PCB, BARE	713586	89536	713586	001		
MP	ż				PUSHBUTTON SQUARE	420638	89536	420638	991	01	
MP	2 3				PUSHBUTTON-SQUARE, DARK PUTTY GREY	406728	89536	406728	993		
MP	4				PUSHBUTTON-SQUARE-, WHITE	406744	89536	406744	911		
MP	5				SPACER, LED .150 LG BRKT, LED BEZEL	434399	89536	434399	005	01	
MP	6				BRKT, LED BEZEL	713826	89536	713826	001		
R	1				RES, CF, 270, +-5%, 0.25W	348789	80031	CR251-4-5P270E	991	01	
S	1				SWITCH, MOMENTARY, DISC KYBD, ILL, RD ORG	524082	89536	524082	001	Θ3	
Š	ż,	3,	5		SWITCH, MOMENTARY SHADOW MD DISC	507319	89536	507319	015		
Š			13,		·	507319					
Š	15-		•			50731 <b>9</b>					
S	4,	8			MOMENTARY SWITCH DARK DARK PUTTY GRAY	525170	89536	525170	992	01	
S	14,	20			SWITCH, MOMENTARY, DISC KYBD, ILL, LT PTY	507335	89536	507335	002		
TP		3			CONN, TAB, FASTON, PRESS-IN, 0.110 WIDE	512889	02660		903		
U	1.	11		*	IC, LSTTL, OCTAL D F/F, +EDG TRG, W/CLEAR	454892	01295	SN74LS273N	002		
U	2,	15		*	IC,BIPLR,8CHNL DRIVER	685412	89536		002		
U	7,	8,	16	×	IC, ARRAY, 7 TRANS, NPN, DARLINGTON PAIRS	454116	01295		003		
U	9			*	IC, LSTTL, QUAD 2 INPUT NAND GATE	393033	01295	SN74LS00N	001	01	
Ü	10,	12			IC, LSTTL, 8BIT ADDRSABLE LATCH, W/CLR	419242			992	01	
U	13				IC,CMOS,HEX BUFFER W/3-STATE OUTPUT	407759	12040	MM80C97N	991	01	
U	14			×	IC, VOLT REG, FIXED, +5 VOLTS, 1.5 AMPS	428847		MC7805CT	001	91	
Z	1				RES, NET, DIP, 14 PIN, 7 RES, 1K, +-5%	407445	89536	407445	001		

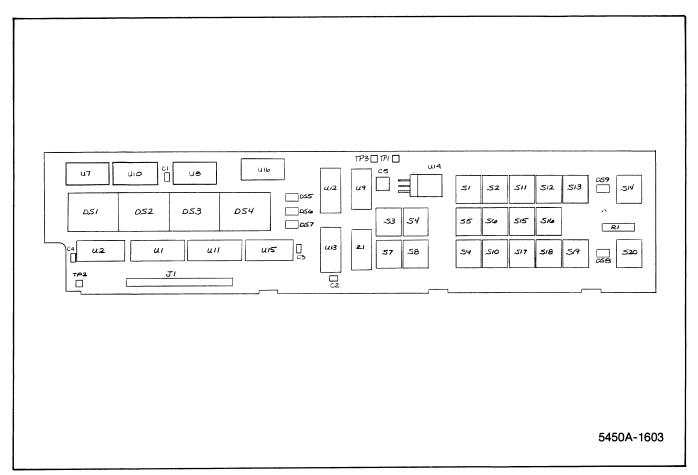


Figure 5-4. A3 Digital Assembly

	TABLE 5-6. A4 MEMORY ASSEMBLY (SEE FIGURE 5-5.) 5450A-4004	713727					N
REFERENCE DESIGNATOR A->NUMERICS>	SDESCRIPTION	FLUKE STOCK NO	MFRS SPLY CODE-		TOT QTY	R S -Q	0 T -E
C 1- 4 H 1 J 1 J 2 J 3 MP 1 MF 2 P 1 P 2 R 1 U 1 U 2- 4 U 5 X 1 X 3- 5	CAP,CER,0.025UF,+-20%,100V,Z5W SCREW,MACHINE,PHP SEMS,STEEL,4-40X1/4 CONN,PWB,HEADER,SIP,0.100,11 PIN SOCKET,DIP,0.100 CTR,24 PIN CONN,PWB,HEADER,SIP,0.100,4 POS MEMORY PCB, BARE HINGE, STANDOF,4-40 X 3/4 BRASS/CAD CABLE ASSY MEMORY EXPANDER CABLE ASSY,DIP CONN,24 CONDUCT,3.00° RES,CC,10K,+-10%,0.125W * IC,LSTTL,3-8 LINE DCDR W/ENABLE * IC, 8K X 8 EPROM SOCKET,DIP,0.100 CTR, 24 PIN SOCKET,DIP,0.100 CTR,28 PIN	185918 494666	91506 89536 89536 89536 89536	CO23B101H253M 185918 1-640456-1 324-AG39D 417329 713719 354779 527978 CAD24P02261CC003 BB1031 SN74LS138N 605030 685545 376236 328-AG39D	004 002 001 001 001 002 001 001 003 001 003	01 01 01	

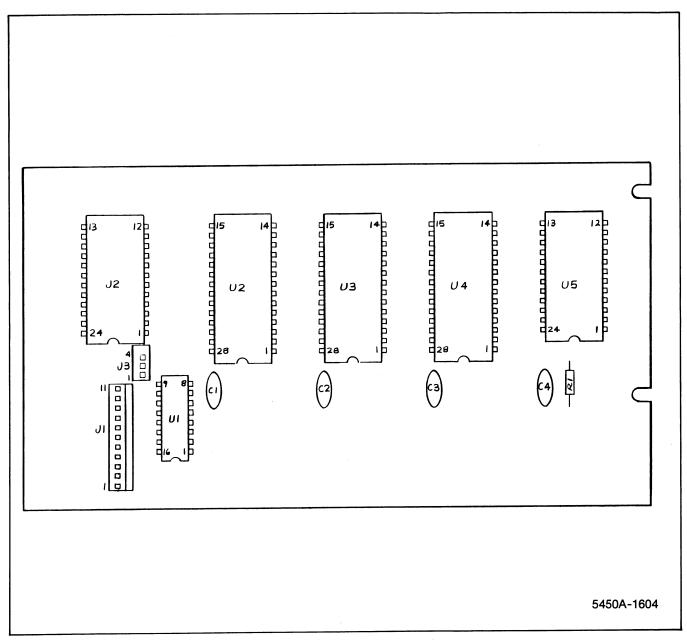


Figure 5-5. A4 Memory Assembly

TABLE 5-7. A5 POWER SUPPLY ASSEMBLY 713636 (SEE FIGURE 5-6.) 5450A-4005P

REFERENCE Designator a->numerics>		TOR	SDESCRIPTION		MFRS SPLY CODE-	MANUFACTURERS PART NUMBER OR GENERIC TYPE	TOT QTY	R S -Q	N 0 T -E
С	1,	2	CAP, CER, 0.22UF, +-20%, 50V, Z5U	309849	71590	CW3COC224K	002	Θ1	
C	3		CAP, CER, 1.0UF, +-20%, 50V, Z5U	436782	72982	8131-050-601-105M	991		
CR	1,	2	* DIODE,SI, 100 PIV, 22.0 AMP	325746	04713	MR751	002	<b>01</b>	
F	í		FUSE,1/4 X 1-1/4,FAST,2A,250V	109173	71400	AGC2	001	θ5	
F	201		FUSE,1/4 X 1-1/4,SLOW,0.5A,250V	109322	71400	MDL1-2	001	95	
Н	1		SCREW, MACHINE, PHP, STEEL, 8-32X1/4	228890	89536	228890	004		
.J	í,	2	CONN, TAB, FASTON, PRESS-IN, 0.110 WIDE	512889	02660	62395	002	01	
L	1		COMMON MODE CHOKE .200 MH	492116	89536	492116	001		
MP	1		XFMR PWB	713628	89536	713628	001		
MP	2		BRACKET, XFMR FRONT	713503	89536	<b>7135</b> 03	001		
MP	3		BRACKET, TRANSFORMER REAR	491126	89536	491126	001		
RV	1		VARISTOR,33V,+-10%,1.0MA	485391	89536	485391	001	01	
RV	2		VARISTOR, 22V, +-20%, 1.0MA	500777	89536	500777	001		
2	1,	2	SWITCH, SLIDE DPDT	234278	89536	234278	002	91	
T	1		POWER TRANSFORMER	713453	89536	713453	991		

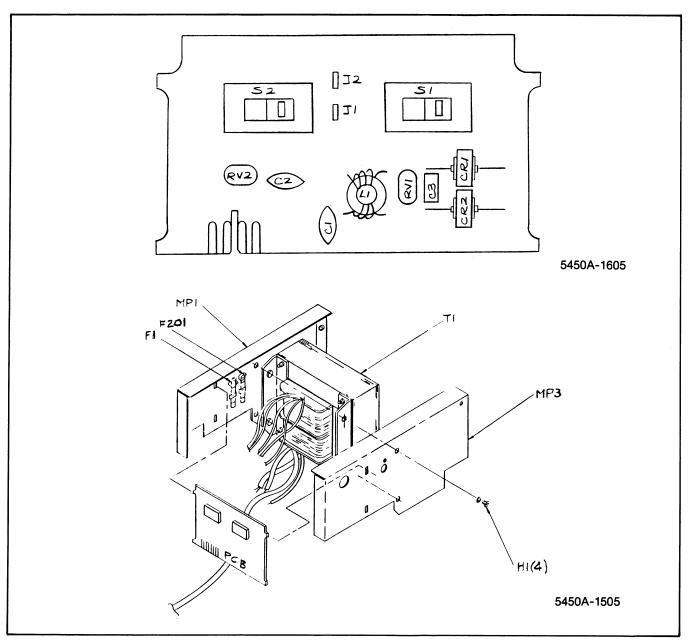


Figure 5-6. A5 Power Supply Assembly

# Section 6 Option & Accessories

# 6-1. INTRODUCTION

This section documents the options and accessories available for use with the 5450A. At this printing, there are no options, and the accessories are limited to a choice of a composite rack mounting kit or individual rack ears. Each is described in the following accessories subsection.

# 6-2. RACK ADAPTER ONLY (Y8599)

The 5450A can be rack mounted using Rack Adapter Kit Y8599. Use the following procedure to install the kit:

- 1. Peel off the nameplate decals from the front side handles of the 5450A.
- 2. Remove the front corner screws that match the hole pattern in the rack mounting ears. (Do not remove any other screws.)
- 3. Attach the rack mounting ears to the front handles of the 5450A using the screws supplied in the kit.

# 6-3. RACK ADAPTER WITH 22- TO 24-INCH SLIDES (Y8598)

The 5450A can be rack mounted using Rack Adapter Kit Y8598. Use the procedure given previously to install the rack mounting ears provided in the kit. Use the following procedure to install the slides:

- Remove the horizontal side trim decal from both sides of the instrument.
- 2. Refer to Figure 6-1. Using the screws supplied with the kit, attach the chassis section (B) of the slide kit to each side of the 5450A.
- 3. Install the cabinet section (C) of the slide kit in the equipment rack. Bar units, #10-32 KEP

nuts, and flat head and binder head #10-32 screws are enclosed for mounting the slides to the front and rear vertical cabinet members. Flat head screws are used when the front cabinet members are countersunk.

- 4. The rear support bracket (enclosed) allows adjustment from 0 to 2-1/2 inch beyond the end of the slide.
- 5. Pull the center section of the slide out through the front of the equipment rack until it locks in the extended position.
- 6. Depress the spring locks on the chassis section (B) and insert the instrument between the extended slide sections.
- Remove the four molded feet and bails from the bottom cover.

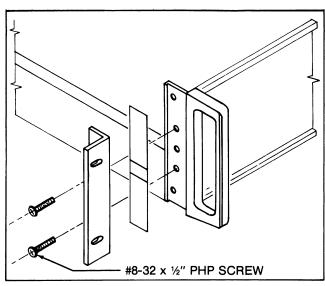


Figure 6-1. Rack Adapter Installation

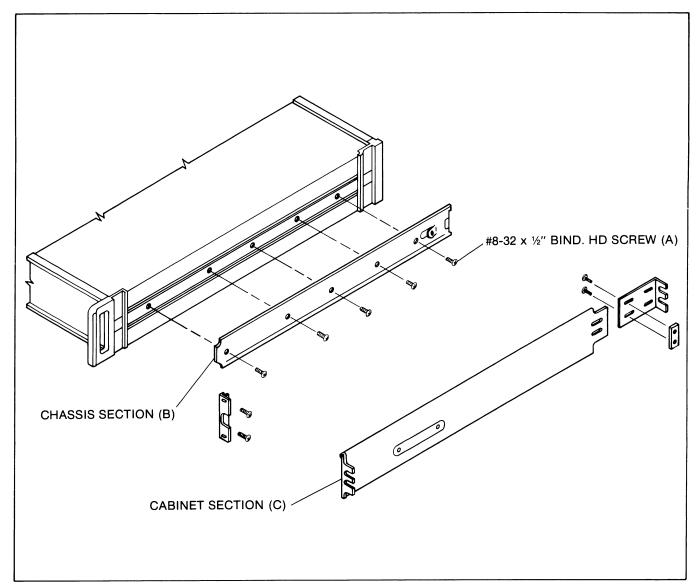


Figure 6-2. Rack Slide Installation

# Section 7 **General Information**

7-1. This section of the manual contains generalized user information as well as supplemental information to the List of Replaceable Parts contained in Section 5.

# List of Abbreviations and Symbols

ac         alternating current         Hz         hertz         pot         potentiometer           af         audio frequency         IC         integrated circuit         p-p         peak-to-peak           afd         audio-to-digital         if         intermediate frequency         ppm         parts per million           assy         assembly         in         inchreal         pmm         parts per million           AWG         american wire gauge         inti         internal         memory           B         bel         I/O         input/output         psi         pound-force per square inch           CC         Celsius         ktlz         kilohort         rf         radio-frequency           cap         capacitor         kQ         kilohorts         ms         root mean square           ccw         coramic to metal(seal)         LED         light-mitting diode         scope         osecol (time)           cerr         ceramic         If         low frequency         s or sec         second (time)           cer         ceramic         less         lesat significant bit         st         shid           cer         ceramic         mean         millited         st         shid <th>A or amp</th> <th>ampere</th> <th>hf</th> <th>high frequency</th> <th>(+) or pos</th> <th>positive</th>	A or amp	ampere	hf	high frequency	(+) or pos	positive
a/d         audio frequency         IC         intermediate frequency         p-p         peak-to-peak parts per million           asys         assembly         in         intermediate frequency         pFROM         porgrammabile read-only           AWG         american wire gauge         in1         intermediate frequency         pFROM         porgrammabile read-only           B         bel         I/O         input/output         ps         pound-force per square inch           bcd         binary coded decimal         k         kilo (10°)         RAM         radior-requency           CCP         Celsius         kLQ         kilohertz         r         radior requency           CCP         capacitor         kQ         kilohertz         m         root mean square           Cer         ceramic         to wirequency         scope         oscoll (time)           Cer         ceramic         to mitted         LED         light-emitting diode         scope         oscolloscope           cer         ceramic         to mitted         LSD         least significant bit         SH         shield           cer         ceramic         LSD         least significant bit         SH         shield           continue	•	•	Hz	hertz	pot	potentiometer
assy assembly         in inch(es)         PROM morgammable read-only memory           AWG american wire gauge Be Be bel bel bel bel binary coded decimal         i/O input/output         psi pound-force per square inch input pound-force inch input pound-forc		•	IC	integrated circuit	р-р	peak-to-peak
AWG         american wire gauge         int         internal         : internal         : internal         memory           B         bel         iv/O         input/output         pai         pound-force per square inch random-access memory           °C         Celsius         kkl         kilo (10°)         RAM         radio frequency           °C         Celsius         kV         kilobint(s)         ROM         root mean square           ccw         counterclockwise         kV         kilobint(s)         ROM         read-only memory           cer         ceramic to metal(seal)         LED         light-emitting diode         sor sec         second (time)           ctr         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ctr         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ctr         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ctr         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ctx         certmeter         LSD         least significant bit         Si         slitton	a/d	• •	if	intermediate frequency	ppm	parts per million
AWG         american wire gauge         Intl         internal         memory           B         bel         I/O         input/output         ps         memory           Dob         binary coded decimal         k         kilo (10°)         RAM         random-access memory           CC         Celsius         kHz         kilohonts         rf         radio frequency           Cop         capacitor         kQ         kilohonts         RSO         root mean square           CCW         counterclockwise         kV         kilohonts         RSO         read-only memory           Cer         ceramic         If         low frequency         s or sec         second (time)           Cer         ceramic to metal(seal)         LED         light-emitting diode         score         scelloscope         scilloscope           ckt         circuit         LSD         least significant bit         Sh         shild register           cmm         commormode rejection ratio         M         mega (10°°)         ser         shift register           cmm         commormode rejection ratio         M         mega (10°°)         ser         shift register           cmm         commormode rejection ratio         M         m	assv	assembly	in	inch(es)	PROM	programmablle read-only
Bod         bel         I/O         input/output         paid         pound-force per square inch           bod         binary coded decimal         k         kilo (10°)         RAM         random-acces memory           CC         Celsius         kHz         kilohentz         rf         radio frequency           capa         capacitor         kQ         kilohom(s)         rms         root mean square           cer         ceramic         kI         low frequency         sor sec         second (time)           cer         ceramic to metal(seal)         LED         light-emitting diode         scope         scilloscope           ckt         circuit         LSB         least significant bit         SH         shiel           cm         centimeter         LSD         least significant digit         SH         shift register           cm         composition         m         milli (10°-)         sern         shift register           cont         continue         mA         max         maximum         tb         terminal board           cv         conclosition         mA         milli (10°-)         st         shift register           cont         continue         mA         milli (10°-)	•	american wire gauge	intl	internal		memory
°C         Celsius         kHz         kilohertz         rf         radio frequency           cap         capacitor         kΩ         kilohm(s)         rms         root mean square           ccw         counterclockwise         kV         kilohom(s)         rms         root mean square           cer         ceramic         If         low frequency         s or sec         second (time)           cermet         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ckt         circuit         LSD         least significant digit         SI         shield           cm         centimeter         LSD         least significant digit         SI         shield           cm         common mode rejection ratio         M         mega (10°)         serno         serion         serion           comb         composition         m         millilimeter         serno         shift register           continue         mA         millilimeter         ta         tantalum           cont         continue         mA         maximum         tb         terminal board           cont         continue         mA         maximum         tb         term	В		I/O	input/output	psi	pound-force per square inch
Cap         capacitor         kΩ         kilohm(s)         rms         root mean square           ccw         counterclockwise         kV         kilovolt(s)         ROM         read-only memory           cer         ceramic         If         low frequency         sor sec         scond (time)           cermet         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ckt         circuit         LSB         least significant digit         SH         shield           cm         centimeter         LSD         least significant digit         SH         shield           cm         common mode rejection ratio         M         mega (10°)         sermo         serial number           comp         composition         m         milliampere(s)         Ta         tantalum           cont         continue         mA         milliampere(s)         Ta         tantalum           crt         cathode-ray tube         max         maximum         tb         terminal board           crt         cathode-ray tube         mx         maximum         tb         terminal board           cht         digital-to-analog         MHz         megabertz	bcd	binary coded decimal	k	kilo (10³)	RAM.	random-access memory
ccw         counterclockwise         kV         kilovolit(s)         ROM         read-only memory           cer         ceramic         If         low frequency         s or sec         second (time)           cermet         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ckt         circuit         LSB         least significant bit         SH         shield           cm         centimeter         LSD         least significant digit         Si         silicon           cmr         common mode rejection ratio         M         mega (10°)         sermo         shift register           com         composition         m         milli (10°°)         sr         shift register           cont         continue         mA         milliampere(s)         Ta         tantalum           cont         continue         mA         milliampere(s)         Ta         tantalum           crt         cathode-ray tube         ma         milliampere(s)         Ta         tantalum           crt         cathode-ray tube         ma         millitineer         to         temperature coefficient or           crt         didigital-to-analog         MHz         megaher	°C	Celsius	kHz	kilohertz	rf	radio frequency
Cer         ceramic to metal(seal)         LED         light-emitting diode         so or sec         second (time)           cermet         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ckt         circuit         LSD         least significant bit         SH         shield           cm         centimeter         LSD         least significant digit         SI         silicon           cmr         common mode rejection ratio         M         mega (10°)         semo         serial number           comt         continue         m         millilimpere(s)         Ta         tantalum           cont         continue         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or           dw         digital-to-analog         MHz         megahertz         ctrout         temperature compensating           dac         digital-to-analog converter         min         millimeter         tc         temperature compensating           db         decibel         decibel         mm         millimeter         tcx         temperature compensating           db	сар	capacitor	kΩ	kilohm(s)	rms	root mean square
cermet         ceramic to metal(seal)         LED         light-emitting diode         scope         oscilloscope           ckt         circuit         LSB         least significant bit         SH         shield           cmm         centimeter         LSD         least significant digit         SI         silicon           cmr         common mode rejection ratio         M         mega (10°)         semo         serial number           comp         composition         m         milli (10°2)         sr         shift register           cont         continue         mA         milliamper(s)         Ta         tantalum           cort         cathod-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or           d/a         digital-to-analog converter         min         millimeter         tcx         temperature compensating           dec         digital-to-analog converter         min         millimeter         tcx         temperature compensating           db         decible         mm         millimeter         tcx         temperature compensating           db         dericturrent	ccw	counterclockwise	kV	kilovolt(s)	ROM	read-only memory
ckt         circuit         LSB         least significant bit         SH         shield           cm         centimeter         LSD         least significant digit         Si         silicon           cmr         composition         M         mega (10°)         serno         serial number           comp         composition         m         milli (10°3)         sr         shift register           cont         continue         mA         milliampere(s)         Ta         tantalum           ct         cathode-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or           d/a         digital-to-analog         MHz         megahertz         temperature compensating           dac         digital-to-analog converter         min         minimum         texo         temperature compensating           dac         digital voltmeter         ms         millimeter         texo         ctxomperature compensating           db         decibel         mm         millimeter         tex         tex         text           dmm         digital voltmeter         MSB         most significant bit </td <td>cer</td> <td>ceramic</td> <td>If</td> <td>low frequency</td> <td>s or sec</td> <td>second (time)</td>	cer	ceramic	If	low frequency	s or sec	second (time)
cm         centimeter         LSD         least significant digit         Si         silicon           cmr         common mode rejection ratio         M         mega (10°)         serno         serial number           component         common mode rejection ratio         M         mega (10°)         serno         serial number           component         continue         mA         milliampere(s)         Ta         tantalum           cont         continue         mA         milliampere(s)         Ta         tantalum           ctromation         continue         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature compensating           da         digital voltando         MHz         megaertz         temperature compensating           dac         digital-to-analog converter         min         millimeter         texo         temperature compensating           dac         digital-to-analog converter         min         millimeter         texo         temperature compensating           dac         digital-to-analog converter         min         millimeter         texo         temperature compensating         dest           db	cermet	ceramic to metal(seal)	LED	light-emitting diode	scope	oscilloscope
cmrr         common mode rejection ratio         M         mega (10°)         serno         serial number           comp         composition         m         milliampere(s)         Ta         shift register           cnt         cathode-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or temperature compensating           dac         digital-to-analog         MHz         megahertz         temperature compensating           db         decibel         mm         millimeter         tcxo         temperature compensated           db         decibel         mm         millimeter         tcxo         temperature compensating           dc         direct current         ms         millimeter         tcxo         temperature compensating           dc         direct current	ckt	circuit	LSB	least significant bit	SH	shield
comp cont         composition continue         m         milli (10-3) max         sr         shift register           cont         continue         mA         milliampere(s)         Ta         tantalum           ct         cathode-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or temperature compensating           dac         digital-to-analog converter         min         minimum         tcxo         temperature compensating           db         decibel         mm         millimeter         crystal oscillator           db         derict current         ms         millisecond         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSB         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         mTTR         mean time to repair         ut         unit unit under test           F	cm	centimeter	LSD	least significant digit	Si	silicon
cont         continue         mA         milliampere(s)         Ta         tantalum           crt         cathode-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or temperature compensating dad digital-to-analog converter         mf         metal film         tc         temperature compensating temperature compensated or temperature compensated or temperature compensated digital decibel         mm         millimeter         crystal oscillator           db         decibel         mm         millimeter         up test point         temperature compensated           db         decibel         mm         millimeter         crystal oscillator           dc         direct current         ms         millisecond         up test point         temperature compensated           db         decibel         mm         millimeter         try         crystal oscillator           dc         direct current         ms         millisecond         up test point         up intro (10-°)           dm         digital rollmeter         ms         most significant digit         up test point         up intro (10-°)           ext         external         ms         mat time between fai	cmrr	common mode rejection ratio	M	mega (10 <sup>6</sup> )	serno	serial number
crt         cathode-ray tube         max         maximum         tb         terminal board           cw         clockwise         mf         metal film         tc         temperature coefficient or temperature compensating           dac         digital-to-analog converter         min         minimum         tcxo         temperature compensating           dB         decibel         mm         millimeter         crystal oscillator           dc         direct current         ms         millisecond         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         microsecond(s) (10-6)           dvm         digital voltmeter         MSD         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time between failures         us or μs         microsecond(s) (10-6)           ext         farad         mV         millivolt(s)         V         volt	comp	composition	m	milli (10 <sup>-3</sup> )	sr	shift register
cw         clockwise         mf         metal film         tc         temperature coefficient or temperature compensating temperature deficient or temperature compensating temperature described and digital-to-analog converter         min         minimum         texo         temperature compensated temperature compensated temperature compensated described and digital compensations.           db         decible         mm         millimeter         texo         temperature compensated corystal oscillator crystal oscillator.           dc         direct current         ms         millimeter         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10-6)           dwm         digital voltmeter         MSD         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time to repair         uut         unit under test           F         farad         mV         millivit(s)         V         voltage           FET         Field-effect transistor         MΩ         megohm(s)         var         variable           ff         flije-flop	cont	continue	mA	milliampere(s)	Та	tantalum
d/a         digital-to-analog         MHz         megahertz         temperature compensating           dac         digital-to-analog converter         min         minimum         texo         temperature compensated           dB         decibel         mm         millimeter         crystal oscillator           dc         direct current         ms         millisecond         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant digit         u fr         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time to repair         uut         unit under test           F         farad         mV         millivolt(s)         V         volt           °F         Fahrenheit         mV         multivibrator         v         voltage           FET         Field-effect transistor         MΩ         mean of (10-6)         vo         vo         voltage controlled oscillator           freq         frequency	crt	cathode-ray tube	max	maximum	tb	terminal board
dac         digital-to-analog converter         min         minimum         texx         temperature compensated crystal oscillator           dB         decibel         mm         millimeter         crystal oscillator           dc         direct current         ms         millimeter         tp         test point           dmm         digital woltmeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant bit         u or μ         micro second(s) (10-6)           elect         electrolytic         most all miles with significant bit         u or μ         unit under test           FET         farad         mV         millivolt(s)         V         voltage           FET	cw	clockwise	mf	metal film	tc	temperature coefficient or
dB         decibel         mm         millimeter         crystal oscillator           dc         direct current         ms         millisecond         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10 °°)           dvm         digital voltmeter         MSD         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10 °°)           ext         external         MTTR         mean time to repair         uut         unit under test           F         farad         mV         millivolt(s)         V         voltage           °F         Fahrenheit         mv         multivibrator         v         voltage           FET         Field-effect transistor         MΩ         megohm(s)         var         variable           ff         flip-flop         n         nano (10 °°)         vco         voltage controlled oscillator           freq         frequency         na         not applicable         vhf         very high frequency           FSN         federal stock number         NC         norma	d/a	digital-to-analog	MHz	megahertz		temperature compensating
dc         direct current         ms         millisecond         tp         test point           dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time to repair         uut         unit under test           F         farad         mV         millivolt(s)         V         voltage           FET         Fahrenheit         mv         multivibrator         v         voltage           FET         Field-effect transistor         MΩ         megohm(s)         var         variable           ff         flip-flop         n         nano (10-9)         vco         voltage controlled oscillator           freq         frequency         na         not applicable         vhf         very high frequency           FSN         federal stock number         NC         normally closed         vlf         very low frequency           g         giga (10°) <th< td=""><td>dac</td><td>digital-to-analog converter</td><td>min</td><td>minimum</td><td>tcxo</td><td>temperature compensated</td></th<>	dac	digital-to-analog converter	min	minimum	tcxo	temperature compensated
dmm         digital multimeter         MSB         most significant bit         u or μ         micro (10-6)           dvm         digital voltmeter         MSD         most significant digit         uhf         ultra high frequency           elect         electrolytic         MTBF         mean time between failures         us or μs         microsecond(s) (10-6)           ext         external         MTTR         mean time to repair         uut         unit under test           F         farad         mV         millivolt(s)         V         volt           °F         Fahrenheit         mv         multivibrator         v         voltage           FET         Field-effect transistor         MΩ         megohm(s)         var         variable           ff         flip-flop         n         nano (10-9)         vco         voltage controlled oscillator           freq         frequency         na         not applicable         vhf         very high frequency           FSN         federal stock number         NC         normally closed         vlf         very low frequency           g         gram         NO         normally open         ww         witter         witter           g         gigal (10°)	dB	decibel	mm	millimeter		crystal oscillator
dvmdigital voltmeter, electMSDmost significant digituhfultra high frequencyelectelectrolyticMTBFmean time between failuresus or $\mu$ smicrosecond(s) (10-6)extexternalMTTRmean time to repairuutunit under testFfaradmVmillivolt(s)Vvolt°FFahrenheitmvmultivibratorvvoltageFETField-effect transistorM $\Omega$ megohm(s)varvariableffflip-flopnnano (10-9)vcovoltage controlled oscillatorfreqfrequencynanot applicablevhfvery high frequencyFSNfederal stock numberNCnormally closedvlfvery low frequencyggram(-) or negnegativeWwatt(s)Ggiga (10°)NOnormally openwwwire woundgdguardnsnanosecondxfmrtransformerGegermaniumopnl amploperational amplifierxstrtransistorGHzgigahertzppico (10-1°)xtalcrystalgmvguaranteed minimum valueparaparagraphxtlocrystal oscillatorgmdgroundpcbprinted circuit board $\Omega$ ohm(s)HhenrypFpicofarad $\mu$ micro (10-6)	dc	direct current	ms	millisecond	tp	test point
elect electrolytic ext external MTTR mean time between failures us or $\mu$ s microsecond(s) (10-6) ext external MTTR mean time to repair uut unit under test $\Gamma$ farad mV millivolt(s) V volt $\Gamma$ Fahrenheit mV multivibrator V voltage $\Gamma$ Feld-effect transistor M $\Gamma$ mean time to repair variable $\Gamma$ filip-flop n nano (10-9) V vo voltage controlled oscillator frequency na not applicable V vif very high frequency $\Gamma$ federal stock number NC normally closed V vif very low frequency $\Gamma$ g gram (-) or neg negative W watt(s) $\Gamma$ G giga (10°) NO normally open ww wire wound $\Gamma$ gd guard ns nanosecond $\Gamma$ fransformer $\Gamma$ Ge germanium opni ampl operational amplifier $\Gamma$ stransformer $\Gamma$ GHz gigahertz $\Gamma$ p pico (10-1°) xtal crystal sgmv guaranteed minimum value $\Gamma$ para paragraph $\Gamma$ to crystal oscillator ohm(s) $\Gamma$ henry $\Gamma$ pF picofarad $\Gamma$ micro (10-6)	dmm	digital multimeter	MSB	most significant bit	<b>u</b> or $\mu$	micro (10 <sup>-6</sup> )
ext external MTTR mean time to repair uut unit under test F farad mV millivolt(s) V volt volt $^{\circ}$ F Fahrenheit mv multivibrator v voltage FET Field-effect transistor M $\Omega$ megohm(s) var variable ff flip-flop n nano $(10^{-9})$ vco voltage controlled oscillator freq frequency na not applicable vhf very high frequency FSN federal stock number NC normally closed vlf very low frequency g gram (-) or neg negative W watt(s) G giga $(10^9)$ NO normally open ww wire wound gd guard ns nanosecond xfmr transformer Ge germanium opnl ampl operational amplifier xstr transistor GHz gigahertz p pico $(10^{-12})$ xtal crystal gmv guaranteed minimum value para paragraph xtlo crystal oscillator $\Omega$ henry	dvm	digital voltmeter,	MSD	most significant digit	uhf	ultra high frequency
F farad $mV$ millivolt(s) $V$ volt $^{\circ}F$ Fahrenheit $mV$ multivibrator $V$ voltage $V$ voltage $V$ FET Field-effect transistor $V$ megohm(s) $V$ voltage $V$ variable $V$ flip-flop $V$ nonano $V$ voltage controlled oscillator $V$ voltage controlled oscillator $V$ frequency $V$ no normally closed $V$ very high frequency $V$ very high frequency $V$ very high frequency $V$ very low freque	elect	electrolytic	MTBF	mean time between failures	us or $\mu$ s	microsecond(s) (10 <sup>-6</sup> )
°FFahrenheitmvmultivibratorvvoltageFETField-effect transistorMΩmegohm(s)varvariableffflip-flopnnano $(10^{-9})$ vcovoltage controlled oscillatorfreqfrequencynanot applicablevhfvery high frequencyFSNfederal stock numberNCnormally closedvlfvery low frequencyggram(-) or negnegativeWwatt(s)Ggiga $(10^9)$ NOnormally openwwwire woundgdguardnsnanosecondxfmrtransformerGegermaniumopnl amploperational amplifierxstrtransistorGHzgigahertzppico $(10^{-12})$ xtalcrystalgmvguaranteed minimum valueparaparagraphxtlocrystal oscillatorgndgroundpcbprinted circuit board $\Omega$ ohm(s)HhenrypFpicofarad $\mu$ micro $(10^{-6})$	ext	external	MTTR	mean time to repair		unit under test
FET Field-effect transistor $\mathbf{M}\Omega$ megohm(s) var variable ff flip-flop $\mathbf{n}$ nano $(10^{-9})$ vco voltage controlled oscillator freq frequency $\mathbf{n}$ na not applicable vhf very high frequency FSN federal stock number $\mathbf{N}$ NC normally closed vlf very low frequency $\mathbf{g}$ gram $\mathbf{G}$ normally open $\mathbf{S}$ wax watt(s) $\mathbf{G}$ giga $(10^9)$ NO normally open $\mathbf{S}$ www wire wound $\mathbf{G}$ guard $\mathbf{S}$ nanosecond $\mathbf{S}$ transformer $\mathbf{G}$ germanium opni ampl operational amplifier $\mathbf{S}$ str transistor $\mathbf{G}$ gigahertz $\mathbf{S}$ p pico $(10^{-12})$ variable very low frequency $\mathbf{S}$ transistor $\mathbf{G}$ gigahertz $\mathbf{S}$ p printed circuit board $\mathbf{S}$ ohm(s) $\mathbf{S}$ ohm(s) $\mathbf{S}$ henry	F	farad	mV	millivolt(s)	V	volt
ff       flip-flop       n       nano (10-9)       vco       voltage controlled oscillator         freq       frequency       na       not applicable       vhf       very high frequency         FSN       federal stock number       NC       normally closed       vlf       very low frequency         g       gram       (-) or neg       negative       W       watt(s)         G       giga (10°)       NO       normally open       ww       wire wound         gd       guard       ns       nanosecond       xfmr       transformer         Ge       germanium       opnl ampl       operational amplifier       xstr       transistor         GHz       gigahertz       p       pico (10-1²)       xtal       crystal         gmv       guaranteed minimum value       para       paragraph       xtlo       crystal oscillator         gnd       ground       pcb       printed circuit board $\Omega$ ohm(s)         H       henry       pF       picofarad $\mu$ micro (10-6)	°F	Fahrenheit	mv	multivibrator	V	•
freq       frequency       na       not applicable       vhf       very high frequency         FSN       federal stock number       NC       normally closed       vlf       very low frequency         g       gram       (-) or neg       negative       W       watt(s)         G       giga (10°)       NO       normally open       ww       wire wound         gd       guard       ns       nanosecond       xfmr       transformer         Ge       germanium       opnl ampl       operational amplifier       xstr       transistor         GHz       gigahertz       p       pico (10-1²)       xtal       crystal         gmv       guaranteed minimum value       para       paragraph       xtlo       crystal oscillator         gnd       ground       pcb       printed circuit board $\Omega$ ohm(s)         H       henry       pF       picofarad $\mu$ micro (10-6)	FET	Field-effect transistor	MΩ	megohm(s)	var	
FSN federal stock number NC normally closed vif very low frequency gram (-) or neg negative W watt(s)  G giga $(10^9)$ NO normally open ww wire wound gd guard ns nanosecond xfmr transformer  Ge germanium opnl ampl operational amplifier xstr transistor  GHz gigahertz p pico $(10^{-12})$ xtal crystal gmv guaranteed minimum value para paragraph xtlo crystal oscillator gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro $(10^{-6})$	ff	flip-flop	n	nano (10 <sup>-9</sup> )		· ·
g gram (-) or neg negative $P$ watt(s)  G giga (10°) NO normally open $P$ ww wire wound  gd guard $P$ nanosecond $P$ transformer  Ge germanium $P$ operational amplifier $P$ sate $P$ transistor  GHz gigahertz $P$ pico (10-12) $P$ sate $P$ crystal  gmv guaranteed minimum value $P$ para paragraph $P$ sate $P$ sate $P$ crystal oscillator  gnd ground $P$ peb picofarad $P$ micro (10-6)	freq	frequency	na	not applicable		, , , ,
G giga $(10^9)$ NO normally open ww wire wound gad guard ns nanosecond xfmr transformer Ge germanium opnl ampl operational amplifier xstr transistor GHz gigahertz p pico $(10^{-12})$ xtal crystal gmv guaranteed minimum value para paragraph xtlo crystal oscillator gnd ground pcb printed circuit board $\Omega$ ohm(s) H henry pF picofarad	FSN	federal stock number	NC	normally closed	vif	very low frequency
gd guard ns nanosecond xfmr transformer  Ge germanium opnl ampl operational amplifier xstr transistor  GHz gigahertz p pico $(10^{-12})$ xtal crystal  gmv guaranteed minimum value para paragraph xtlo crystal oscillator  gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro $(10^{-6})$	g	gram	(-) or neg	negative	W	` '
Ge germanium opnl ampl operational amplifier xstr transistor  GHz gigahertz p pico $(10^{-12})$ xtal crystal  gmv guaranteed minimum value para paragraph xtlo crystal oscillator  gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro $(10^{-6})$	G	giga (10º)	NO	normally open	ww	
GHz gigahertz p pico $(10^{-12})$ xtal crystal gmv guaranteed minimum value para paragraph xtlo crystal oscillator gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro $(10^{-6})$	gd	guard	ns			
gnw guaranteed minimum value para paragraph xtlo crystal oscillator gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro (10 <sup>-6</sup> )	Ge	germanium	opni ampi			
gnd ground pcb printed circuit board $\Omega$ ohm(s)  H henry pF picofarad $\mu$ micro (10 <sup>-6</sup> )	GHz	gigahertz	р	pico (10 <sup>-12</sup> )		· ·
H henry pF picofarad $\mu$ micro (10 <sup>-6</sup> )	gmv	guaranteed minimum value	para	paragraph		•
n nemy provided	gnd	ground	pcb	printed circuit board		` '
hd heavy duty pn part number	н	henry	pF	picofarad	$\mu$	micro (10 <sup>-6</sup> )
	hd	heavy duty	pn	part number		

00213

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01537

Motorola Communications & Electronics Inc Franklin Park, Illinois

RCL Electronics Inc. Manchester, New Hampshire

01730

Replaced by 73586

01884 Use 56289

Sprague Electric Co. Dearborn Electronic Div. Lockwood, Florida

Ferroxcube Corp. Saugerties, New York

General Instrument Corp. Harris ASW Div. Westwood, Maine

02395

Rason Mfg. Co. Brooklyn, New York

Snelgrove, C.R. Co., Ltd. Don Mills, Ontario, Canada M3B 1M2

02606 Fenwal Labs

Div. of Travenal Labs. Morton Grove, Illinois 02660

Bunker Ramo Corp., Conn Div. Formerly Amphenol-Borg Electric Corp. Broadview, Illinois

Areo Capacitors, Inc. Chatsworth, California

03508

General Electric Co. Semiconductor Products Syracuse, New York

03614

Replaced by 71400

Replaced by 44655

03797 Eldema Div.

Genisco Technology Corp. Compton, California

Transistron Electronic Corp. Wakefield, Massachusetts

KDI Pyrofilm Corp. Whippany, New Jersey

Clairex Electronics Div. Clairex Corp. Mt. Vernon, New York

03980

Muirhead Inc.

Mountainside, New Jersey

Arrow Hart Inc. Hartford, Connecticut

Replaced by 72136

Replaced by 81312

Essex International Inc. Wire & Cable Div. Anaheim, California

04221

Aemco, Div. of Midtex Inc. Mankato, Minnesota

04222

AVX Ceramics Div. AVX Corp. Myrtle Beach, Florida

Telonic Industries Laguna Beach, California

Replaced by 75376

Motorola Inc. Semiconductor Products

Phoenix, Arizona

04946

Standard Wire & Cable Los Angeles, California

05082

Replaced by 94988

Jonathan Mfg. Co. Fullerton, California

Components Corp. now Corcom, Inc. Chicago, Illinois

05277

Westinghouse Electric Corp. Semiconductor Div. Youngwood, Pennsylvania

05278

Replaced by 43543

Southwest Machine & Plastic Co. Glendora, California

Union Carbide Corp. Materials Systems Div. New York, New York

05571 Use 56289

Sprague Electric Co.

Pacific Div. Los Angeles, California

Viking Industries Chatsworth, California

05704

Replaced by 16258

Wakefield Engineering Inc. Wakefield, Massachusetts

General Electric Co. Electronic Capacitor & Battery Products Dept. Columbia, South Carolina

06136

Replaced by 63743

06383

Panduit Corp. Tinley Park, Illinois

06473

Bunker Ramo Corp. Amphenol SAMS Div. Chatsworth, California

Beede Electrical Instrument Co. Penacook, New Hampshire

06739 Electron Corp. Littleton, Colorado

06743 Clevite Corp. Cleveland, Ohio 06751

Components, Inc. Semcor Div. Phoenix, Arizona

06860

Gould Automotive Div. City of Industry, California

Vernitron Corp., Piezo Electric Div. Formerly Clevite Corp., Piezo Electric Div. Bedford, Ohio

06980

Eimac Div. Varian Associates San Carlos, California

07047

The Ross Milton Co. South Hampton, Pennsylvania

Replaced by 14674

Westinghouse Electric Corp., Electronic Tube Div. Horsehead, New York

07233

TRW Electronic Components Cinch Graphic City of Industry, California

07256

Silicon Transistor Corp. Div. of BBF Group Inc. Chelmsford, Massachusetts

07261

Aumet Corp.

Culver City, California

Fairchild Semiconductor Div. of Fairchild Camera & Instrument Corp. Mountain View, California

07344

Bircher Co., Inc. Rochester, New York

07597

Burndy Corp. Tape/Cable Div. Rochester, New York

Lerma Engineering Corp. Northampton, Massachusetts

Teledyne Semiconductor Formerly Continental Device Hawthorne, California

07933 Use 49956 Raytheon Co.

Semiconductor Div. HQ Mountain View, California

08225

Industro Transistor Corp. Long Island City, New York

08261

Spectra Strip Corp. Garden Grove, California

Reliance Mica Corp. Brooklyn, New York

General Electric Co. Miniature Lamp Products Dept Cleveland, Ohio

08863

Nylomatic Corp. Norrisville, Pennsylvania

Use 53085 Skottie Electronics Inc. Archbald, Pennsylvania

G.E. Co. Semi-Conductor Products Dept. Power Semi-Conductor Products OPN Sec. Auburn, New York

09353

C and K Components Watertown, Massachusetts

Scientific Components, Inc. Santa Barbara, California

Burndy Corp. Norwalk, Connecticut

Dale Electronics Inc. Yankton, S. Dakota

Barker Engineering Corp. Formerly Amerace, Amerace ESNA Corp. Kenilworth, New Jersey

CTS of Berne

Berne, Indiana

CTS Keene Inc. Paso Robles, California

CBS Electronic Div. Columbia Broadcasting System Newburyport, Minnesota

11403

Best Products Co. Chicago, Illinois

Keystone Columbia Inc. Warren, Michigan

Teledyne Relays Hawthorne, California

General Instrument Corp. Rectifier Division Hicksville, New York

11726

Qualidyne Corp. Santa Clara, California

Chicago Rivet & Machine Co. Bellwood, Illinois

12040

National Semiconductor Corp. Danburry, Connecticut

12060

Diodes, Inc. Chatsworth, California

Philadelphia Handle Co. Camden, New Jersey

12300

Potter-Brumfield Div. AMF Canada LTD. Guelph, Ontario, Canada

Presin Co., Inc. Shelton, Connecticut

12327

Freeway Corp. formerly Freeway Washer & Stamping Co. Cleveland, Ohio

The Budd Co. Polychem Products Plastic Products Div. Bridgeport, Pennsylvania

U.S. Terminals Inc. Cincinnati, Ohio

Hamlin Inc. Lake Mills, Wisconsin

Clarostat Mfg. Co. Dover, New Hampshire

James Electronics Chicago, Illinois

12856

Micrometals

Sierra Madre, California

Dickson Electronics Corp. Scottsdale, Arizona

12969

Unitrode Corp.

Watertown, Massachusetts

Thermalloy Co., Inc. Dallas, Texas

Solitron Devices Inc. Tappan, New York

Amphenol Cadre Div. Bunker-Ramo Corp. Los Gatos, California

13606

Use 56289 Sprague Electric Co. Transistor Div. Concord, New Hampshire

Replaced by 23732

Semtech Corp. Newbury Park, California

14140

Edison Electronic Div. Mc Gray-Edison Co. Manchester, New Hampshire

Cal-R-Inc. formerly California Resistor, Corp. Santa Monica, California

14298

American Components, Inc. an Insilco Co. Conshohocken, Pennsylvania

Cornell-Dublier Electronics Division of Federal Pacific Electric Co. Govt. Control Dept. Newark, New Jersey

14752

Electro Cube Inc. San Gabriel, California

14869

Replaced by 96853

14936

General Instrument Corp. Semi Conductor Products Group Hicksville, New York

Elec-Trol Inc. Saugus, California

15801

Fenwal Electronics Inc. Div. of Kidde Walter and Co., Inc. Framingham, Massachusetts

15818

Teledyne Semiconductors, formerly Amelco Semiconductor Mountain View, California

Litton Systems Inc. Useco Div. formerly Useco Inc. Van Nuys, California

International Business Machines Corp. Essex Junction, Vermont

Replaced by 14140

16258

Space-Lok Inc. Burbank, California 16299

Corning Glass

Electronic Components Div. Raleigh, North Carolina

16332

Replaced by 28478

16473

Cambridge Scientific Ind. Div. of Chemed Corporation Cambridge, Maryland

16742

Paramount Plastics Fabricators, Inc. Downey, California

16758 Delco Electronics Div. of General Motors Corp.

Kokomo, Indiana 17001 Replaced by 71468

Circuit Structures Lab. Burbank, California

High Pressure Eng. Co., Inc. Oklahoma City, Oklahoma

Atlantic Semiconductors, Inc. Asbury Park, New Jersey

Siliconix, Inc. Santa Clara, California

Replaced by 14140

18178

Vactec Inc.

Maryland Heights, Missouri

18324

Signetics Corp. Sunnyvale, California

18612

Vishay Resistor Products Div. Vishay Intertechnology Inc. Malvern, Pennsylvania

18736 Voltronics Corp. Hanover, New Jersey

GTE Sylvania Inc. Precision Material Group Parts Division Titusville, Pennsylvania

Perine Machinery & Supply Co. Seattle, Washington

19701

Electro-Midland Corp. Mepco-Electra Inc. Mineral Wells, Texas

20584

Enochs Mfg. Inc. Indianapolis, Indiana

Self-Organizing Systems, Inc. Dallas, Texas

Bucheye Stamping Co. Columbus, Ohio

Solitron Devices Inc. Transistor Division Riveria Beach, Florida

22767

**ITT Semiconductors** Palo Alto, California

Product Comp. Corp. Mount Vernon, New York

23732 Tracor Inc.

Rockville, Maryland

Stanford Applied Engrng. Santa Clara, California

Pamotor Div., Wm. J. Purdy Co. Burlingame, California

Replaced by 94222

Analog Devices Inc. Norwood, Massachusetts

General Radio Concord, Massachusetts

Lenox Fugle Electronics Inc. South Plainfield, New Jersey

Siemen Corp. Isilen, New Jersey

Amperex Electronic Corp. Semiconductor & Micro-Circuits Div. Slatersville, Rhode Island

National Semiconductor Corp. Santa Clara, California

Molex Products Downers Grove, Illinois

Minnesota Mining & Mfg. Co. Consumer Products Div. St. Paul, Minnesota

28425

Serv-/-Link formerly Bohannan Industries Fort Worth, Texas

28478

Deltrol Controls Div. Deltrol Corporation Milwaukee, Wisconsin 28480

Hewlett Packard Co. Corporate HQ Palo Alto, California

Heyman Mfg. Co. Kenilworth, New Jersey

Monsanto, Co., Inc. Santa Clara, California

Stackpole Components Co. Raleigh, North Carolina

AB Enterprise Inc. Ahoskie, North Carolina

Illinois Tool Works, Inc. Chicago, Illinois

31091

Optimax Inc. Colmar, Pennsylvania

32539

Mura Corp. Great Neck, New York

Griffith Plastic Corp. Burlingame, California

Advanced Mechanical Components Northridge, California

Erie Technological Products, Inc. Frequency Control Div. Carlisle, Pennsylvania

32997

Bourns Inc.

Trimpot Products Division Riverside, California

33173

General Electric Co. Products Dept. Owensboro, Kentucky

34333

Silicon General Westminister, California

Advanced Micro Devices Sunnyvale, California

34802

Electromotive Inc. Kenilworth, New Jersey

P.R. Mallory & Co., Inc. Indianapolis, Indiana

42498

National Radio Melrose, Massachusetts 43543 Nytronics Inc. Transformer Co. Div. Geneva, New York

44655

Ohmite Mfg. Co. Skokie, Illinois

RCA Corp. New York, New York

Raytheon Company Lexington, Massachusetts

Mostek Corp. Carrollton, Texas

Litronix Inc. Cupertino, California

Scientific Components Inc. Linden, New Jersey

Sangamo Electric Co. Springfield, Illinois

Cutler-Hammer Inc. formerly Shallcross, A Cutter-Hammer Co. Selma, North Carolina

Simpson Electric Co. Div. of Am. Gage and Mach. Co. Elgin, Illinois

Sprague Electric Co. North Adams, Massachusetts

Superior Electric Co. Bristol, Connecticut

Torin Corp. formerly Torrington Mfg. Co. Torrington, Connecticut

Ward Leonard Electric Co., Inc. Mount Vernon, New York

West Mfg. Co. San Francisco, California

Weston Instruments Inc. Newark, New Jersey

Winslow Tele-Tronics Inc. Eaton Town, New Jersey

Atlantic India Rubber Works Chicago, Illinois

Amperite Company Union City, New Jersey 70903 Belden Corp. Geneva, Illinois

Birnback Radio Co., Inc. Freeport, New York

Bussmann Mfg. Div. of McGraw-Edison Co. Saint Louis, Missouri

71450 CTS Corp.

Elkhart, Indiana

ITT Cannon Electric Inc. Santa Ana, California

Clare, C.P. & Co. Chicago, Illinois

Centrelab Electronics Div. of Globe Union Inc. Milwaukee, Wisconsin

Coto Coil Co., Inc. Providence, Rhode Island

Chicago Miniature Lamp Works Chicago, Illinois

**TRW Electronics Components** Cinch Connector Operations Div. Elk Grove Village Chicago, Illinois

72005

Wilber B. Driver Co. Newark, New Jersey

Replaced by 06980

Electro Motive Mfg. Co. Williamantic, Connecticut

72259 Nytronics Inc.

Pelham Manor, New Jersey

Dialight Div.

Amperex Electronic Corp. Brooklyn, New York

G.C. Electronics Div. of Hydrometals, Inc. Brooklyn, New York

Replaced by 90303

Dzus Fastener Co., Inc. West Islip, New York

Gulton Ind. Inc. Gudeman Div. Chicago, Illinois

72982

Erie Tech. Products Inc. Erie, Pennsylvania

73138

Bechman Instrument Inc. Helipot Division Fullerton, California

Hughes Aircraft Co. Electron Dynamics Div. Torrance, California

Amperex Electronic Corp. Hicksville, New York

73559

Carling Electric Inc. West Hartford, Connecticut

Circle F Industries Trenton, New Jersey

73734

Federal Screw Products, Inc. Chicago, Illinois

73743

Fischer Special Mfg. Co. Cincinnati, Ohio

73899

JFD Electronics Co. Components Corp. Brooklyn, New York

Guardian Electric Mfg. Co. Chicago, Illinois

Quan Nichols Co. Chicago, Illinois

Radio Switch Corp. Marlboro, New Jersey

Signalite Div.

General Instrument Corp. Neptune, New Jersey

74306

Piezo Crystal Co. Carlisle, Pennsylvania

Hoyt Elect. Instr. Works Penacook, New Hampshire

Johnson E.F., Co. Waseca, Minnesota

75042

TRW Electronics Components **IRC Fixed Resistors** Philadelphia, Pennsylvania

Kurz-Kasch Inc. Dayton, Ohio

CTS Knights Inc. Sandwich, Illinois 75382

Kulka Electric Corp. Mount Vernon, New York

Littlefuse Inc. Des Plaines, Illinois

Oak Industries Inc. Switch Div. Crystal Lake, Illinois

77342 AMF Inc.

Potter & Brumfield Div. Princeton, Indiana

General Instrument Corp. Rectifier Division Brooklyn, New York

Rubbercraft Corp. of CA. LTD. Torrance, California

Shakeproof Div. of Illinois Tool Works Inc.

Elgin, Illinois

Sigma Instruments, Inc. South Braintree, Massachusetts

Stackpole Carbon Co. Saint Marys, Pennsylvania

Eaton Corp. Engineered Fastener Div. Tinnerman Plant Cleveland, Ohio

Waldes Kohinoor Inc. Long Island City, New York

Western Rubber Company Goshen, Indiana

Zierick Mfg. Corp. Mt. Kisko, New York

80031 Electro-Midland Corp.

Mepco Div. A North American Phillips Co. Norristown, New Jersey

LFE Corp., Process Control Div. formerly API Instrument Co. Chesterland, Ohio

80183

Use 56289 Sprague Products North Adams, Massachusetts

Bourns Inc., Instrument Div. Riverside, California

80583

Hammarlund Mfg. Co., Inc. Red Bank, New Jersey

Arnold Stevens, Inc. South Boston, Massachusetts

Grayhill, Inc. La Grange, Illinois

Winchester Electronics Div. of Litton Industries Inc. Oakville, Connecticut

Therm-O-Disc Inc. Mansfield, Ohio

81483

International Rectifier Corp. Los Angeles, California

Korry Mfg. Co. Seattle, Washington

81741

Chicago Lock Co. Chicago, Illinois

Palmer Electronics Corp. South Gate, California

82389

Switchcraft Inc. Chicago, Illinois

North American Phillips Controls Corp. Frederick, Maryland

Roanwell Corp. New York, New York

Rotron Inc.

Woodstock, New York

ITT Royal Electric Div. Pawtucket, Rhode Island

83003

Varo Inc. Garland, Texas

The Carr Co., United Can Div. of TRW Cambridge, Massachusetts

83298

Bendix Corp. Electric Power Div. Eatontown, New Jersey

83330

Herman H. Smith, Inc. Brooklyn, New York

83478

Rubbercraft Corp. of America, Inc. West Haven, Connecticut 83594

Burroughs Corp. Electronic Components Div.

Plainfield, New Jersey

83740

Union Carbide Corp. Battery Products Div.

formerly Consumer Products Div. New York, New York

84171

Arco Electronics Great Neck, New York

**TRW Electronic Components TRW Capacitors** Ogallala, Nebraska

84613

Fuse Indicator Corp. Rockville, Maryland

84682

Essex International Inc. Industrial Wire Div. Peabody, Massachusetts

86577

Precision Metal Products of Maiden Inc. Stoneham, Massachusetts

86684

Radio Corp. of America Electronic Components Div. Harrison, New Jersey

86928

Seastrom Mfg. Co., Inc. Glendale, California

87034

Illuminated Products Inc. Subsidiary of Oak Industries Inc. Anahiem, California

88219

Gould Inc. Industrial Div. Trenton, New Jersey

88245 Litton Systems Inc. Useco Div. Van Nuys, California

Cornell-Dubilier Electronic Div. Federal Pacific Co. Fuquay-Varian, North Carolina

88486

Plastic Wire & Cable Jewitt City, Connecticut

88690

Replaced by 04217

John Fluke Mfg. Co., Inc. Seattle, Washington

G.E. Co., Newark Lamp Works Newark, New Jersey

90201 Mallory Capacitor Co. Div. of P.R. Mallory Co., Inc. Indianapolis, Indiana

90211 Use 56365

90211 Use 56365 Square D Co. Chicago, Illinois

90215 Best Stamp & Mfg. Co. Kansas City, Missouri

90303 Mallory Battery Co. Div. of Mallory Co., Inc. Tarrytown, New York

91094 Essex International Inc. Suglex/IWP Div. Newmarket, New Hampshire

91293 Johanson Mfg. Co. Boonton, New Jersey

91407 Replaced by 58474

91502 Associáted Machine Santa Clara, California

91506 Augat Inc. Attleboro, Massachusetts

91637 Dale Electronics Inc. Columbus, Nebraska

91662 Elco Corp. Willow Grove, Pennsylvania

91737 Use 71468 Gremar Mfg. Co., Inc. ITT Cannon/Gremar Santa Ana, California

91802 Industrial Devices, Inc. Edgewater, New Jersey

91833 Keystone Electronics Corp. New York, New York 91836 King's Electronics Co., Inc. Tuckahoe, New York

91929 Honeywell Inc. Micro Switch Div. Freeport, Illinois

91934 Miller Electric Co., Inc. Div. of Aunet Woonsocket, Rhode Island

92194 Alpha Wire Corp. Elizabeth, New Jersey

93332 Sylvania Electric Products Semiconductor Products Div. Woburn, Massachusetts

94145 Replaced by 49956

94154 Use 94988 Wagner Electric Corp. Tung-Sol Div. Newark, New Jersey

94222 Southco Inc. formerly South Chester Corp. Lester, Pennsylvania

95146 Alco Electronic Products Inc. Lawrence, Massachusetts

95263 Leecraft Mfg. Co. Long Island City, New York

95264 Replaced by 98278

95275 Vitramon Inc. Bridgeport, Connecticut

95303 RCA Corp. Receiving Tube Div. Cincinnati, Ohio

95348 Gordo's Corp. Bloomfield, New Jersey 95354 Methode Mfg. Corp. Rolling Meadows, Illinois

95712 Bendix Corp. Electrical Components Div. Microwave Devices Plant Franklin, Indiana

95987 Weckesser Co. Inc. Chicago, Illinois

96733 San Fernando Electric Mfg. Co. San Fernando, California

96853
Gulton Industries Inc.
Measurement and Controls Div.
formerly Rustrak Instruments Co.
Manchester, New Hampshire

Thomson Industries, Inc. Manhasset, New York

97540 Master Mobile Mounts, Div. of Whitehall Electronics Corp. Ft. Meyers, Florida

97913 Industrial Electronic Hardware Corp. New York, New York

97945 Penwalt Corp. SS White Industrial Products Div. Piscataway, New Jersey

97966 Replaced by 11358

98094 Replaced by 49956

98159 Rubber-Teck, Inc. Gardena, California

Malco A Microdot Co., Inc. Connector & Cable Div. Pasadena, California 98291 Sealectro Corp. Mamaroneck, New York

98388 Royal Industries Products Div. San Diego, California

98743 Replaced by 12749

98925 Replaced by 14433

99120 Plastic Capacitors, Inc. Chicago, Illinois

99217 Bell Industries Elect. Comp. Div. formerly Southern Elect. Div. Burbank, California

99392 STM Oakland, California

99515 ITT Jennings Monrovia Plant Div. of ITT Jennings formerly Marshall Industries Capacitor Div. Monrovia, California

99779 Use 29587 Bunker-Ramo Corp. Barnes Div. Landsdowne, Pennsylvania

99800 American Precision Industries Inc. Delevan Division East Aurora, New York

99942 Centrelab Semiconductor Centrelab Electronics Div. of Globe-Union Inc. El Monte, California

Toyo Electronics (R-Ohm Corp.) Irvine, California

National Connector Minneapolis, Minnesota

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NJ, Paramus (201) 262-9550
TX, Dallas (214) 869-0311
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Elmeasco Instruments Ptv. Ltd. P.O. Box 623 12 Maroondah Highway Ringwood, Victoria 3134 Australia

Tel: (3) 879-2322, TLX: (790) AA36206

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G.P.O. Box 1240 Adelaide, South Australia 5001 Tel: (8) 271-1839

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Walter Rekirsch Elektronische Gerate GmbH & Co. Vertrieb KG Obachgasse 28 1220 Vienna, Austria Tel: (222) 235555, TLX: (847) 134759

Bahrain Rasma

P.O. Box 5701 Manama, Bahrain Tel: 251-364; TLX: (955) 9003

Bangladesh •

Motherland Corporation 24 Hatkhola Road, Tikatuli Dacca-3, Bangladesh Tel: 257249 or 255776 TLX: (950) 642022

Belgium = Fluke (Belgium) S.A./N.V.

6 Rue de Geneve 1140 Brussels Belgium Tel: (2) 2164090, TLX: (846) 26312

Bolivia • Coasin Bolivia S.R.L.

Casilla 7295 La Paz. Bolivia Tel: (2) 40962, TLX: (336) 5255

Brazil ● Hi-Tek Eletronica Ltda. Al. Amazonas 422, Alphaville, Barueri, CEP 06400, Sao Paulo, Brazil Tel: (11) 421-5477, TLX: (391) 01135589

Brunei ● Rank O'Connor's Berhad No. 8 Blk D Sufri Shophouse Complex Mile 1 Jalan Tutong Bandar Seri Begawan Negara Brunei Darussalam Tel: (2) 23109 or 23557 TLX: (799) 2265 RANKOC

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Allan Crawford Assoc., Ltd. 2625 Queensview Drive Ottawa, Ontario K2B 8K2 Canada Tel: (613) 596-9300, TLX: 0533600

Allan Crawford Assoc., Ltd.

Suite No. 106

4180 Lougheed Hwy Burnaby, British Columbia V5C 6A7 Canada

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Allan Crawford Assoc., Ltd. 1935 30th Avenue, N.E. Calgary, Alberta T2E 6Z5 Canada Tel: (403) 230-1341, TLX: 03821186

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China, Peoples Republic of •

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Sistemas E Instrumentacion, Ltda. Carrera 13, No. 37-43, Of. 401 Ap. Aereo 29583 Bogota DE, Colombia Tel: 232-4532, TLX: (396) 45787

Cyprus =

Chris Radiovision, Ltd. P.O. Box 1989 Nicosia, Cyprus Tel: (21) 66121, TLX: (826) 2395

Cyprus, Northern Ucok Buroteknik 2C & 2D Muftu Ziyai Street Lefkosa, Northern Cyprus Mersin 10, Turkey Tel: (741) 357-20-71777

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Amtest Associates Ltd. Clarence House 31 Clarence Street Staines, Middlesex TW18 4SY England Tel: (784) 63555, TLX: (851) 928855

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# Section 8 Manual Change Information

# 8-1. INTRODUCTION

This section contains information necessary to backdate this manual to conform to earlier printed circuit assembly (PCA) configurations. To identify the configuration of the PCAs used in your instrument, refer to the revision letter (marked in ink) on the component side of each PCA. Table 8-1 defines the PCA revision levels documented in this manual.

# 8-2. NEWER INSTRUMENTS

As changes and improvements are made to the instrument, they are identified by incrementing the revision letter marked on the affected PCA. These

changes are documented on a supplemental change/errata sheet which, when applicable, is inserted at the front of the manual.

# 8-3. OLDER INSTRUMENTS

To backdate this manual to conform with earlier assembly revision levels, perform the changes indicated in Table 8-1.

# 8-4. CHANGES

There are no backdating changes at this printing. All PCA assemblies are documented at their original revision level.

To adapt manual to earlier rev configurations perform changes Ref Fluke in desending order (by no.), ending with change under desired rev letter Assembly Or Part Option Name ABCDEFGHJKLMNP No. No. Χ 713545 Analog PCA **A1** 713578 Χ • Digital PCA A2 713602 Х Display PCA **A3** 713610 • 1x Memory PCA Α4 • X 713644 Power Supply PCA A5 -

Table 8-1. Manual Status and Backdating Information

X = The PCB revision levels documented in this manual.

These revision letters were never used in the instrument.

<sup>-=</sup> No revision letter on the PCB.

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# Section 9 Schematic Diagrams

FIGURE	TITLE	DRAWING NO.	PAGE
8-1.	A1 Analog PCA	5450A-1001	9-2
8-2.	A2 Digital PCA	5450A-1002	9-5
8-3.	A3 Display PCA	5450A-1003	9-10
8-4.	A4 Memory PCA	5450A-1004	9-12
8-5	A5 Power Supply PCA	5450A-1005	9-13

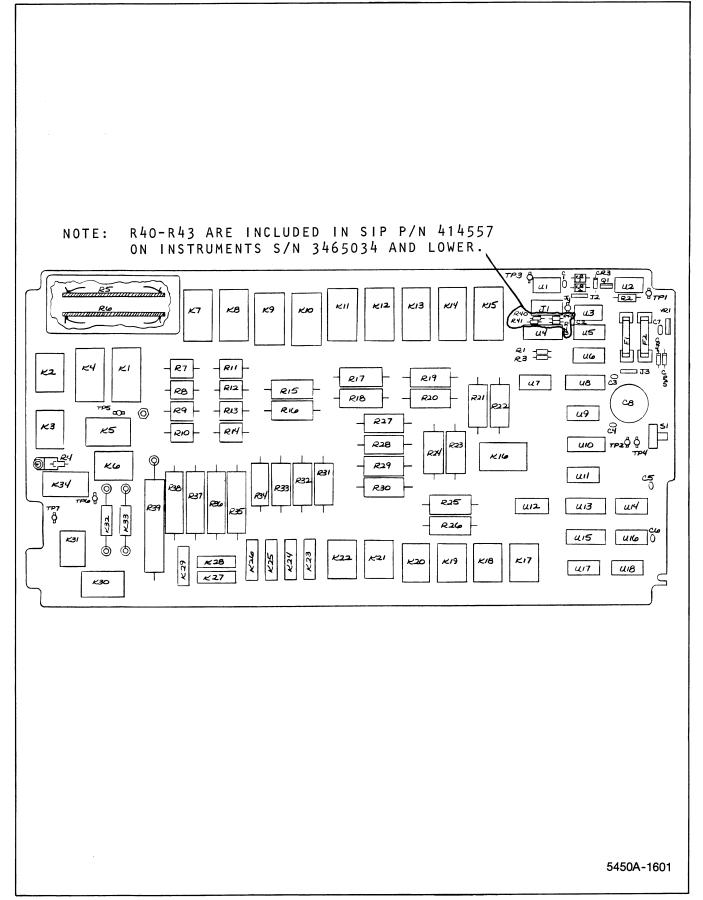


Figure 9-1. A1 Analog PCA

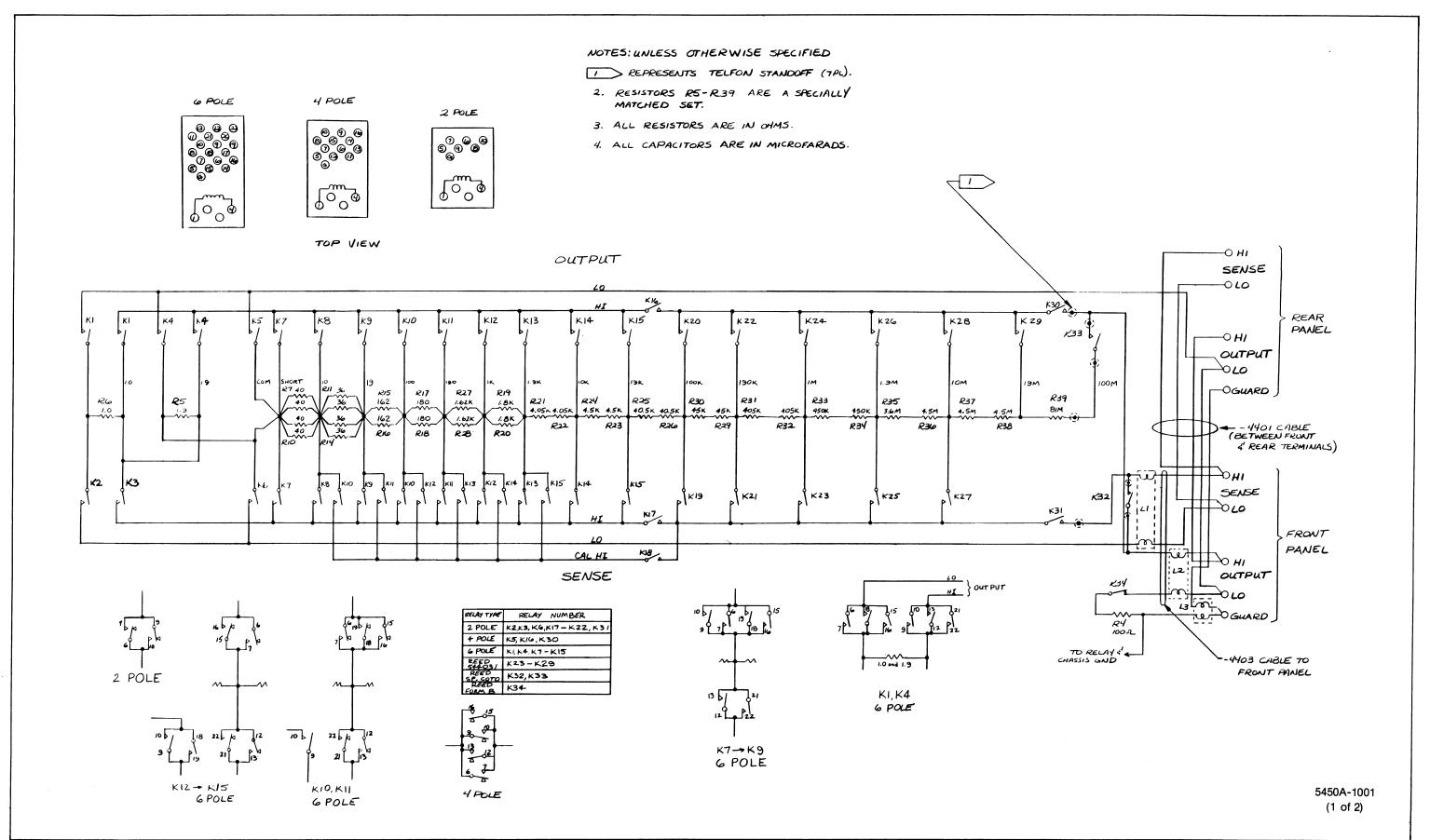


Figure 9-1. A1 Analog PCA (cont)

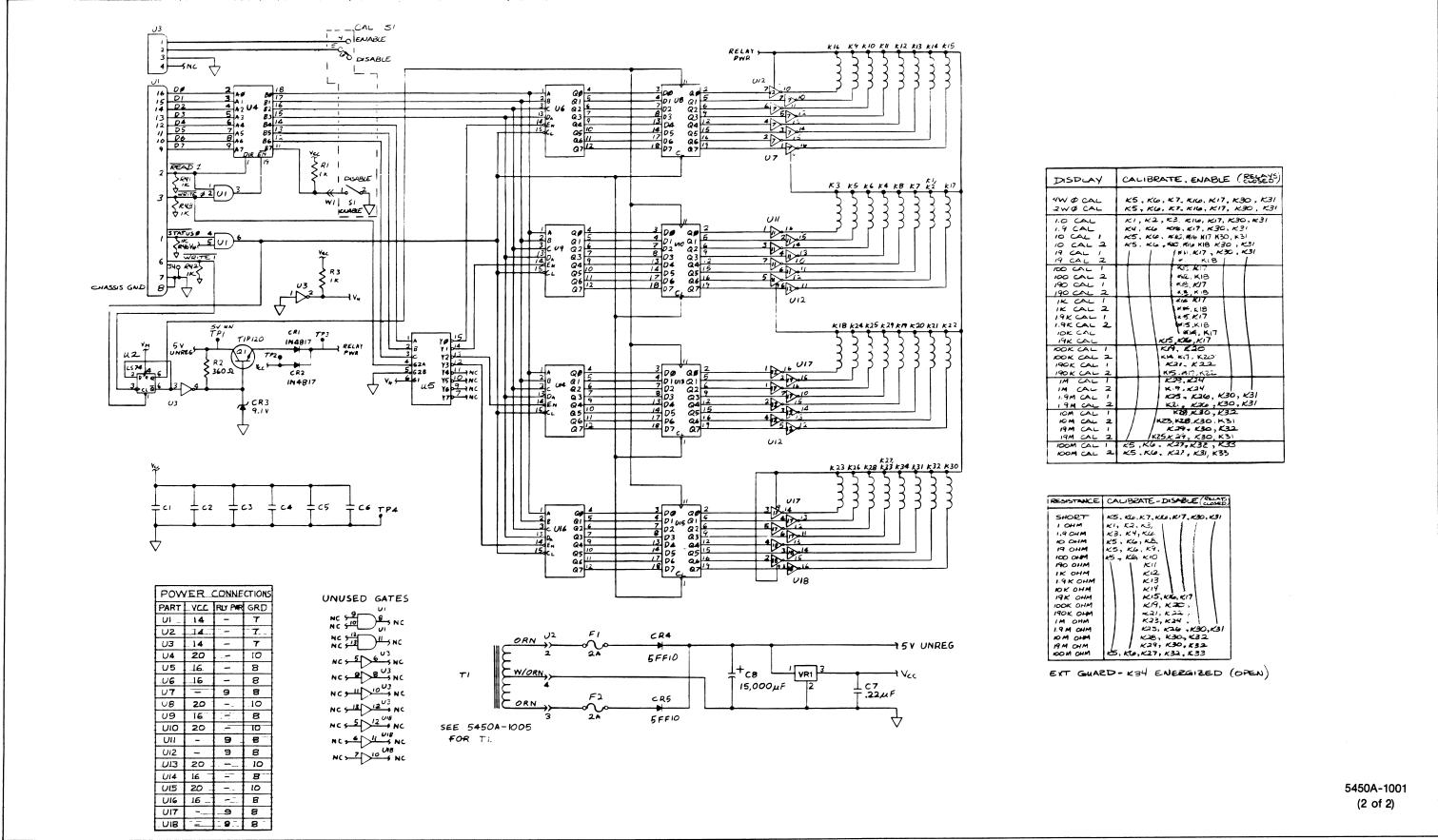


Figure 9-1. A1 Analog PCA (cont)

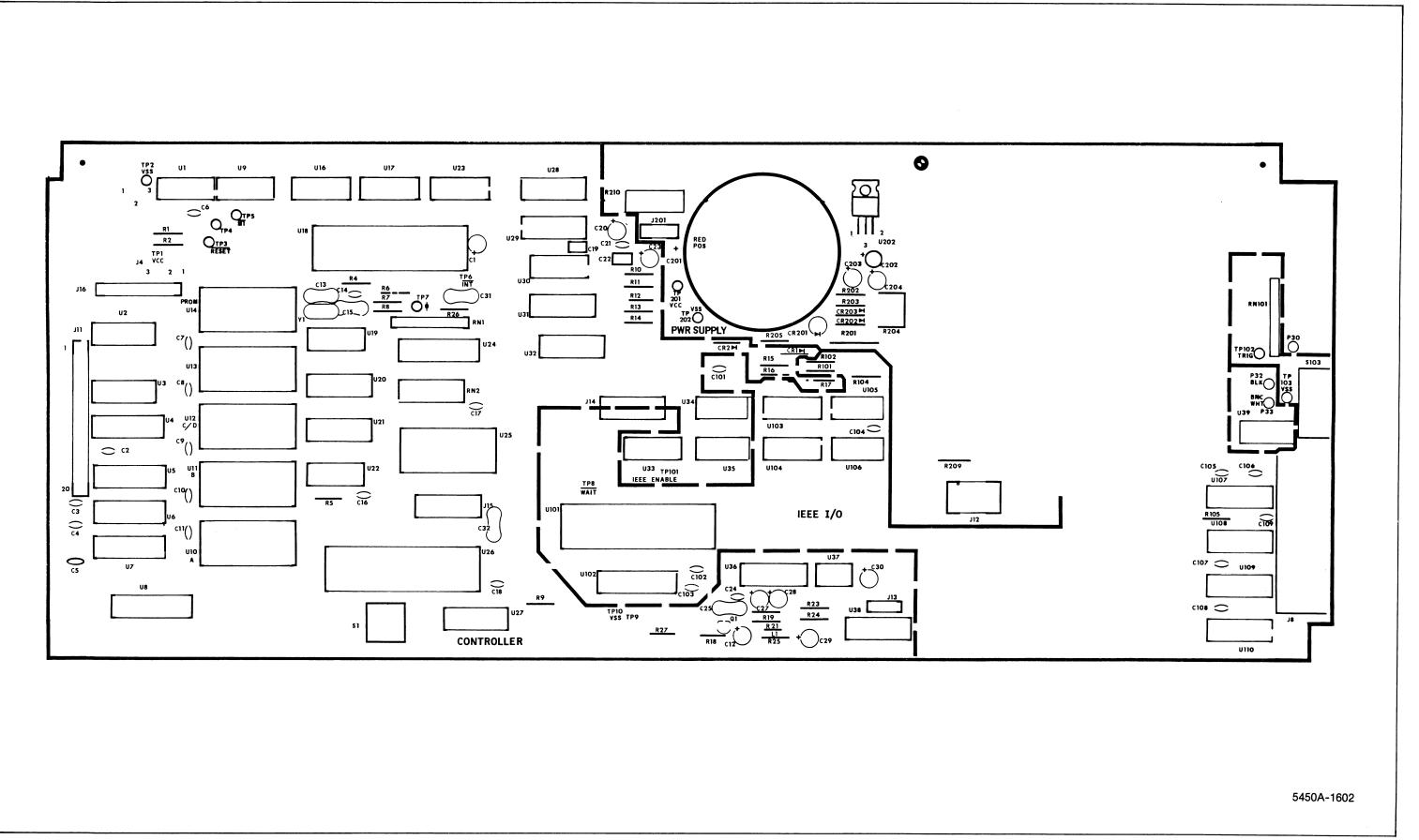


Figure 9-2. A2 Digital PCA

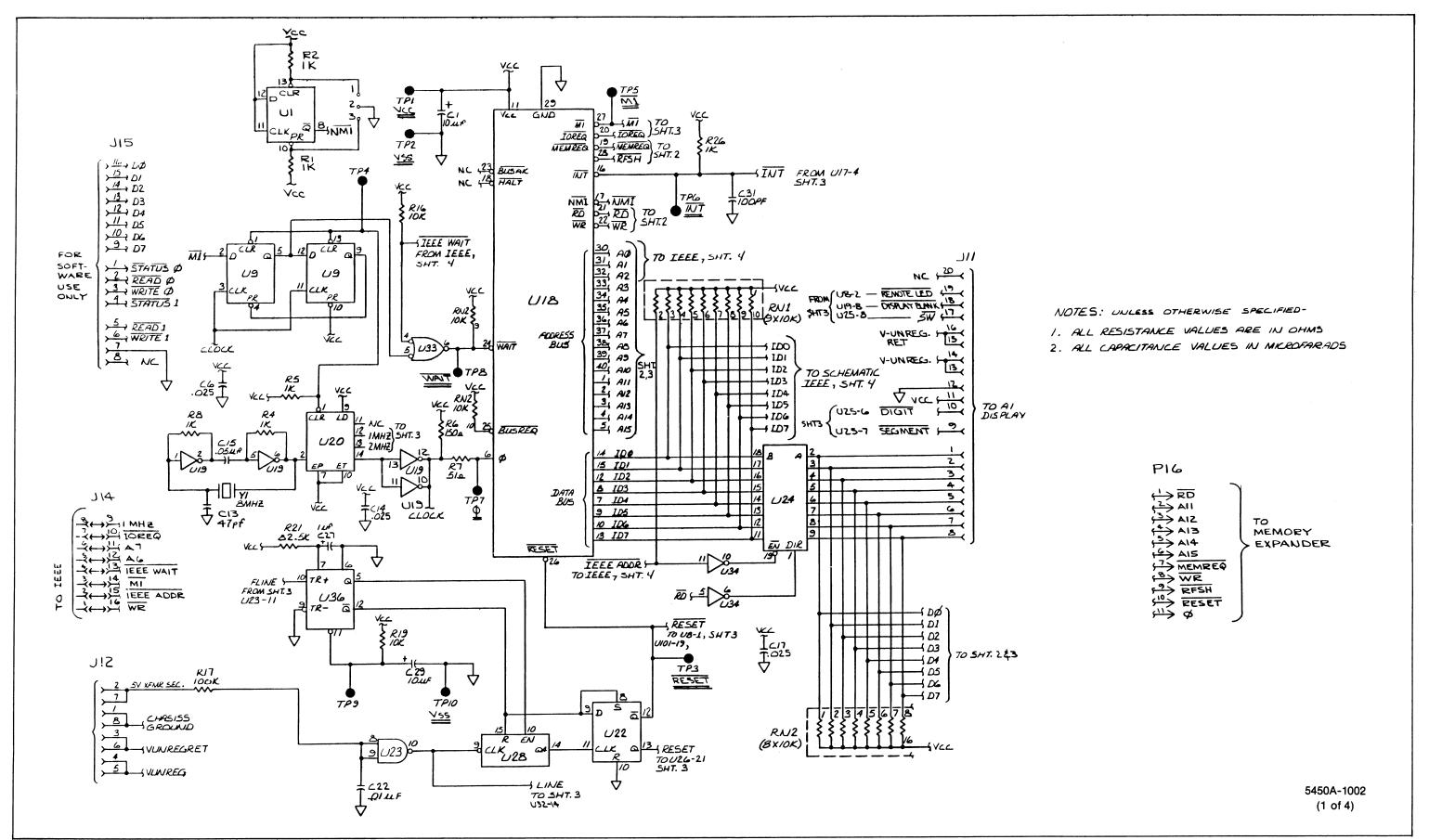


Figure 9-2. A2 Digital PCA (cont)

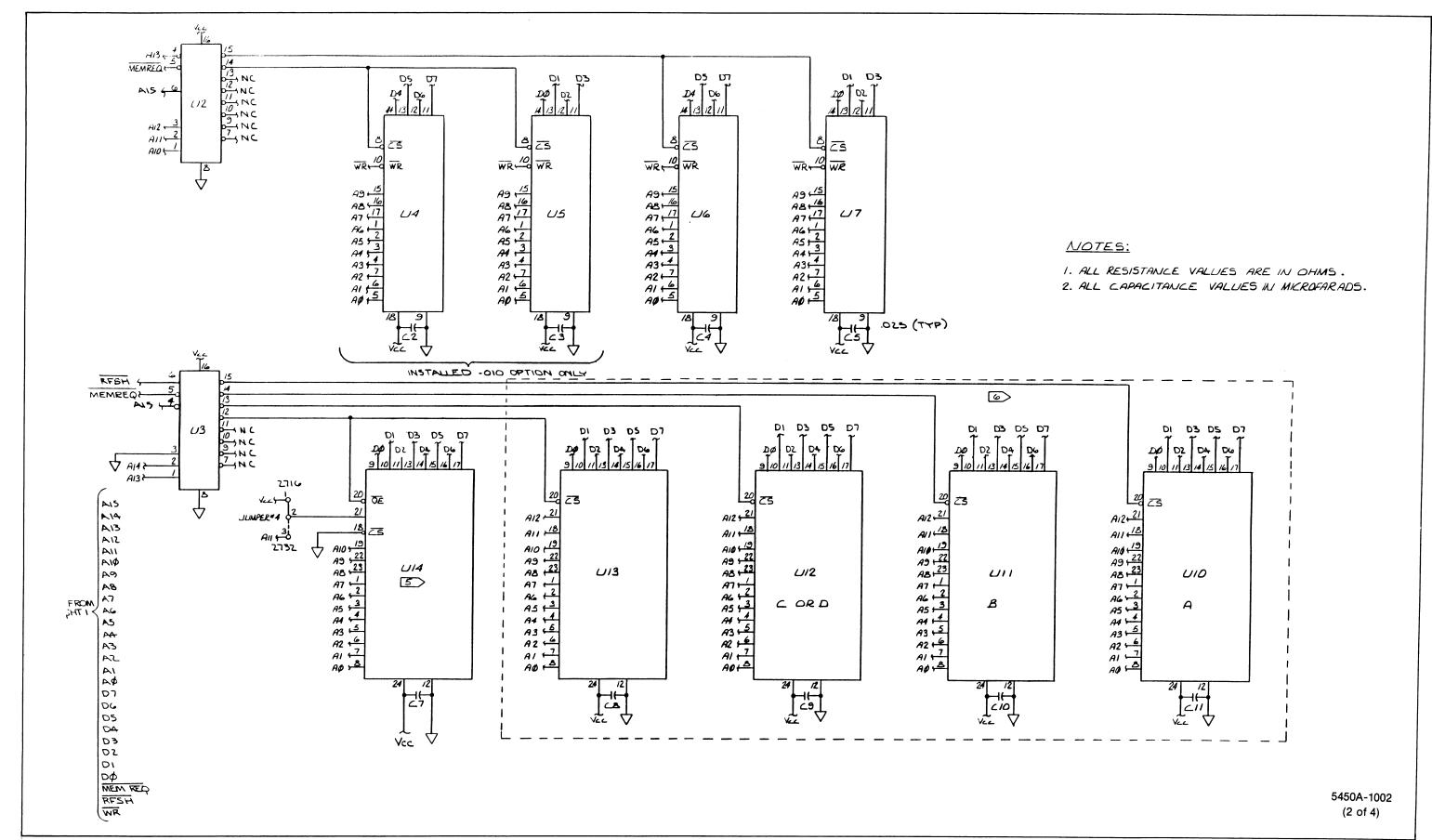


Figure 9-2. A2 Digital PCA (cont)

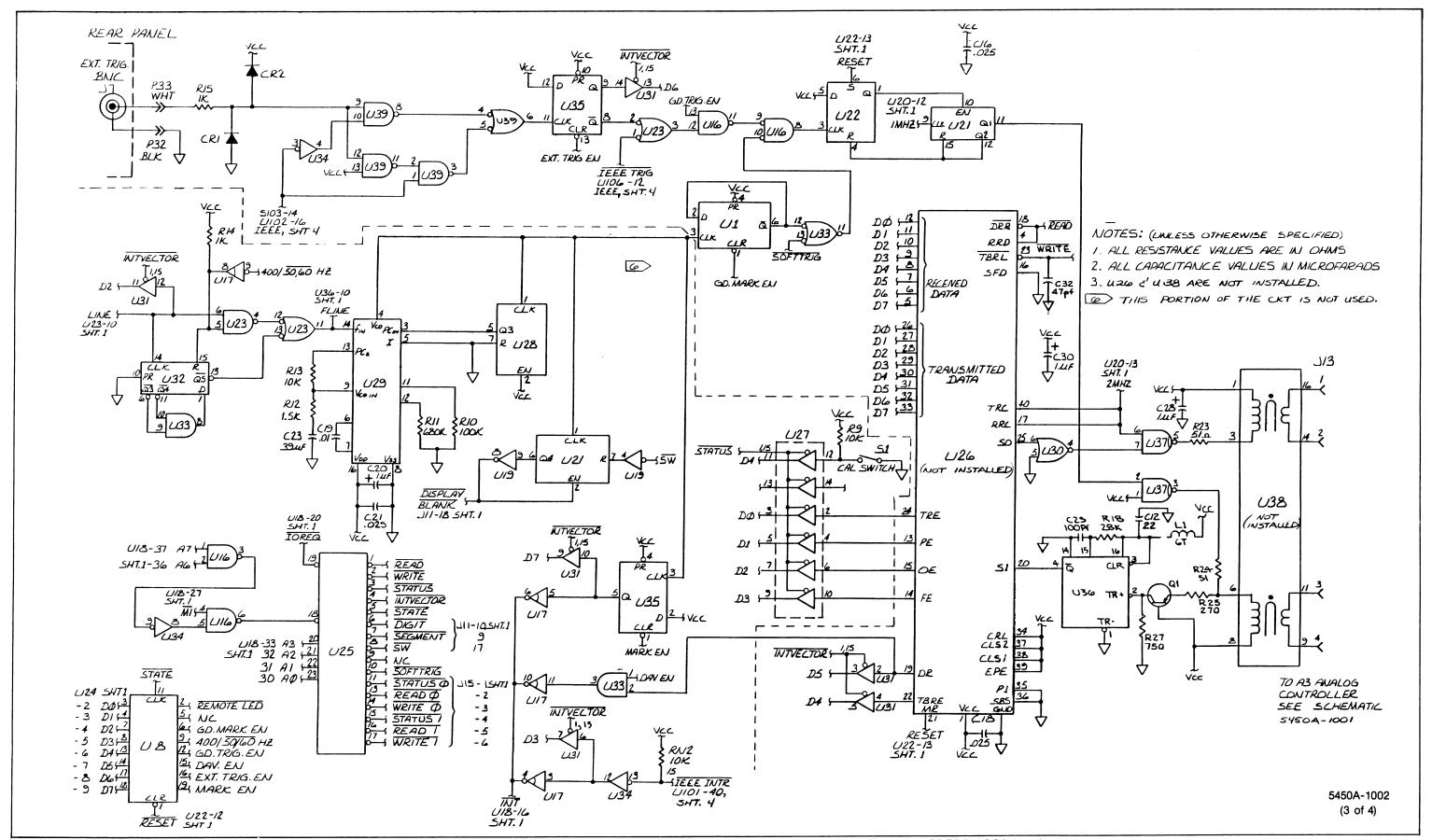


Figure 9-2. A2 Digital PCA (cont)

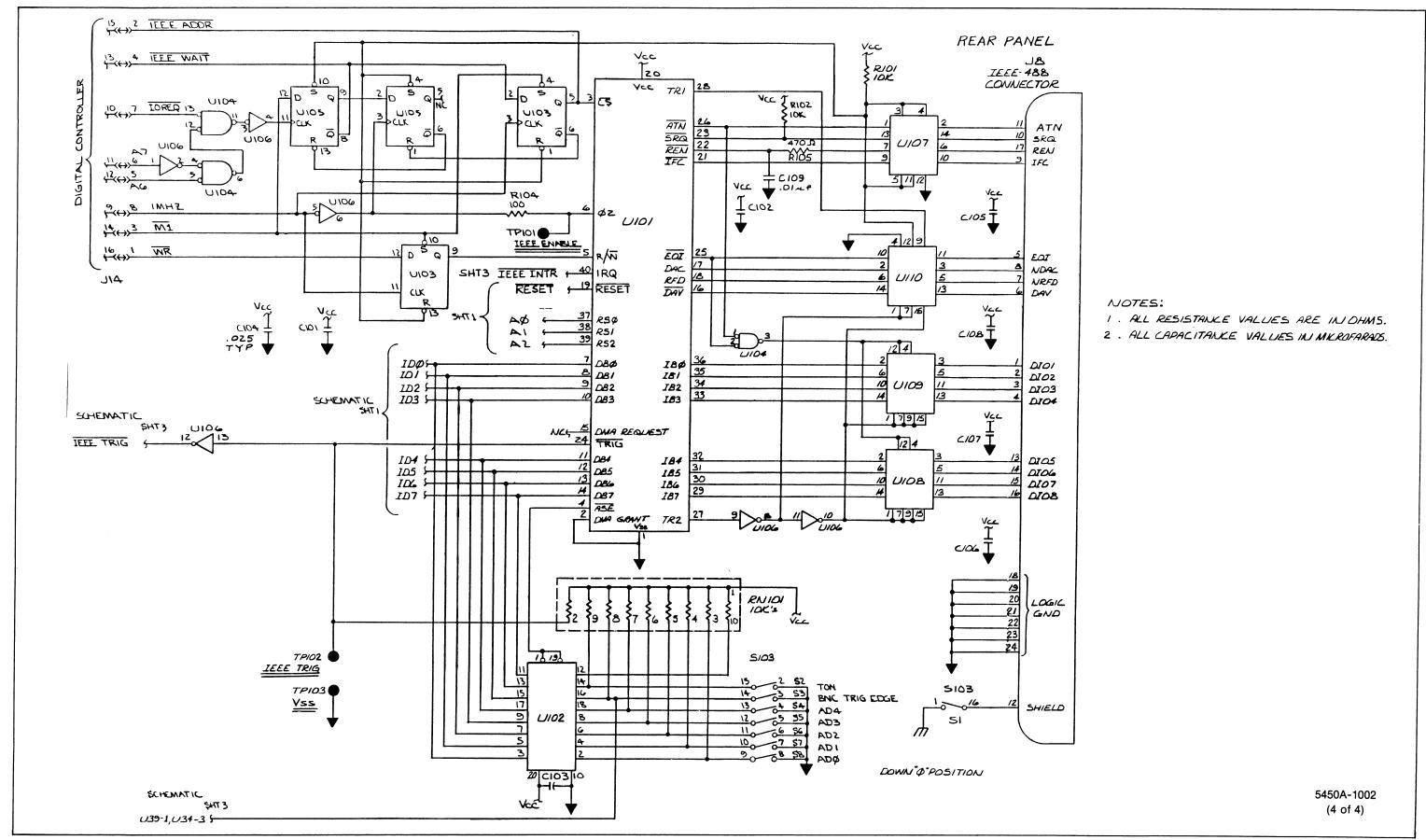
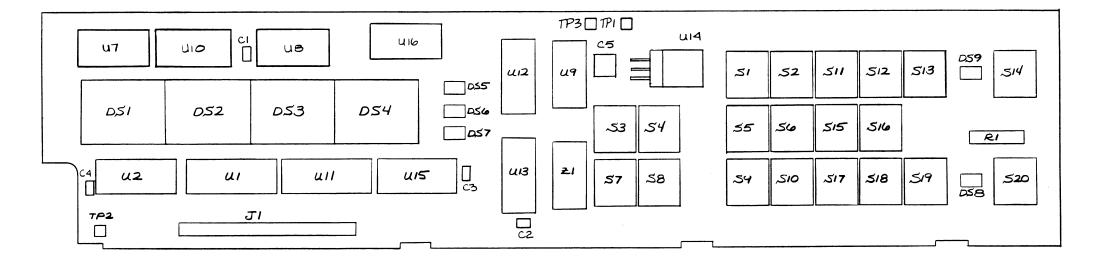


Figure 9-2. A2 Digital PCA (cont)



5450A-1603

Figure 9-3. A3 Display PCA

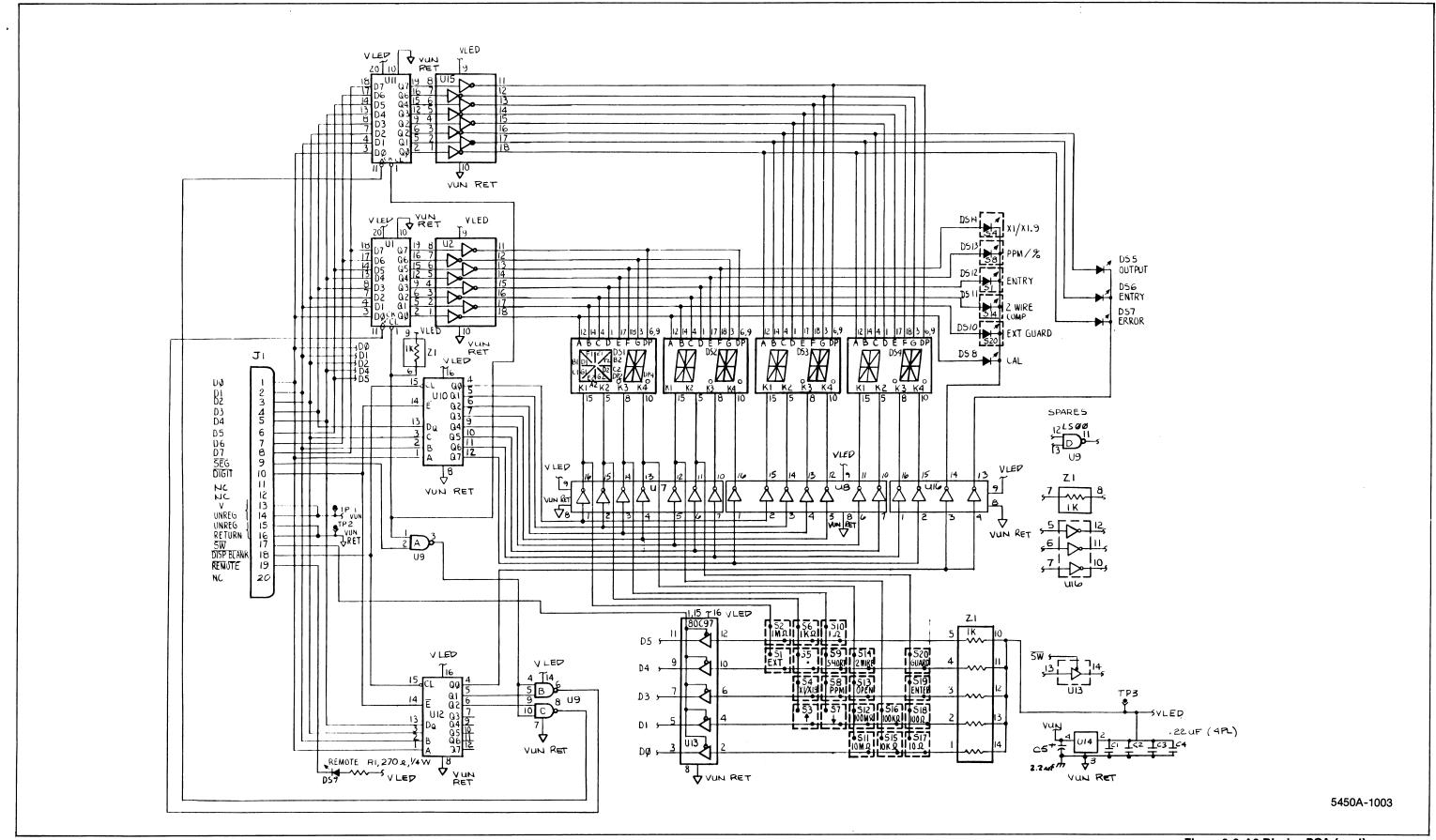


Figure 9-3. A3 Display PCA (cont)

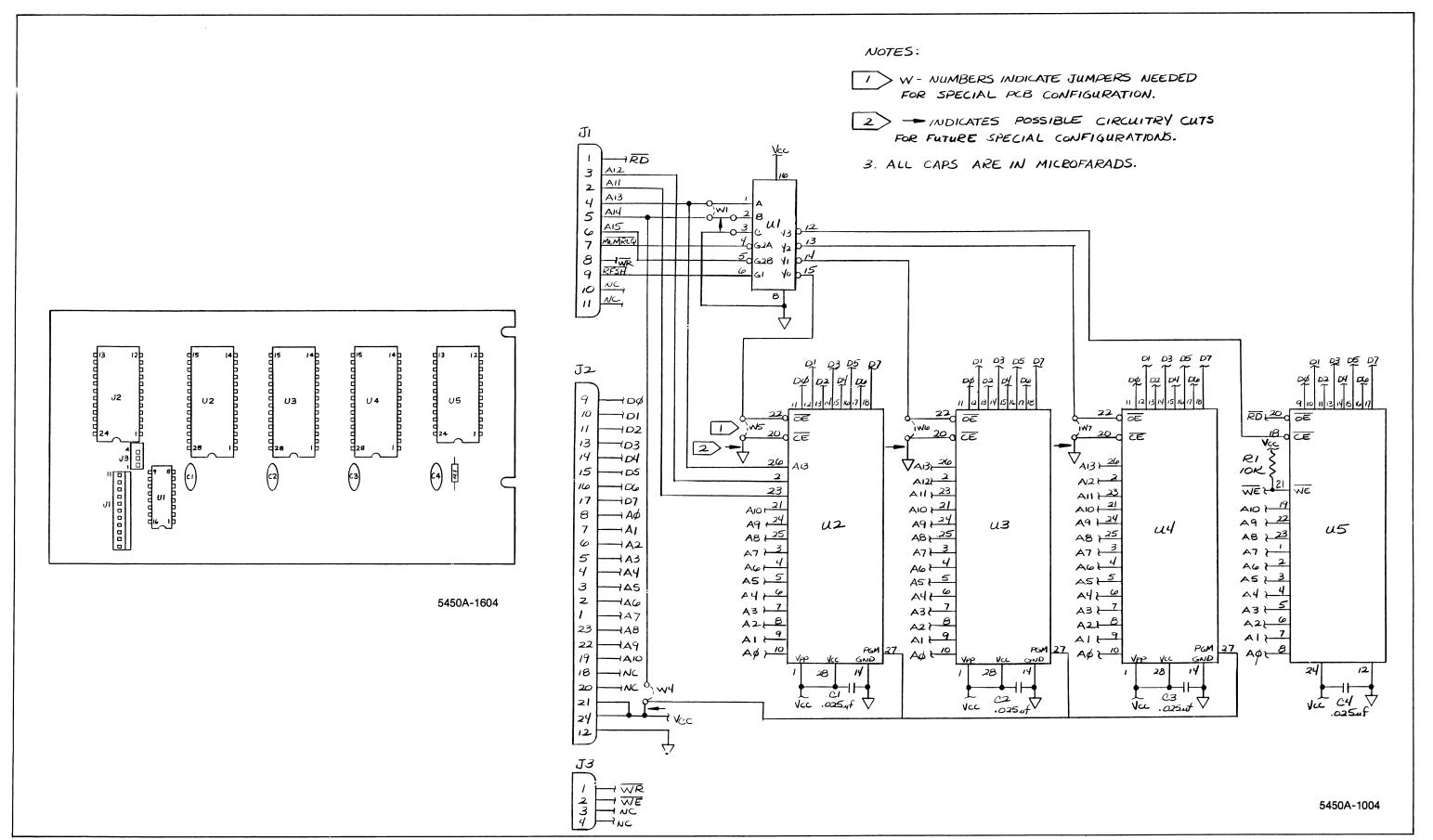


Figure 9-4. A4 Memory PCA

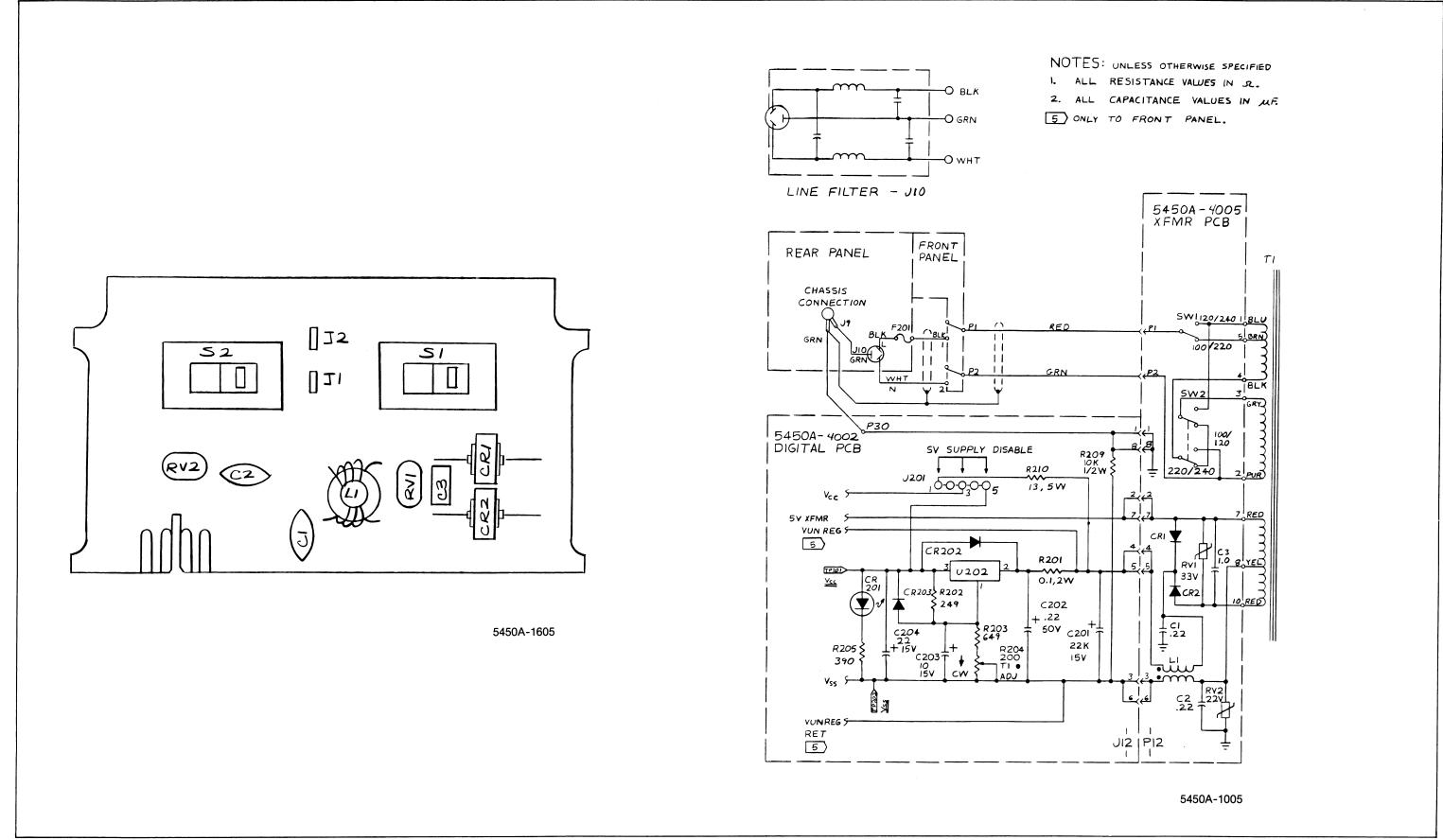


Figure 9-5. A5 Power Supply PCA